

Service

Service

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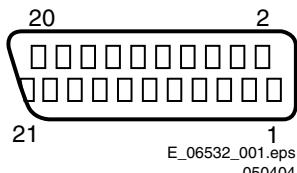
Service Manual

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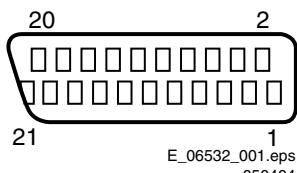
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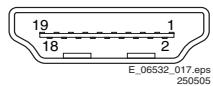


EXT1: Video RGB - In, CVBS - In/Out, Audio - In/Out**Figure 1-3 SCART connector**

1	- Audio R	0.5 V _{RMS} / 1 kohm	⊕
2	- Audio R	0.5 V _{RMS} / 10 kohm	⊕
3	- Audio L	0.5 V _{RMS} / 1 kohm	⊕
4	- Ground Audio	Gnd	⊥
5	- Ground Blue	Gnd	⊥
6	- Audio L	0.5 V _{RMS} / 10 kohm	⊕
7	- Video Blue/U	0.7 V _{PP} / 75 ohm	⊕
8	- Function Select	0 - 2 V: INT 4.5 - 7 V: EXT 16:9 9.5 - 12 V: EXT 4:3	⊕
9	- Ground Green	Gnd	⊥
10	- n.c.		
11	- Video Green/Y	0.7 V _{PP} / 75 ohm	⊕
12	- n.c.		
13	- Ground Red	Gnd	⊥
14	- Ground FBL	Gnd	⊥
15	- Video Red/V	0.7 V _{PP} / 75 ohm	⊕
16	- Status/FBL	0 - 0.4 V: INT 1 - 3 V: EXT / 75 ohm	⊕
17	- Ground Video	Gnd	⊥
18	- Ground Video	Gnd	⊥
19	- Video CVBS	1 V _{PP} / 75 ohm	⊕
20	- Video CVBS	1 V _{PP} / 75 ohm	⊕
21	- Shield	Gnd	⊥

EXT2: Video YC - In, CVBS - In/Out, Audio - In/Out**Figure 1-4 SCART connector**

1	- Audio R	0.5 V _{RMS} / 1 kohm	⊕
2	- Audio R	0.5 V _{RMS} / 10 kohm	⊕
3	- Audio L	0.5 V _{RMS} / 1 kohm	⊕
4	- Ground Audio	Gnd	⊥
5	- Ground Blue	Gnd	⊥
6	- Audio L	0.5 V _{RMS} / 10 kohm	⊕
7	- C-FRONT	0.7 V _{PP} / 75 ohm	⊕
8	- Function Select	0 - 2 V: INT 4.5 - 7 V: EXT 16:9 9.5 - 12 V: EXT 4:3	⊕
9	- Ground Green	Gnd	⊥
10	- Easylink P50	0 - 5 V / 4.7 kohm	⊕
11	- n.c.		
12	- n.c.		
13	- Ground Red	Gnd	⊥
14	- Ground Data	Gnd	⊥
15	- C	0.7 V _{PP} / 75 ohm	⊕
16	- n.c.		
17	- Ground Video	Gnd	⊥
18	- Ground FBL	Gnd	⊥
19	- Video CVBS	1 V _{PP} / 75 ohm	⊕
20	- Video CVBS/Y	1 V _{PP} / 75 ohm	⊕
21	- Shield	Gnd	⊥

HDMI: Digital Video, Digital Audio - In**Figure 1-5 HDMI (type A) connector**

1	- D2+	Data channel
2	- Shield	Gnd
3	- D2-	Data channel
4	- D1+	Data channel
5	- Shield	Gnd
6	- D1-	Data channel
7	- D0+	Data channel
8	- Shield	Gnd
9	- D0-	Data channel
10	- CLK+	Data channel
11	- Shield	Gnd
12	- CLK-	Data channel
13	- n.c.	
14	- n.c.	
15	- DDC_SCL	DDC clock
16	- DDC_SDA	DDC data
17	- Ground	Gnd
18	- +5V	
19	- HPD	Hot Plug Detect
20	- Ground	Gnd

Cinch: Video YPbPr - In

Gn - Video Y	1 V _{PP} / 75 ohm
Bu - Video Pb	0.7 V _{PP} / 75 ohm
Rd - Video Pr	0.7 V _{PP} / 75 ohm

Cinch: DVI Audio - In

Rd - Audio - R	0.5 V _{RMS} / 10 kohm
Wh - Audio - L	0.5 V _{RMS} / 10 kohm

Cinch: HD/SDI Audio - In

Rd - Audio - R	0.5 V _{RMS} / 10 kohm
Wh - Audio - L	0.5 V _{RMS} / 10 kohm

Cinch: Audio - Out

Rd - Audio - R	0.5 V _{RMS} / 10 kohm
Wh - Audio - L	0.5 V _{RMS} / 10 kohm

1.3 Chassis Overview



Figure 1-6 PWB location

2. Safety Instructions, Warnings, and Notes

Index of this chapter:

- 2.1 Safety Instructions
- 2.2 Maintenance Instructions
- 2.3 Warnings
- 2.4 Notes

2.1 Safety Instructions

Safety regulations require the following **during** a repair:

- Connect the set to the Mains/AC Power via an isolation transformer (> 800 VA).
- Replace safety components, indicated by the symbol **▲**, only by components identical to the original ones. Any other component substitution (other than original type) may increase risk of fire or electrical shock hazard.
- Wear safety goggles when you replace the CRT.

Safety regulations require that **after** a repair, the set must be returned in its original condition. Pay in particular attention to the following points:

- General repair instruction: as a strict precaution, we advise you to re-solder the solder connections through which the horizontal deflection current flows. In particular this is valid for the:
 1. Pins of the line output transformer (LOT).
 2. Fly-back capacitor(s).
 3. S-correction capacitor(s).
 4. Line output transistor.
 5. Pins of the connector with wires to the deflection coil.
 6. Other components through which the deflection current flows.

Note: This re-soldering is advised to prevent bad connections due to metal fatigue in solder connections, and is therefore only necessary for television sets more than two years old.

- Route the wire trees and EHT cable correctly and secure them with the mounted cable clamps.
- Check the insulation of the Mains/AC Power lead for external damage.
- Check the strain relief of the Mains/AC Power cord for proper function, to prevent the cord from touching the CRT, hot components, or heat sinks.
- Check the electrical DC resistance between the Mains/AC Power plug and the secondary side (only for sets that have a Mains/AC Power isolated power supply):
 1. Unplug the Mains/AC Power cord and connect a wire between the two pins of the Mains/AC Power plug.
 2. Set the Mains/AC Power switch to the "on" position (keep the Mains/AC Power cord unplugged!).
 3. Measure the resistance value between the pins of the Mains/AC Power plug and the metal shielding of the tuner or the aerial connection on the set. The reading should be between 4.5 Mohm and 12 Mohm.
 4. Switch "off" the set, and remove the wire between the two pins of the Mains/AC Power plug.
- Check the cabinet for defects, to prevent touching of any inner parts by the customer.

2.2 Maintenance Instructions

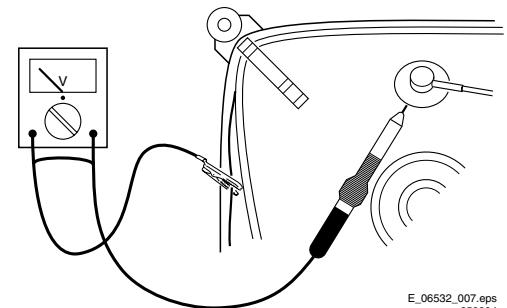
We recommend a maintenance inspection carried out by qualified service personnel. The interval depends on the usage conditions:

- When a customer uses the set under normal circumstances, for example in a living room, the recommended interval is three to five years.
- When a customer uses the set in an environment with higher dust, grease, or moisture levels, for example in a kitchen, the recommended interval is one year.
- The maintenance inspection includes the following actions:

1. Perform the "general repair instruction" noted above.
2. Clean the power supply and deflection circuitry on the chassis.
3. Clean the picture tube panel and the neck of the picture tube.

2.3 Warnings

- In order to prevent damage to ICs and transistors, avoid all high voltage flashovers. In order to prevent damage to the picture tube, use the method shown in figure "Discharge picture tube", to discharge the picture tube. Use a high voltage probe and a multi-meter (position V_{DC}). Discharge until the meter reading is 0 V (after approx. 30 s).



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Figure 2-1 Discharge picture tube

- All ICs and many other semiconductors are susceptible to electrostatic discharges (ESD **▲**). Careless handling during repair can reduce life drastically. Make sure that, during repair, you are connected with the same potential as the mass of the set by a wristband with resistance. Keep components and tools also at this same potential. Available ESD protection equipment:
 - Complete kit ESD3 (small tablemat, wristband, connection box, extension cable and earth cable) 4822 310 10671.
 - Wristband tester 4822 344 13999.
- Be careful during measurements in the high voltage section.
- Never replace modules or other components while the unit is switched "on".
- When you align the set, use plastic rather than metal tools. This will prevent any short circuits and prevents circuits from becoming unstable.

2.4 Notes

2.4.1 General

- Measure the voltages and waveforms with regard to the chassis (= tuner) ground (\perp), or hot ground (\oplus), depending on the tested area of circuitry. The voltages and waveforms shown in the diagrams are indicative. Measure them in the Service Default Mode (see chapter 5) with a colour bar signal and stereo sound (L: 3 kHz, R: 1 kHz unless stated otherwise) and picture carrier at 475.25 MHz for PAL, or 61.25 MHz for NTSC (channel 3).
- Where necessary, measure the waveforms and voltages with (Γ) and without (\times) aerial signal. Measure the voltages in the power supply section both in normal operation (①) and in stand-by (②). These values are indicated by means of the appropriate symbols.
- The semiconductors indicated in the circuit diagram and in the parts lists, are interchangeable per position with the

semiconductors in the unit, irrespective of the type indication on these semiconductors.

- Manufactured under license from Dolby Laboratories. "Dolby", "Pro Logic" and the "double-D symbol", are trademarks of Dolby Laboratories.

2.4.2 Schematic Notes

- All resistor values are in ohms, and the value multiplier is often used to indicate the decimal point location (e.g. 2K2 indicates 2.2 kohm).
- Resistor values with no multiplier may be indicated with either an "E" or an "R" (e.g. 220E or 220R indicates 220 ohm).
- All capacitor values are given in micro-farads ($\mu = x10^{-6}$), nano-farads ($n = x10^{-9}$), or pico-farads ($p = x10^{-12}$).
- Capacitor values may also use the value multiplier as the decimal point indication (e.g. 2p2 indicates 2.2 pF).
- An "asterisk" (*) indicates component usage varies. Refer to the diversity tables for the correct values.
- The correct component values are listed in the Spare Parts List. Therefore, always check this list when there is any doubt.

2.4.3 Rework on BGA (Ball Grid Array) ICs

General

Although (LF)BGA assembly yields are very high, there may still be a requirement for component rework. By rework, we mean the process of removing the component from the PWB and replacing it with a new component. If an (LF)BGA is removed from a PWB, the solder balls of the component are deformed drastically so the removed (LF)BGA has to be discarded.

Device Removal

As is the case with any component that is being removed, it is essential when removing an (LF)BGA, that the board, tracks, solder lands, or surrounding components are not damaged. To remove an (LF)BGA, the board must be uniformly heated to a temperature close to the reflow soldering temperature. A uniform temperature reduces the risk of warping the PWB. To do this, we recommend that the board is heated until it is certain that all the joints are molten. Then carefully pull the component off the board with a vacuum nozzle. For the appropriate temperature profiles, see the IC data sheet.

Area Preparation

When the component has been removed, the vacant IC area must be cleaned before replacing the (LF)BGA.

Removing an IC often leaves varying amounts of solder on the mounting lands. This excessive solder can be removed with either a solder sucker or solder wick. The remaining flux can be removed with a brush and cleaning agent.

After the board is properly cleaned and inspected, apply flux on the solder lands and on the connection balls of the (LF)BGA.

Note: Do not apply solder paste, as this has been shown to result in problems during re-soldering.

Device Replacement

The last step in the repair process is to solder the new component on the board. Ideally, the (LF)BGA should be aligned under a microscope or magnifying glass. If this is not possible, try to align the (LF)BGA with any board markers. So as not to damage neighbouring components, it may be necessary to reduce some temperatures and times.

More Information

For more information on how to handle BGA devices, visit this URL: www.atyourservice.ce.philips.com (needs subscription, not available for all regions). After login, select "Magazine", then go to "Repair downloads". Here you will find information on how to deal with BGA-ICs.

2.4.4 Lead-free Solder

Philips CE is producing lead-free sets (PBF) from 1.1.2005 onwards.

Identification: The bottom line of a type plate gives a 14-digit serial number. Digits 5 and 6 refer to the production year, digits 7 and 8 refer to production week (in example below it is 1991 week 18).



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Figure 2-2 Serial number example

Regardless of the special lead-free logo (which is not always indicated), one must treat all sets from this date onwards according to the rules as described below.



Figure 2-3 Lead-free logo

Due to lead-free technology some rules have to be respected by the workshop during a repair:

- Use only lead-free soldering tin Philips SAC305 with order code 0622 149 00106. If lead-free solder paste is required, please contact the manufacturer of your soldering equipment. In general, use of solder paste within workshops should be avoided because paste is not easy to store and to handle.
- Use only adequate solder tools applicable for lead-free soldering tin. The solder tool must be able:
 - To reach a solder-tip temperature of at least 400°C.
 - To stabilise the adjusted temperature at the solder-tip.
 - To exchange solder-tips for different applications.
- Adjust your solder tool so that a temperature of around 360°C - 380°C is reached and stabilised at the solder joint. Heating time of the solder-joint should not exceed ~ 4 sec. Avoid temperatures above 400°C, otherwise wear-out of tips will increase drastically and flux-fluid will be destroyed. To avoid wear-out of tips, switch "off" unused equipment or reduce heat.
- Mix of lead-free soldering tin/parts with leaded soldering tin/parts is possible but PHILIPS recommends strongly **to avoid** mixed regimes. If this cannot be avoided, carefully clean the solder-joint from old tin and re-solder with new tin.
- Use only original spare-parts listed in the Service-Manuals. Not listed standard material (commodities) has to be purchased at external companies.
- Special information for lead-free BGA ICs: these ICs will be delivered in so-called "dry-packaging" to protect the IC against moisture. This packaging may only be opened shortly before it is used (soldered). Otherwise the body of the IC gets "wet" inside and during the heating time the structure of the IC will be destroyed due to high (steam-) pressure inside the body. If the packaging was opened before usage, the IC has to be heated up for some hours (around 90°C) for drying (think of ESD-protection!).

Do not re-use BGAs at all!

- For sets produced before 1.1.2005, containing leaded soldering tin and components, all needed spare parts will be available till the end of the service period. For the repair of such sets nothing changes.

In case of doubt whether the board is lead-free or not (or with mixed technologies), you can use the following method:

- Always use the highest temperature to solder, when using SAC305 (see also instructions below).
- De-solder thoroughly (clean solder joints to avoid mix of two alloys).

Caution: For BGA-ICs, you **must** use the correct temperature-profile, which is coupled to the 12NC. For an overview of these profiles, visit the website www.atyourservice.ce.philips.com (needs subscription, but is not available for all regions)

You will find this and more technical information within the "Magazine", chapter "Repair downloads".

For additional questions please contact your local repair help desk.

2.4.5 Alternative BOM identification

In September 2003, Philips CE introduced a change in the way the serial number (or production number, see Figure 2-2) is composed. From this date on, the **third digit** in the serial number (example: AG2B033500001) indicates the number of the alternative BOM (Bill of Materials used for producing the specific model of TV set). It is possible that the same TV model on the market is produced with e.g. two different types of displays, coming from two different O.E.M.s.

By looking at the third digit of the serial number, the service technician can see if there is more than one type of B.O.M. used in the production of the TV set he is working with. He can then consult the At Your Service Web site, where he can type in the Commercial Type Version Number of the TV set (e.g. 29PT9521/12), after which a screen will appear that gives information about the number of alternative B.O.M.s used. If the third digit of the serial number contains the number 1 (example: AG1B033500001), then there is only one B.O.M. version of the TV set on the market. If the third digit is a 2 (example: AG2B033500001), then there are two different B.O.M.s. **Information about this is important for ordering the correct spare parts!**

For the third digit, the numbers 1...9 and the characters A...Z can be used, so in total: 9 plus 26 = 35 different B.O.M.s can be indicated by the third digit of the serial number.

2.4.6 Practical Service Precautions

- **It makes sense to avoid exposure to electrical shock.** While some sources are expected to have a possible dangerous impact, others of quite high potential are of limited current and are sometimes held in less regard.
- **Always respect voltages.** While some may not be dangerous in themselves, they can cause unexpected reactions that are best avoided. Before reaching into a powered TV set, it is best to test the high voltage insulation. It is easy to do, and is a good service precaution.

3. Directions for Use

You can download this information from the following websites:

<http://www.philips.com/support>

<http://www.p4c.philips.com>

4. Mechanical Instructions

Index of this chapter:

- 4.1 Service Connector (for IAP)
- 4.2 Set Disassembly
- 4.3 Service Positions
- 4.4 Assy / Board Removal
- 4.5 Set Re-assembly

Note: Figures below can deviate slightly from the actual situation, due to the different set executions.

4.1 Service Connector (for IAP)

For software uploading with the IAP tool (In Application Programming), it is not necessary to remove EEPROMs from the set. You only have to connect the IAP interface circuit to the service connector (on the rear of the set, and start the software uploading (see also chapter 5 "Service Modes, Error Codes, and Fault Finding").

4.2 Set Disassembly

Follow the disassemble instructions in described order.

4.2.1 Rear Cover Removal

Warning: disconnect the mains power cord before you remove the rear cover.

1. Remove all the fixation screws of the rear cover.
2. Now the rear cover can be removed.

4.3 Service Positions

Only the LSP of this chassis has a service position for better access to the component side of the LSP. For the SSB, there is no specific service position.

4.3.1 Large Signal Panel (LSP)

Component Side LSP

For better accessibility of the LSP, do the following (see Figures "Service position LSP" and "Locking handles LSP"):

1. Simultaneously do the following: a) pull the two plastic locking handles at the mid left and mid right side of the bracket gently backwards to unlock the bracket, and b) loosen the bracket from the bottom tray, by pulling it backwards. N.B.: You do not need to pull the other two locking handles backwards.
2. Remove the LSP-bracket from the bottom tray by lifting it upwards.
3. Hook the bracket in the first row of fixation holes of the bottom tray. In other words, reposition the bracket from [1] to [2].

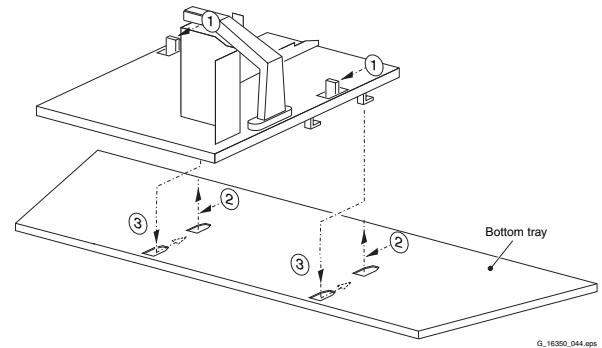


Figure 4-1 Service position LSP

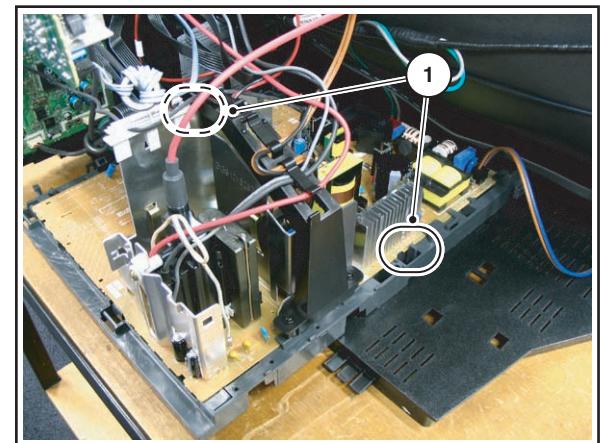


Figure 4-2 Locking handles LSP

Solder Side LSP

To get access to the bottom side (solder side) of the LSP, do the following:

1. Remove all the connectors from the LSP.
2. Remove the LSP, still in its plastic bracket, from the chassis, so the bottom side of the LSP can be reached. If necessary, remove the LSP also from its plastic bracket.

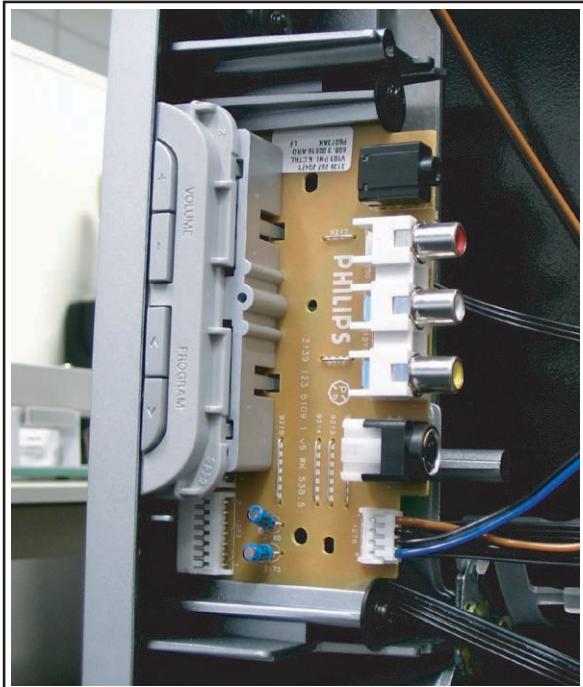
4.3.2 Small Signal Board (SSB)

There is no service position for the SSB. Most test points are located on the component side. If you have to replace ICs, you must remove the complete SSB module from the TV set (see further down in this chapter: Small Signal Board, SSB).

4.4 Assy / Board Removal

Sometimes, it can be necessary to swap a complete assy or Printed Wiring Board (PWB). How that can be done is explained below.

4.4.1 Top Control & Side I/O Assy/Panel

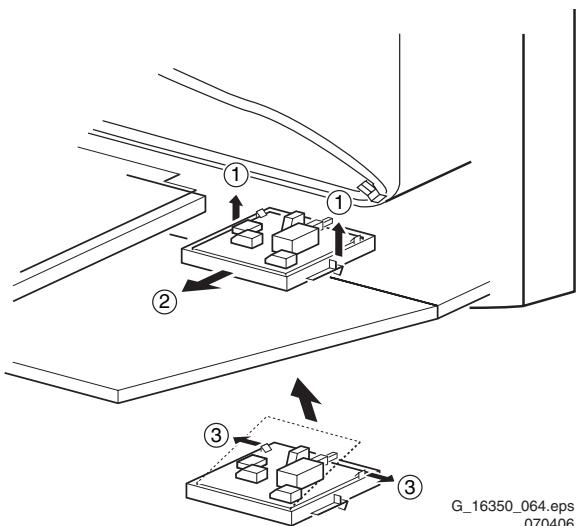


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Figure 4-3 Top control & Side I/O assy/panel

1. Remove the two fixation screws that hold the panel (see Figure "Top control & Side I/O assy/panel").
2. Pull the board backwards and remove it from the TV set.
3. Remove, if necessary, all the connectors from the board.

4.4.2 Mains Switch/LED Panel



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Figure 4-4 Mains Switch / LED panel

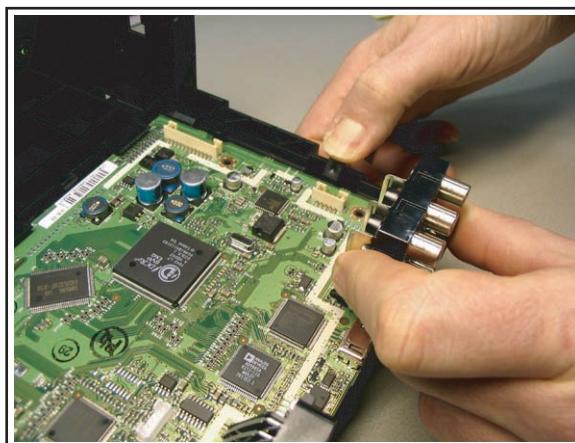
1. Release the two fixation clamps [1] by pushing them backwards and upwards (see Figures above).
2. Pull the complete assy backwards [2].
3. If the board has to be removed, release the two clamps at the sides of the bracket and lift the panel out [3].

4.4.3 Small Signal Board (SSB)



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Figure 4-5 SSB removal from chassis

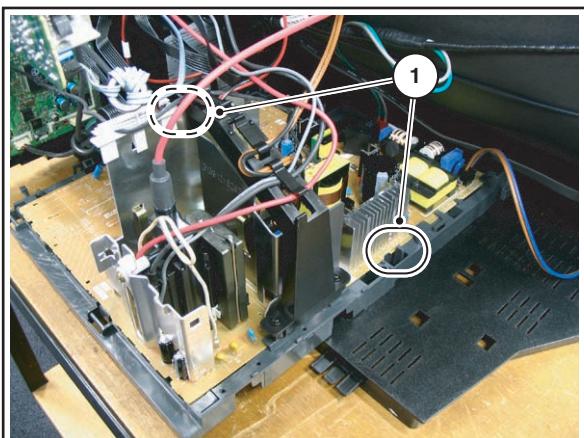


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Figure 4-6 SSB removal from bracket

1. Release the fixation clamp (see Figure "SSB removal from chassis") by pushing it backwards.
2. Take the complete SSB out.
3. If the board has to be removed, release the two clamps at the sides of the bracket and lift the panel out (see Figure "SSB removal from bracket").

4.4.4 Large Signal Panel (LSP)



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Figure 4-7 LSP locking handles

1. Simultaneously do the following:
 - a. Pull the two plastic locking handles at the mid left and mid right side of the bracket gently backwards to unlock the bracket (see Figure "LSP locking handles"), and
 - b. Loosen the bracket from the bottom tray, by pulling it backwards.
N.B.: You do not need to pull the other two locking handles backwards.
2. Remove the LSP-bracket from the bottom tray by lifting it upwards.
3. Remove all cables from the LSP.
4. Remove the board from the bracket by unhooking it from its fixation clamps.

4.5 Set Re-assembly

To re-assemble the whole set, do all processes in reverse order.

Be sure that, before the rear cover is mounted:

- The mains cord is positioned correctly in its guiding brackets (make sure that the strain relief will function correctly!).
- All wires/cables are returned in their original positions. This is very important, in view of the "hot" and "EHT" areas of the set.
- Check if **no wires are touching the heat sinks** that are on the LSP; this may damage the cables!

5. Service Modes, Error Codes, and Fault Finding

Index of this chapter:

- 5.1 Test Points
- 5.2 Service Modes
- 5.3 Problems and Solving Tips Related to CSM
- 5.4 Service Tools
- 5.5 Error Codes
- 5.6 The Blinking LED Procedure
- 5.7 Software Downloading
- 5.8 Fault Finding and Repair Tips

- Timer / Sleep timer.
- Child / parental lock.
- Blue mute.
- Hotel / hospital mode.
- Auto shut off (when no "IDENT" video signal is received for 15 minutes).
- Skipping of non-favourite presets / channels.
- Auto-storage of personal presets.
- Auto user menu time-out.
- Auto Volume Levelling (AVL).

5.1 Test Points

This chassis is equipped with test points in the service printing. In the schematics, test points are identified with a rectangle box around Fxxx or Ixxx. These test points are specifically mentioned in the service manual as "half moons" with a dot in the centre.

The chassis is equipped with test points (Fxxx) printed on the circuit board assemblies. As most signals are digital, it will be almost impossible to measure waveforms with a standard oscilloscope. Therefore, waveforms are not given in this manual.

Perform measurements under the following conditions:

- Television set in Service Default Alignment Mode.
- Video input: Colour bar signal.
- Audio input: 3 kHz left channel, 1 kHz right channel.

5.2 Service Modes

Service Default mode (SDM) and Service Alignment Mode (SAM) offer several features for the service technician, while the Customer Service Mode (CSM) and the Digital Customer Service Mode (DCSM, only for TVs with digital reception module) are used for communication between the call centre and the customer.

This chassis offers the option of using the IAP Tool (In Application Programming), a hardware interface between a computer and the TV chassis, for software uploading to the TV set. See also paragraph "Service Tools: IAP Tool".

5.2.1 Service Default Mode (SDM)

Purpose

- To create a predefined setting for measurements to be made.
- To override software protections.
- To start the "Blinking LED Procedure".
- To inspect the error buffer.
- To check the life timer.

Specifications

Table 5-1 SDM default settings

Region	Freq. (MHz)	Default system
Europe, AP-PAL/Multi	475.25	PAL B/G
NAFTA, AP-NTSC, LATAM	61.25 (ch. 3)	NTSC M

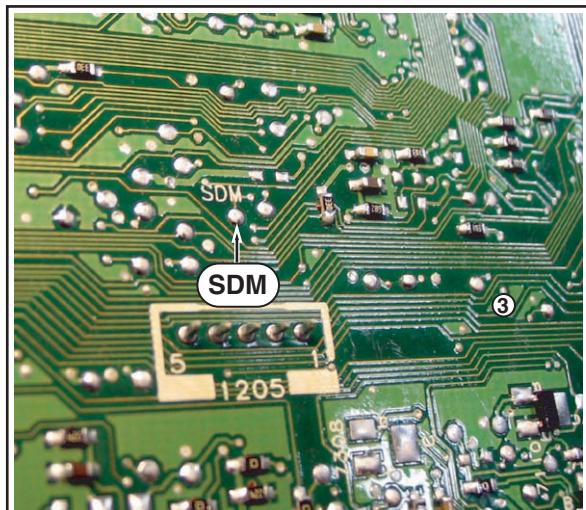
- All picture settings at 50% (brightness, colour contrast, hue).
- Bass, treble and balance at 50%; volume at 25%.
- All service-unfriendly modes (if present) are disabled. The service unfriendly modes are:

How to Enter

To enter SDM, use one of the following methods:

- Press the following key sequence on the remote control transmitter: "062596" directly followed by the MENU button (do not allow the OSD display to time out between entries while keying the sequence).
- Short the SDM contact to mass (see Figure "SDM Service contact") on the TV board and apply AC Power. Remove the short after start-up.

Caution: Entering SDM by shorting the "Service" contact to mass will override the software protections. Do this only for a short period. **When doing this, the service-technician must know exactly what he is doing, as it could damage the television set.**



G_16350_050.eps
060406

Figure 5-1 SDM Service contact (for SDM: short to mass)

After entering SDM, the following screen is visible, with SDM in the upper right corner of the screen to indicate that the television is in Service Default Mode.

```
00025 L06EF1 1.2
ERR 0 0 0 0 0
OP 136 008 006 000 000 002 016
```

m SDM

G_16350_051.eps
060406

Figure 5-2 SDM menu (example)

How to Navigate

When you press the MENU button on the remote control, the set will switch on the normal user menu in the SDM mode.

How to Exit

Switch the set to STANDBY by pressing the POWER button on the remote control transmitter.

If you turn the television set off by removing the mains (i.e., unplugging the television) or by using the POWER button on the TV set, the television set will remain in SDM when mains is re-applied, and the error buffer is not cleared.

5.2.2 Service Alignment Mode (SAM)**Purpose**

- To change option settings.
- To display / clear the error code buffer.
- To perform alignments.

Specifications

- Operation hours counter (maximum five digits displayed).
- Software version, Error codes, and Option settings display.
- Error buffer clearing.
- Option settings.
- Software alignments (Tuner, RGB Align, Geometry, and Audio).
- NVM Editor.
- IAP Mode switching (Compair mode not implemented).

How to Enter

Press the following key sequence on the remote control transmitter: "062596" directly followed by the OSD/STATUS/ INFO button (do not allow the OSD display to time out between entries while keying the sequence).

After entering SAM, the following screen is visible, with SAM in the upper right corner of the screen to indicate that the television is in Service Alignment Mode.

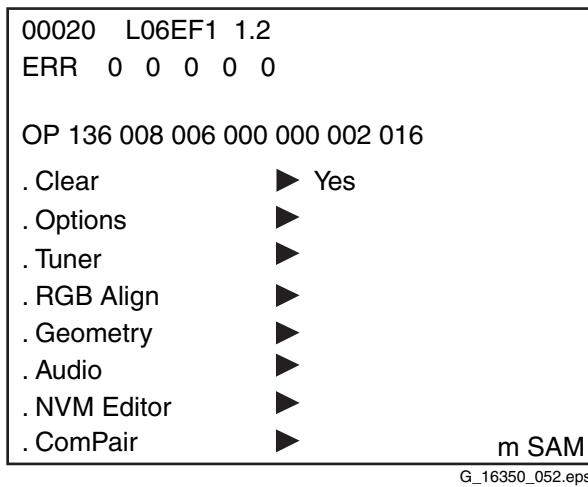


Figure 5-3 SAM menu (example)

Menu Explanation

1. **LLLL.** This represents the run timer. The run timer counts normal operation hours (including "on/off" switching), but does not count stand-by hours.
2. **AAA.BC-X.Y.** This is the software identification of the Main/Scaler microprocessor:
 - **AAA.B** = the chassis name.
 - **B** = the display indicator.
 - **C**= the region: E= Europe, A= Asia Pacific, U= NAFTA, L= LATAM, G= Global.

- **X**= the Main software version number (updated with a major change that is incompatible with previous versions).
- **Y**= the sub software version number (updated with a minor change that is compatible with previous versions).
- 3. **Error Buffer (ERR).** Shows all errors detected since the last time the buffer was erased. Five errors possible.
- 4. **Option Bytes (OP).** Shows all option settings. See "Options" in the Alignments section for a detailed description. Seven codes are available.
- 5. **See Note below** (about other menu items).
- 6. **SAM.** Indication of the Service Alignment Mode.

Note: The other menu items (**Clear, Options, Tuner, RGB Align, Geometry, Audio, NVM Editor, and Compair**) are explained at the end of this chapter, together with the menu structure. See: "SAM Menu structure".

How to Navigate

- In SAM, select menu items with the CURSOR UP/DOWN keys on the remote control transmitter. The selected item will be highlighted. When not all menu items fit on the screen, use the CURSOR UP/DOWN keys to display the next / previous menu items.
- With the CURSOR LEFT/RIGHT keys, it is possible to:
 - Activate the selected menu item.
 - Change the value of the selected menu item.
 - Activate the selected submenu.
- In SAM, when you press the MENU button twice, the set will switch to the normal user menus (with the SAM mode still active in the background). To return to the SAM menu press the MENU button again.
- When you press the MENU key in while in a submenu, you will return to the previous menu.

How to store SAM settings

To store the settings changed in SAM mode, leave the top level SAM menu by using the POWER button on the remote control transmitter or the television set.

How to exit

Switch the set to STANDBY by pressing the POWER button on the remote control transmitter or on the television set.

5.2.3 Customer Service Mode (CSM)**Purpose**

The Customer Service Mode shows error codes and information on the TV's operation settings. The call centre can instruct the customer (by telephone) to enter CSM in order to identify the status of the set. This helps the call centre to diagnose problems and failures in the TV set before making a service call.

The CSM is a read-only mode; therefore, modifications are not possible in this mode.

How to Enter

To enter CSM, press the following key sequence on the remote control transmitter: "123654" (do not allow the OSD display to time out between entries while keying the sequence).

Upon entering the Customer Service Mode, the following screen will appear:

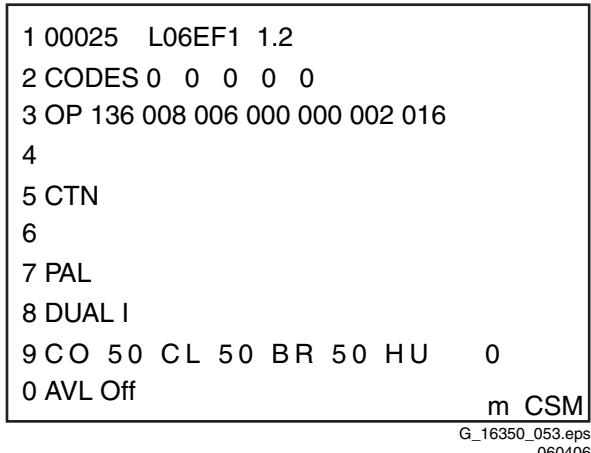


Figure 5-4 CSM menu (example)

Menu Explanation

1. Indication of the decimal value of the operation hours counter, Main/Scaler software version (see "Service Alignment Mode" for an explanation), and service mode (CSM= Customer Service Mode).
2. Displays the last five errors detected in the error code buffer.
3. Displays the option bytes.
4. Reserved.
5. Displays the type number version of the set (option).
6. Reserved.
7. Displays the detected Colour system (e.g. PAL/NTSC).
8. Displays the detected Audio (e.g. stereo/mono).
9. Displays the picture setting information.
10. Displays the sound setting information.

How to Exit

To exit CSM, use one of the following methods:

- Press the MENU, STATUS (or EXIT/INFO/[i+]), or POWER button on the remote control transmitter.
- Press the POWER button on the television set.

5.3 Problems and Solving Tips Related to CSM

5.3.1 Picture Problems

Note: The problems described below are all related to the TV settings. The procedures used to change the value (or status) of the different settings are described.

Picture Too Dark or Too Bright

If:

- The picture improves when you enter the Customer Service Mode,

Then:

1. Press the MENU button on the remote control transmitter. This brings up the normal user menu; the PICTURE sub menu is highlighted.
2. Press the CURSOR RIGHT key to enter the PICTURE sub menu.
3. Press the CURSOR UP/DOWN keys to increase or decrease the BRIGHTNESS value.
4. Press the MENU button on the remote control transmitter twice to exit the user menu.
5. The new PERSONAL preference values are automatically stored.

White Line(s) Around Picture Elements and Text

If:

There are white lines around picture elements and text,

Then:

1. Press the MENU button on the remote control transmitter. This brings up the normal user menu (PICTURE is highlighted).
2. Use the CURSOR DOWN key to select SHARPNESS.
3. Press the CURSOR RIGHT key to enter the SHARPNESS adjustment mode.
4. Press the CURSOR UP/DOWN keys to increase or decrease the SHARPNESS value.
5. Press the MENU button on the remote control transmitter twice to exit the user menu.
6. The new PERSONAL preference value is automatically stored.

Snowy Picture

Check the following:

- Antenna not connected. Connect the antenna.
- No antenna signal or bad antenna signal. Connect a proper antenna signal.
- The tuner is faulty (in this case line 2, the Error Buffer line, will contain error number 9). Check the tuner and replace/ repair the tuner if necessary.

Black and White Picture

If:

- The picture is (nearly) in black and white when it should be in colour,

Then:

1. Press the MENU button on the remote control transmitter. This brings up the normal user menu (PICTURE is highlighted).
2. Press the CURSOR RIGHT key to enter the PICTURE sub menu.
3. Use the CURSOR DOWN key to select COLOUR.
4. Press the CURSOR UP/DOWN keys to increase the COLOUR value.
5. Press the MENU button on the remote control transmitter twice to exit the user menu.
6. The new PERSONAL preference value is automatically stored.

5.4 Service Tools

5.4.1 IAP Tool: system requirements

PC

The PC used for IAP should meet the following criteria:

- Parallel Port;
- Windows XP Operating System;
- 60 MB free disk space.

5.4.2 IAP Tool: use

Introduction

The IAP Tool (In Application Programming) is a service tool for uploading software to a TV set (see Figure "IAP Interface"). In order to use IAP, the following items should be available:

- **PC**;
- **TV set**, to be put in the IAP mode (only when it is connected to the PC via the IAP interface);
- **IAP interface**, (parallel to I²C, for the connection between a PC and the TV set);
- **IAP Trident EXSDK software package**, software will be available via your national service organisation.

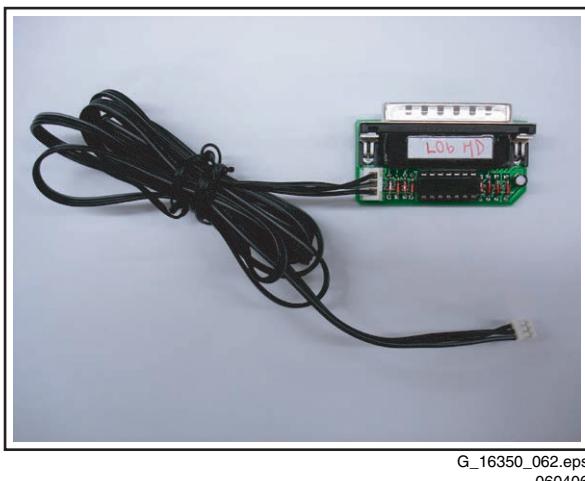


Figure 5-5 IAP interface

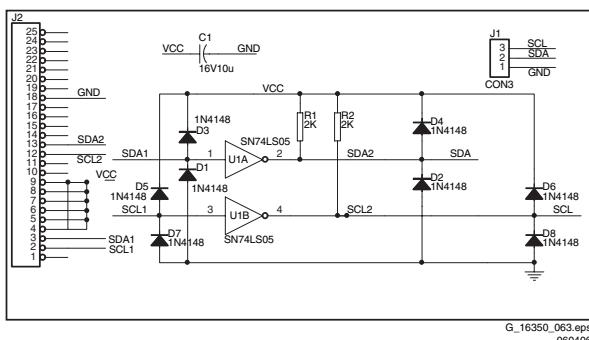


Figure 5-6 IAP interface circuit diagram

Installing the IAP software on a PC

When all the items mentioned above are present, install the software on a PC as follows:

- Extract the Trident EXSDK package (EXSDK.zip) into C:\Trident\Bin.
- The IAPWriter6.exe file is now inside the Bin folder.

Programming the Flash IC

The pre-requisite for flashing the software is that the Flash IC should have a bootloader (this means that there is already software on the TV set). To start the flashing, do as follows:

- Turn on the TV set, enter SAM mode > IAP. The TV is now in the IAP mode.
- Connect the PC to the TV via the Parallel-to-I²C card (IAP Interface).
- Double-click on C:\Trident\Bin\IAPWriter6.exe in order to launch the IAP writer. The main interface will appear.
- Check that these parameters are set correctly:

DEVICE	STM29W400DT
MPU Start Address	080000
MPU End Address	0FBFFF
Buffer Start Address	000000
Buffer End Address	07FFFF

Note: The device depends on the Flash IC used.

- Select File > Load, and select the .bin file to be programmed to the Flash IC.
- Select File Type "Binary".
- Select "Write Device".

Note: If there are no errors reported, the programming is successful.

Note: Sometimes, an error message may appear. Please try a few times if this happens.

How to Order

Note: If you encounter any problems, contact your local support desk.

5.5 Error Codes

5.5.1 Introduction

The error code buffer contains all detected errors since the last time the buffer was erased. The buffer is written from left to right, new errors are logged at the left side, and all other errors shift one position to the right.

When an error has occurred, the error is added to the list of errors, provided the list is not full or the error is a protection error.

When an error occurs and the error buffer is full, then the new error is not added, and the error buffer stays intact (history is maintained), except when the error is a protection error.

To prevent that an occasional error stays in the list forever, the error is removed from the list after 50+ operation hours.

When multiple errors occur (errors occurred within a short time span), there is a high probability that there is some relation between them.

5.5.2 How to Read the Error Buffer

Use one of the following methods:

- On screen via the SAM (only if you have a picture).
 - Examples:
 - 0 0 0 0**: No errors detected
 - 6 0 0 0**: Error code 6 is the last and only detected error
 - 9 6 0 0**: Error code 6 was first detected and error code 9 is the last detected error
- Via the blinking LED procedure (when you have no picture). See next paragraph.
- Via IAP.

5.5.3 How to Clear the Error Buffer

Use **one** of the following methods:

- By activation of the "CLEAR ERRORS" command in the SAM menu.
- With a normal RC, key in sequence "MUTE" followed by "**062599**" and "OK".
- If the content of the error buffer has not changed for 50+ hours, it resets automatically.

5.5.4 Error Codes

The function of error codes is to indicate failures in the TV set. In principle a unique error code is available for every:

- I²C device error.
- I²C bus error (for every bus containing two or more I²C devices).
- Protection error (e.g. +8V protection or Horizontal protection).
- Error not related to an I²C device, but of importance (e.g. BC-loop, RAM error).

Table 5-2 Error Table

Error	Description
0	0 = No error
2	High beam (BCI) protection
3	Vertical guard protection
4	POR bit / +8 V protection
7	Black current loop instability protection
8	General I ² C error Microprocessor (M30620FCNGP)
9	I ² C error while communicating with the PLL tuner (UV1316E)
10	I ² C error while communicating with the EEPROM (NVM at uP, M24C64)
11	I ² C error while communicating with the IF demodulator (TDA9886T/V4)
12	I ² C error while communicating with the Trident (SVPEX42)
13	I ² C error while communicating with the HOP (TDA9332H/N3)
14	I ² C error while communicating with the HDMI (SIL9011 CLU)
15	I ² C error while communicating with the Audio Demodulator (MSP3411G)
16	I ² C error while communicating with the ADC RGB (AD9985KST-110)
19	I ² C error while communicating with the SDRAM IC (K4D263238F)

Service Tips:

- In case of non-intermittent faults, clear the error buffer before you begin the repair. This to ensure that old error codes are no longer present. Before clearing the buffer, write down the content, as this history can give you significant information.
- If possible, check the entire contents of the error buffer. In some situations, an error code is only the result of another error code and not the actual cause (e.g., a fault in the protection detection circuitry can also lead to a protection).

5.6 The Blinking LED Procedure

5.6.1 Introduction

Via this procedure, you can make the contents of the error buffer visible via the front LED. This is especially useful for fault finding, when there is no picture.

When the SDM is activated, the front LED will show (by blinking) the contents of the error-buffer. Error-codes > 10 are shown as follows:

1. A long blink of 750 ms (which is an indication of the decimal digit),
2. A pause of 1500 ms,
3. "n" short blinks (where "n" = 1 - 9),
4. When all the error-codes are displayed, the sequence finishes with a LED blink of 3000 ms,
5. The sequence starts again.

Example: Error 12 9 6 0 0.

After activation of the SDM, the front LED will show:

1. 1 long blink of 750 ms (which is an indication of the decimal digit) followed by a pause of 1500 ms,
2. 2 short blinks of 250 ms, followed by a pause of 3000 ms,
3. 9 short blinks of 250 ms, followed by a pause of 3000 ms,
4. 6 short blinks of 250 ms, followed by a pause of 3000 ms,
5. 1 long blink of 3000 ms to finish the sequence,
6. The sequence starts again.

5.6.2 How to Activate

Use one of the following methods:

- Activate the SDM (only by shorting the soldering pad indicated in Figure "SDM Service contact" on the first page of this chapter to mass). The blinking front LED will show the entire contents of the error buffer (this works in "normal operation" mode and in "protection" mode). In order to avoid confusion with RC5 signal reception blinking, this LED blinking procedure is terminated when an RC5 command is received.
- Transmit the commands "MUTE", "06250x", and "OK" with a normal RC (where "x" is the position in the error buffer that has to be displayed). With x= 1, the last detected error is shown, x= 2 the second last error, etc.... When x= 0, all errors are shown.
- "DIAGNOSE X" with the DST (where "x" is the position in the error buffer that has to be displayed). With x= 1, the last detected error is shown, x= 2 the second last error, etc.... When x= 0, all errors are shown.

Note: It can take some seconds before the blinking LED starts.

5.7 Software Downloading

In this chassis, you can **upgrade** the software via the IAP Tool (In Application Programming). You can find more information on this in the paragraph "Service Tools" in this chapter.

5.8 Fault Finding and Repair Tips

Notes:

- It is assumed that the components are mounted correctly with correct values and no bad solder joints.
- Before any fault finding actions, check if the correct options are set.

5.8.1 NVM Editor

In some cases, it can be handy if one directly can change the NVM contents. This can be done with the "NVM Editor" in SAM mode. In the next table, the default NVM values are given.

Table 5-3 NVM default values

Item	Address (dec)	Default values (hex)		Default values (dec)	
		29PT9521/12	32PW9551/12	29PT9521/12	32PW9551/12
EW (EW Width)	19	15	1A	21	26
PW (EW Parabola Width)	20	19	19	25	25
HS (Horizontal Shift)	21	20	20	32	32
HP (Horizontal Parallelogram)	22	07	07	07	07
HB (Horizontal Bow)	23	07	07	07	07
UCP (EW Upper Corner Parabola)	24	20	20	32	32
LCP (EW Lower Corner Parabola)	25	20	20	32	32
TC (EW Trapezium)	26	1D	1D	29	29
VS (Vertical Slope)	27	27	27	39	39
VA (Vertical Amplitude)	28	20	20	32	32
SC (S-Correction)	29	15	15	21	21
VSH (Vertical Shift)	30	20	20	32	32
VX (Vertical Zoom)	31	19	19	25	25
VSL (Vertical Scroll)	32	20	20	32	32
HOP EW EHT Compensation	33	20	20	32	32
BLOR (Black Level Offset - Red)	34	08	08	08	08
BLOG (Black Level Offset - Green)	35	08	08	08	08
AGC (AGC Takeover)	36	0E	0E	14	14
OIF (IF-PLL Offset)	37	20	20	32	32
AGC10	38	01	01	01	01
H60 (60 Hz Horizontal Shift)	39	00	00	00	00
60 Hz Vertical Amplitude	42	04	04	04	04
YD & CL	43	04	07	04	07
RGB Brightness	46	28	28	40	40
NVM_TABLE_VERSION	60	64	64	100	100
OPTION_TABLE_VERSION	61	3C	3C	60	60
TXT Brightness	64	14	14	20	20
V60 offset (60Hz Vertical Amplitude)	66	04	04	04	04
CRYSTALALIGN	208	00	00	00	00
VIDEO PP	264	23	23	35	35
Last Colour	265	36	36	54	54
Last Contrast	266	64	64	100	100
Last Sharpness	267	05	05	05	05
Last Hue	268	32	32	50	50
Last Colour Temp	269	00	00	00	00
White-D Cool Red	294	00	00	00	00
White-D Cool Green	295	00	00	00	00
White-D Cool Blue	296	04	04	04	04
White-D Normal Red	297	25	25	37	37
White-D Normal Green	298	20	20	32	32
White-D Normal Blue	299	28	28	40	40
White-D Warm Red	300	08	08	08	08
White-D Warm Green	301	00	00	00	00
White-D Warm Blue	302	ED	ED	237	237
Audio Last Smart	342	03	03	03	03
Last Volume	343	23	23	35	35
Last Balance	344	0A	0A	10	10
Last Treble	345	00	00	00	00
Last Bass	346	00	00	00	00

Note:

- When aligning a TV set, it is convenient to start with the default settings, and then to change them, if necessary, to customized values.
- If you suspect a defective TV set is programmed with the wrong settings or options, try to restore the set to its default settings or set the options to their virgin mode (the latter can also be done via the NVM Editor in the SAM menu, see chapter 8 and the table Option codes).

If the remote control of a TV set is defective or missing, and you can not enter the CSM or SAM menu, it is always possible to return the TV to its virgin mode by simultaneously pressing the Volume+ and Volume- keys on the Top Control/Side I/O panel of the TV set.

5.8.2 SAM Menu Structure

The SAM Menu structure of the L06.1E AA is different from that of the ES1. Some of the menu items that were in the main menu of the ES1 are now in the submenu. The following table shows the structure of the SAM menu of the L06.1E AA.

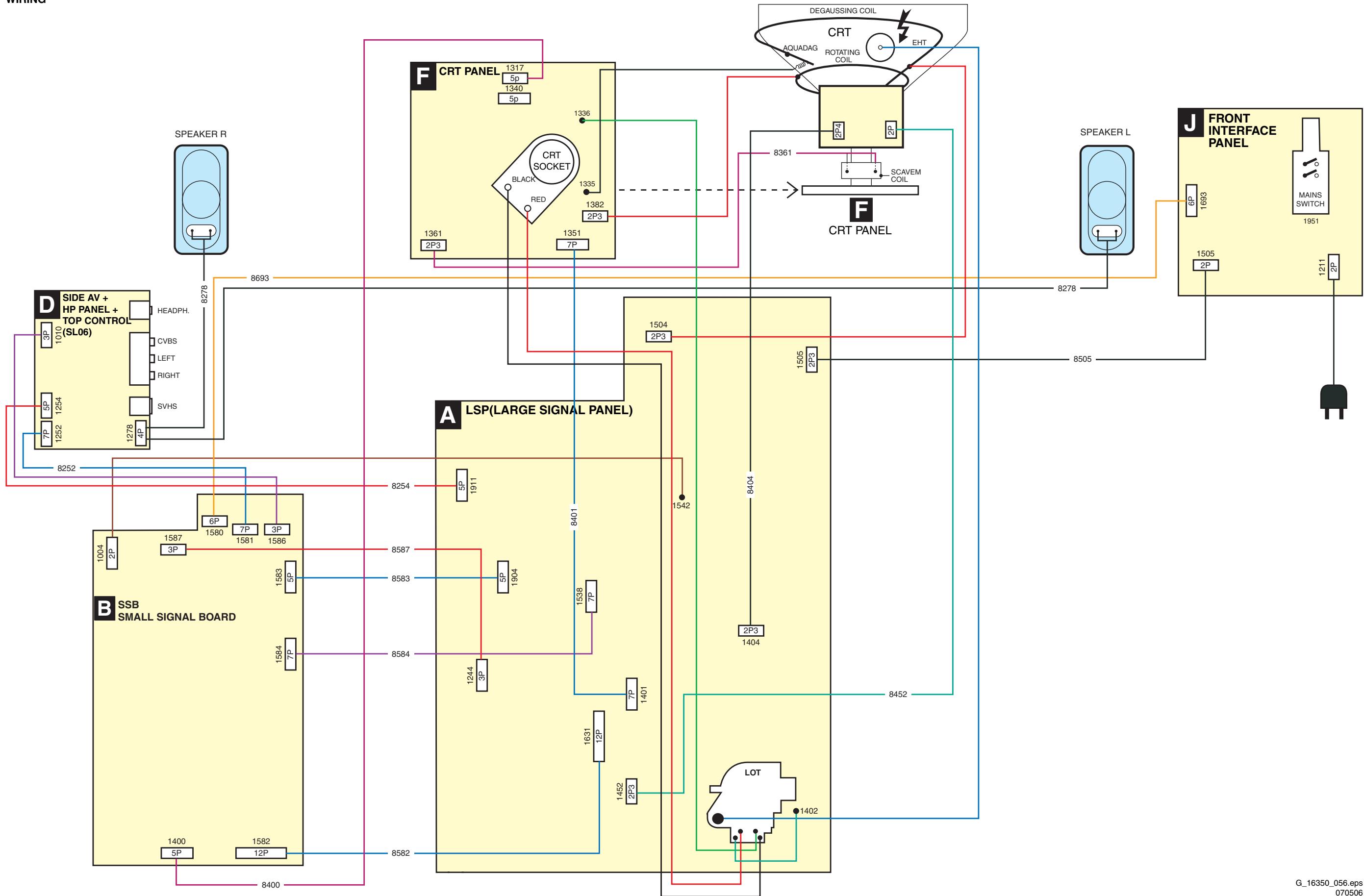
Table 5-4 SAM menu structure

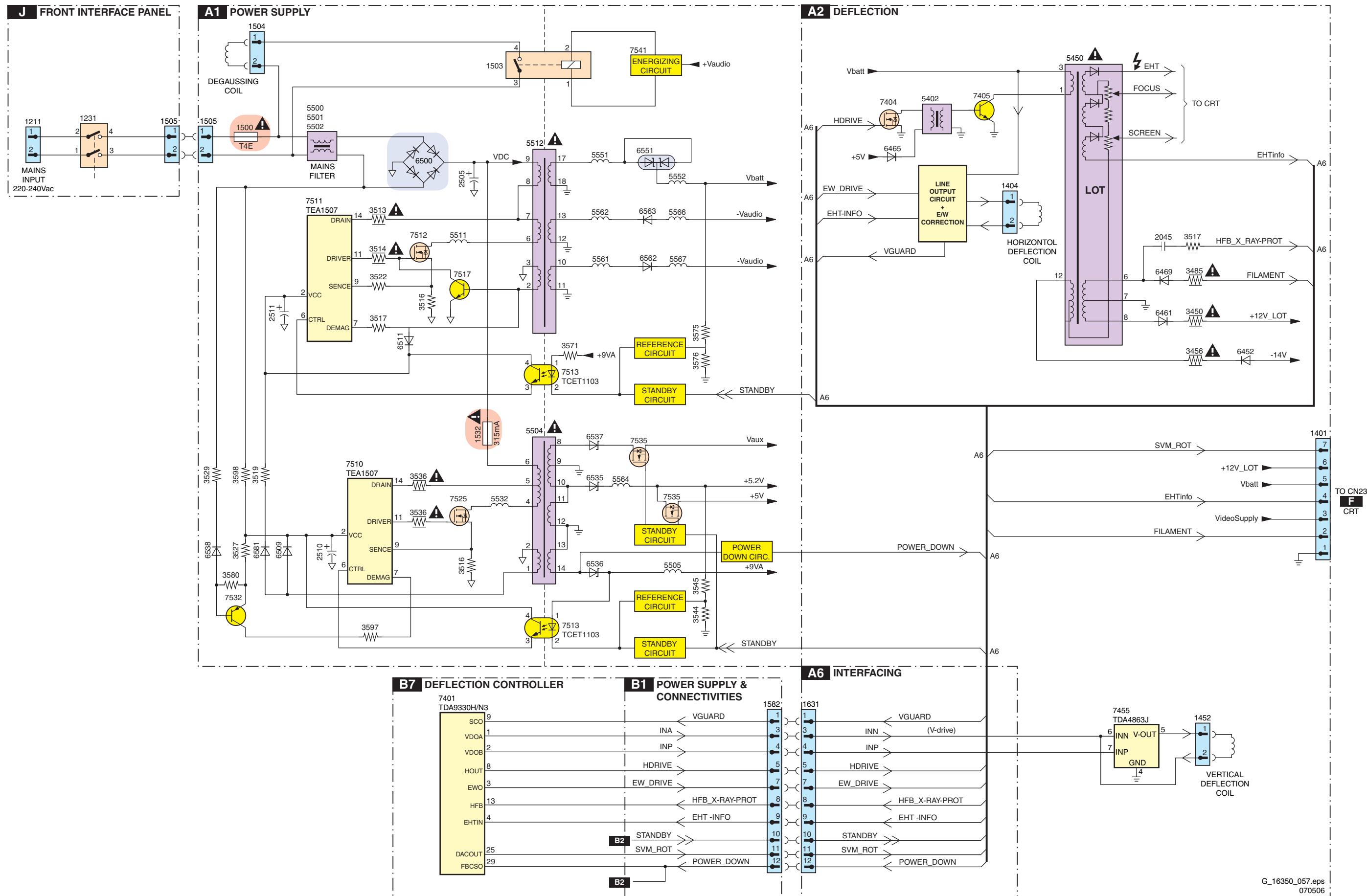
SAM Main Menu	SAM Submenu level 1	SAM Submenu level 2	Example of SAM Submenu values / settings	Explanation of menu items
Clear			Yes	Clear. Erases the contents of the error buffer. Select the CLEAR menu item and press the CURSOR RIGHT key. The content of the error buffer is cleared.
Options	OP1			Options. Used to set the option bits. See "Options" in the Alignments section for a detailed description.
	OP2			
	OP3			
	OP4			
	OP5			
	OP6			
	OP7			
Tuner	IFPLL			Tuner. Used to align the tuner. See "Tuner" in the Alignments section for a detailed description.
	AGC			
RGB Align	AKB		Off / On	RGB Align > White Tone. Used to align the white tone. See "RGB Align > White Tone" in the Alignments section for a detailed description.
	White Tone	Cool		
		Normal		
		Warm		
		BlackL Offset R		
		BlackL Offset G		
	CL		7	
Geometry	Horizontal	HP	8	Geometry. Used to align the Geometry. See "Geometry" in the Alignments section for a detailed description.
		HB	9	
		HSH	33	
		EWL	23	
		EWP	26	
		EWT	31	
		UCP	41	
		LCP	34	
	Vertical	SBL		Note: If the TV set is switched to 50 Hz/double lines (i.e. not Pixel Plus/100 Hz) only "VS" is shown in the menu.
		VS	42	
Audio	AF-M		250	Audio. No audio alignment is necessary for this television set.
	A2-T		400	
	AT		2	
NVM Editor	ADR		0x0001 1	NVM Editor. Can be used to change the NVM data in the television set.
	VAL		0x0000 0	
	Store			
Compair	Compair			ComPalr. In other TV sets, this menu item can be used to switch the television to "In System Programming" (ISP) mode, for software uploading via ComPair. In the L06.1E AA, however, a different system is used: IAP. Caution: When this mode is selected without IAP connected, the TV will be blocked. Remove the AC power to reset the TV.
	IAP			IAP. This tool is used instead of Compair (Compair is not implemented in this TV set).

6. Block Diagrams, Testpoint Overviews, and Waveforms

Wiring Diagram

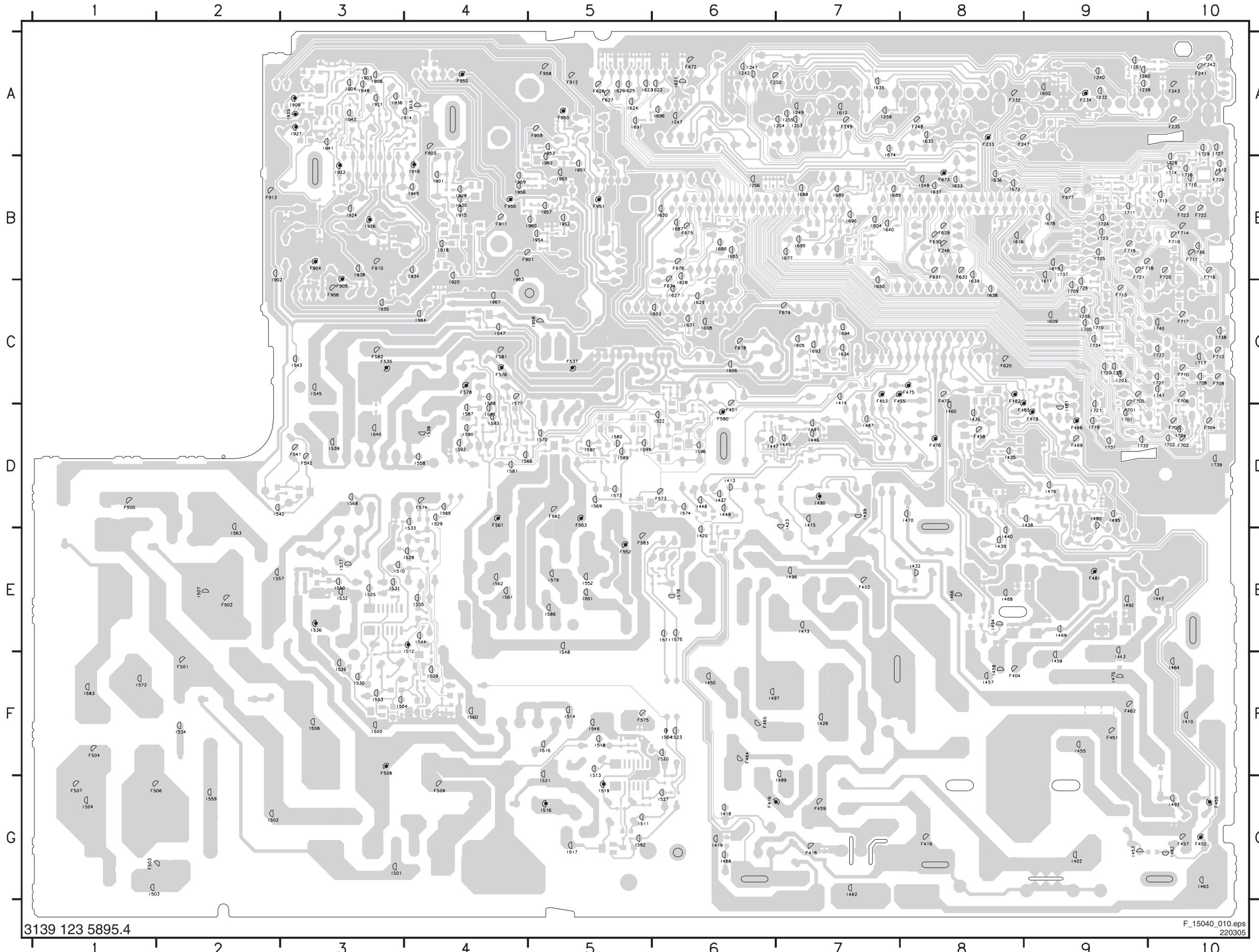
WIRING

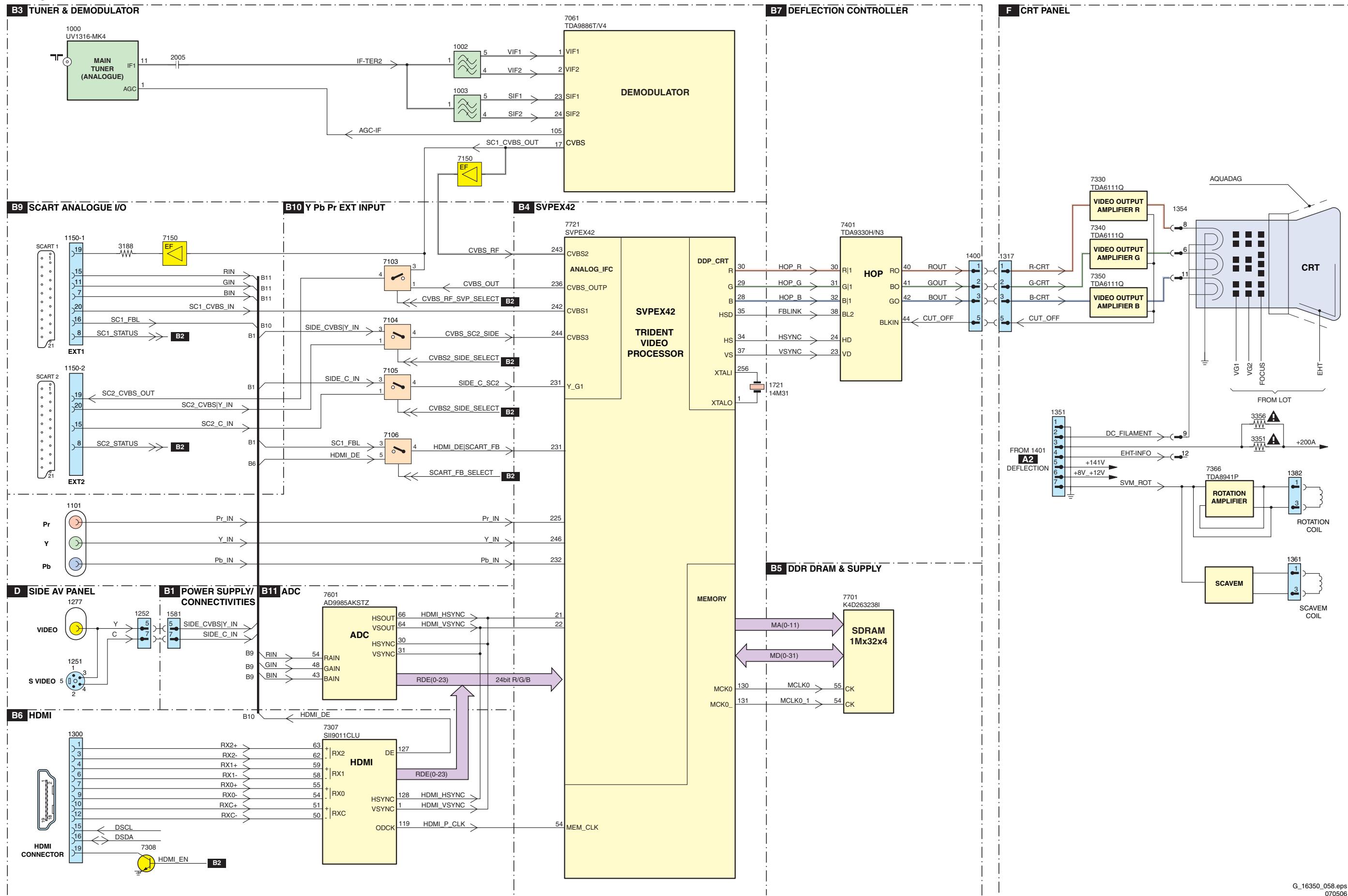


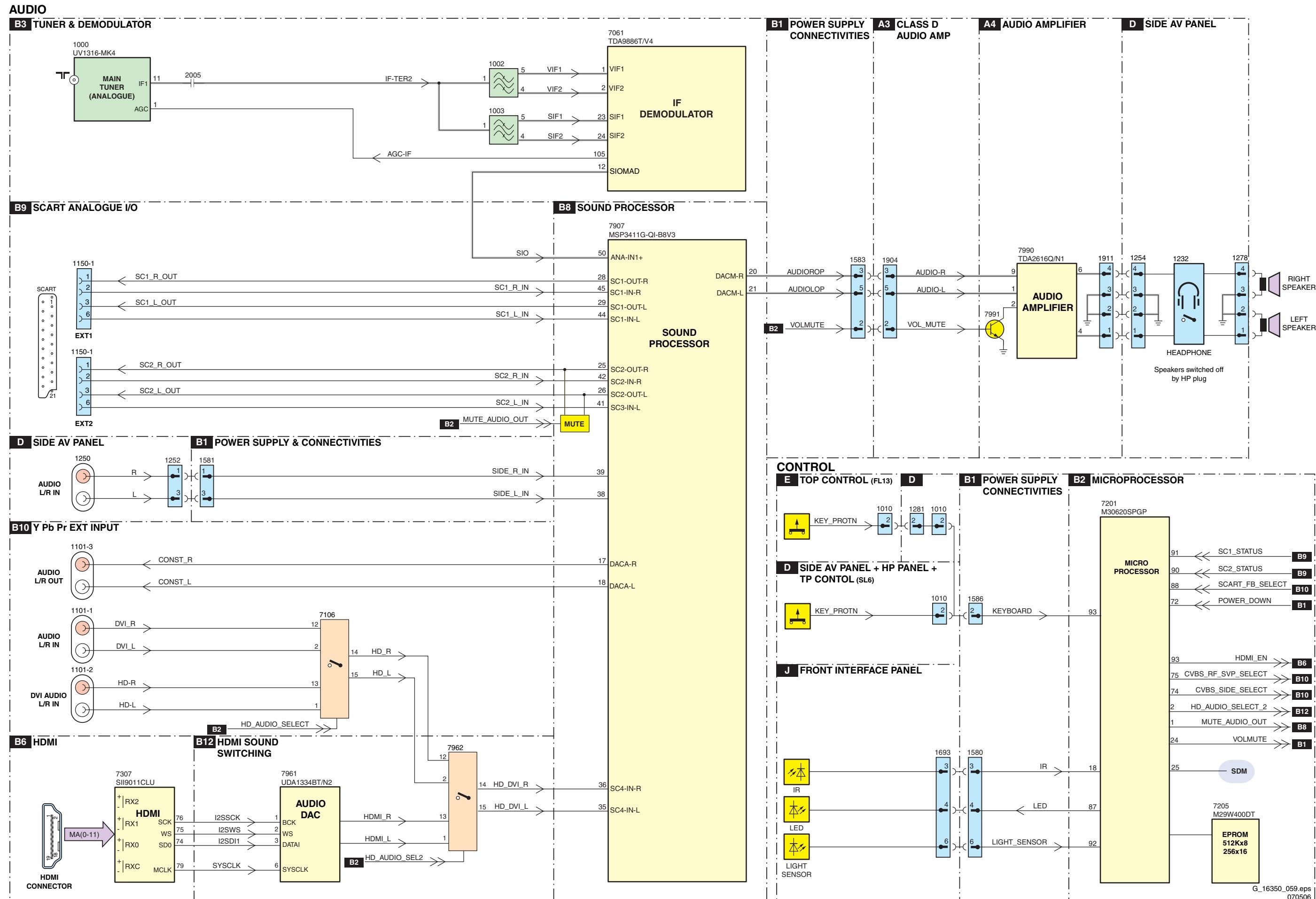
Block Diagram LSP Supply and Deflection**SUPPLY AND DEFLECTION**

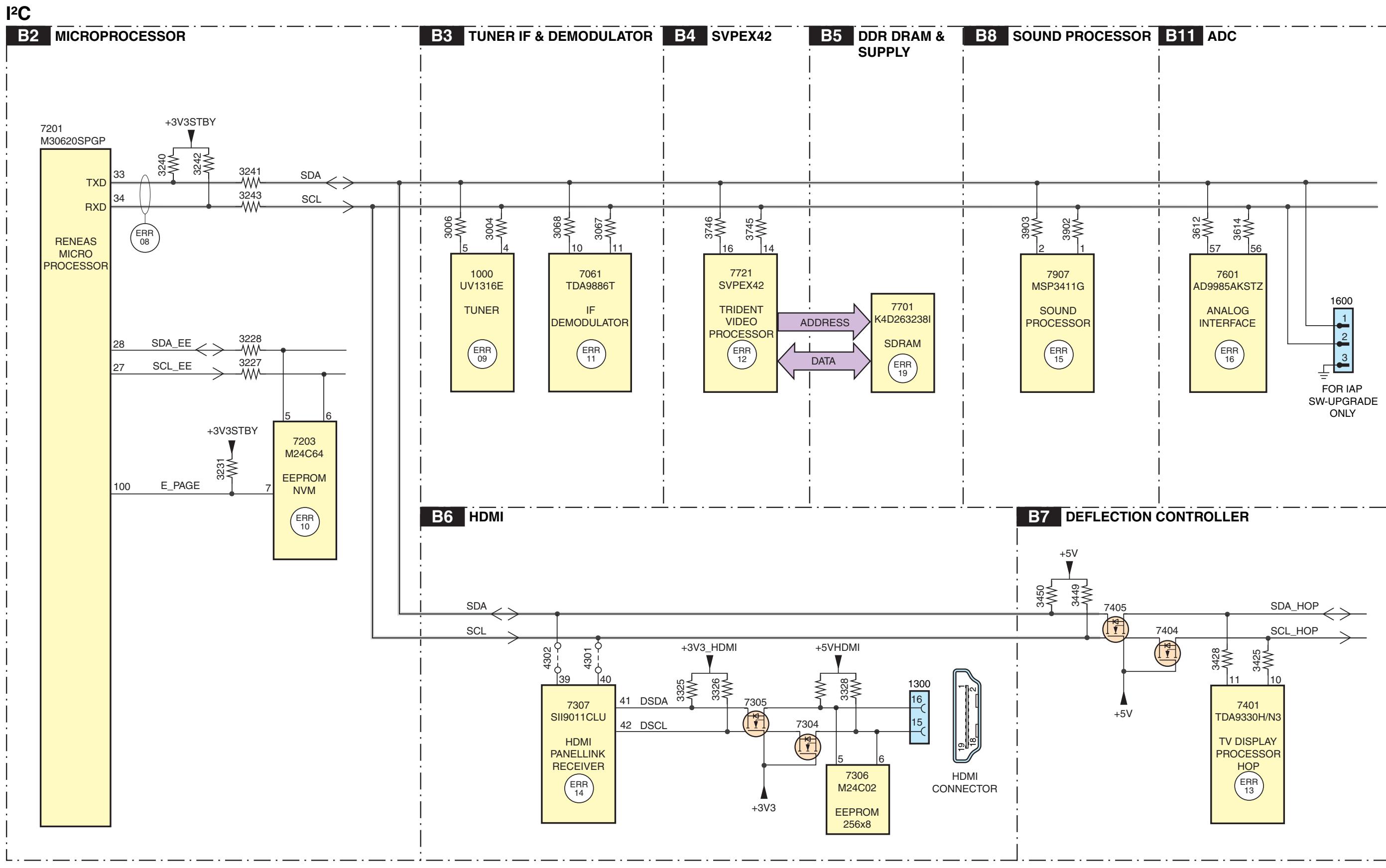
Testpoint Overview LSP

F232 A8 F246 B8 F404 F8 F455 C7 F465 F6 F481 E9 F504 F1 F541 D3 F574 D4 F583 E5 F632 B8 F678 C6 F706 C10 F715 B9 F722 B10 F910 B3 F955 B4 I241 A6 I255 A7 I413 D6 I428 F7 I440 E8 I449 D6 I460 D8 I470 D7 I487 D8 I494 E8 I502 G2 I511 G5 I518 F5 I526 F3 I533 E4 I704 D10
F233 A8 F247 A8 F416 G6 F456 G10 F466 D9 F482 C8 F506 G1 F542 D3 F575 F5 F620 C8 F672 A6 F708 C10 F716 B10 F723 B10 F911 B4 F956 C3 I242 A6 I256 B6 I414 C7 I432 E8 I442 E10 I450 F6 I462 G10 I473 F9 I488 G6 I495 D9 I503 G1 I512 E4 I519 G5 I527 G6 I534 F2 I705 C9
F234 A9 F248 A8 F418 G7 F457 G10 F469 D9 F483 D8 F507 G1 F552 E5 F576 C4 F627 A5 F673 B8 F701 D9 F710 C10 F717 C10 F724 B10 F912 A5 F958 A5 I247 A6 I259 A7 I415 D7 I435 D8 I443 F9 I453 G9 I463 G10 I475 F9 I489 G7 I496 E7 I504 G1 I513 F5 I520 F6 I528 E4 I536 E3 I707 C10
F235 A10 F249 A7 F419 G8 F458 D8 F472 C8 F500 D1 F508 F3 F561 D4 F578 C4 F628 A5 F674 C6 F702 D10 F711 B10 F718 B9 F901 B4 F913 B2 F959 A5 I248 B8 I260 A9 I418 G6 I436 D8 I445 D7 I455 F9 I464 F10 I476 D9 I490 D7 I497 F6 I506 F3 I514 F5 I521 G5 I529 D4 I537 E3 I708 C10
F241 A10 F250 A6 F451 F9 F459 G7 F475 C8 F501 F2 F509 G4 F562 D5 F580 D6 F629 B8 F703 D10 F712 C9 F719 B10 F903 A4 F950 A5 I232 A9 I261 A9 I419 G6 I437 D6 I446 D7 I457 F8 I466 E8 I480 D9 I491 D7 I499 D7 I507 E2 I515 F5 I522 D6 I530 F3 I538 D4 I709 C9
F242 A10 F401 D6 F452 G10 F462 F9 F476 D8 F502 E2 F535 C3 F563 D5 F581 C4 F630 B8 F676 B6 F704 D10 F713 C9 F720 B10 F904 B3 I259 A7 I402 G9 I420 E6 I438 D9 I447 D6 I458 F8 I468 E8 I481 D9 I492 E9 I500 F3 I509 F4 I516 G5 I523 F6 I531 E3 I539 D3 I710 C9
F243 A10 F402 E7 F453 C7 F464 F6 F478 D9 F503 G1 F537 C5 F573 D6 F582 C3 F631 B8 F677 B9 F705 C9 F714 B10 F721 B9 F905 C3 F952 A4 I254 A7 I410 F10 I423 D7 I439 E8 I448 D6 I459 F9 I469 E9 I482 G10 I501 G3 I510 E3 I517 G5 I525 E3 I532 E3 I540 D3 I711 B9
I542 D2 I712 B10 I543 C3 I713 B10 I544 E4 I714 B10 I545 C3 I715 B10 I546 F5 I716 B10 I547 C4 I717 C10 I548 F5 I719 D9 I550 E3 I720 C9 I551 E5 I721 D9 I552 E5 I722 C10 I553 F3 I723 B9 I554 F3 I724 B9 I555 E4 I725 B9 I557 E2 I726 C9 I558 D4 I727 A10 I559 G2 I728 B10 I560 F4 I729 A10 I561 E4 I731 D9 I562 E4 I732 D9 I563 E2 I733 C9 I564 F6 I734 C9 I565 D4 I735 C9 I566 D4 I736 B10 I568 D3 I737 B9 I569 D5 I738 C10 I570 D5 I739 D10 I571 E6 I740 C10 I572 F1 I741 C10 I573 D5 I901 B4 I574 D6 I902 C2 I575 E6 I903 A3 I577 C4 I904 A3 I578 E6 I905 A3 I579 E5 I909 A3 I580 D5 I913 A4 I581 D4 I914 A4 I582 G3 I915 B4 I583 F1 I916 B4 I586 E5 I918 B4 I587 D4 I920 C4 I588 C4 I921 A3 I589 D5 I922 B3 I590 D4 I924 B3 I591 D4 I925 A3 I592 D4 I926 B3 I593 D4 I927 A3 I596 D6 I928 B4 I597 D5 I930 B4 I599 D5 I934 B4 I602 A9 I935 C3 I603 C6 I936 A3 I604 B7 I938 B3 I605 C7 I941 A3 I606 C6 I943 A3 I608 C6 I945 B4 I609 C9 I948 A3 I610 B9 I951 B5 I611 C9 I952 B5 I612 A7 I953 A5 I616 B8 I954 B5 I622 A6 I958 B5 I623 A5 I958 C5 I624 A5 I955 B4 I625 A5 I960 B5 I626 A5 I962 B5 I627 C6 I963 B4 I628 C6 I964 C4 I629 C6 I965 B5 I630 B6 I967 C4 I631 C6 I632 A8 I633 B8 I634 C7 I635 A7 I636 B8 I637 B8 I638 C8 I639 C8 I640 B7 I650 C7 I672 B8 I674 A7 I677 B7 I678 B9 I680 B7 I685 B6 I686 B6 I687 B6 I688 B7 I689 B7 I690 B7 I691 A5 I693 C7 I694 C7 I695 B7 I696 A6 I701 D9 I702 D10 I703 C9



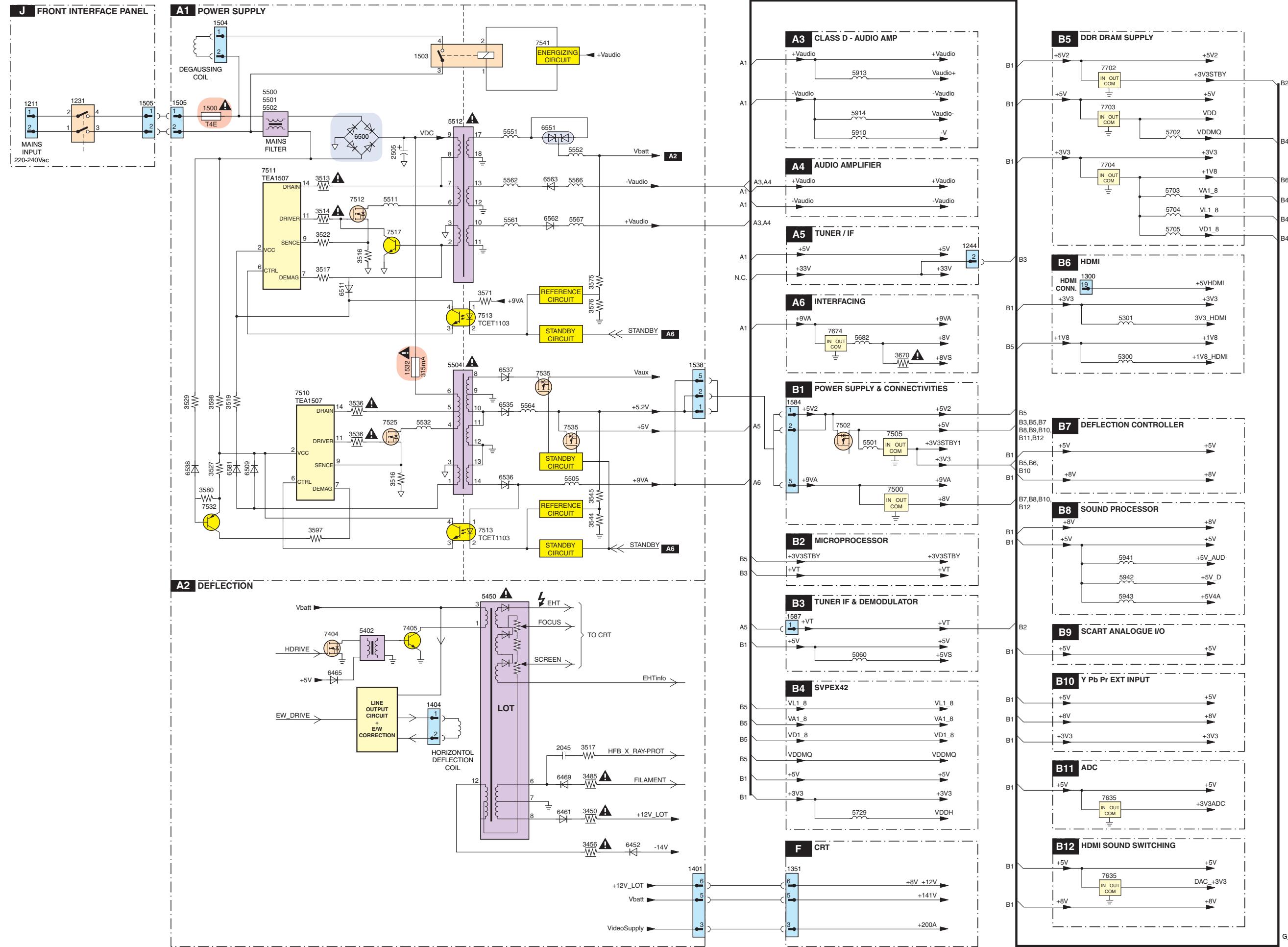
Block Diagram Video**VIDEO**

Block Diagram Audio

I²C Overview

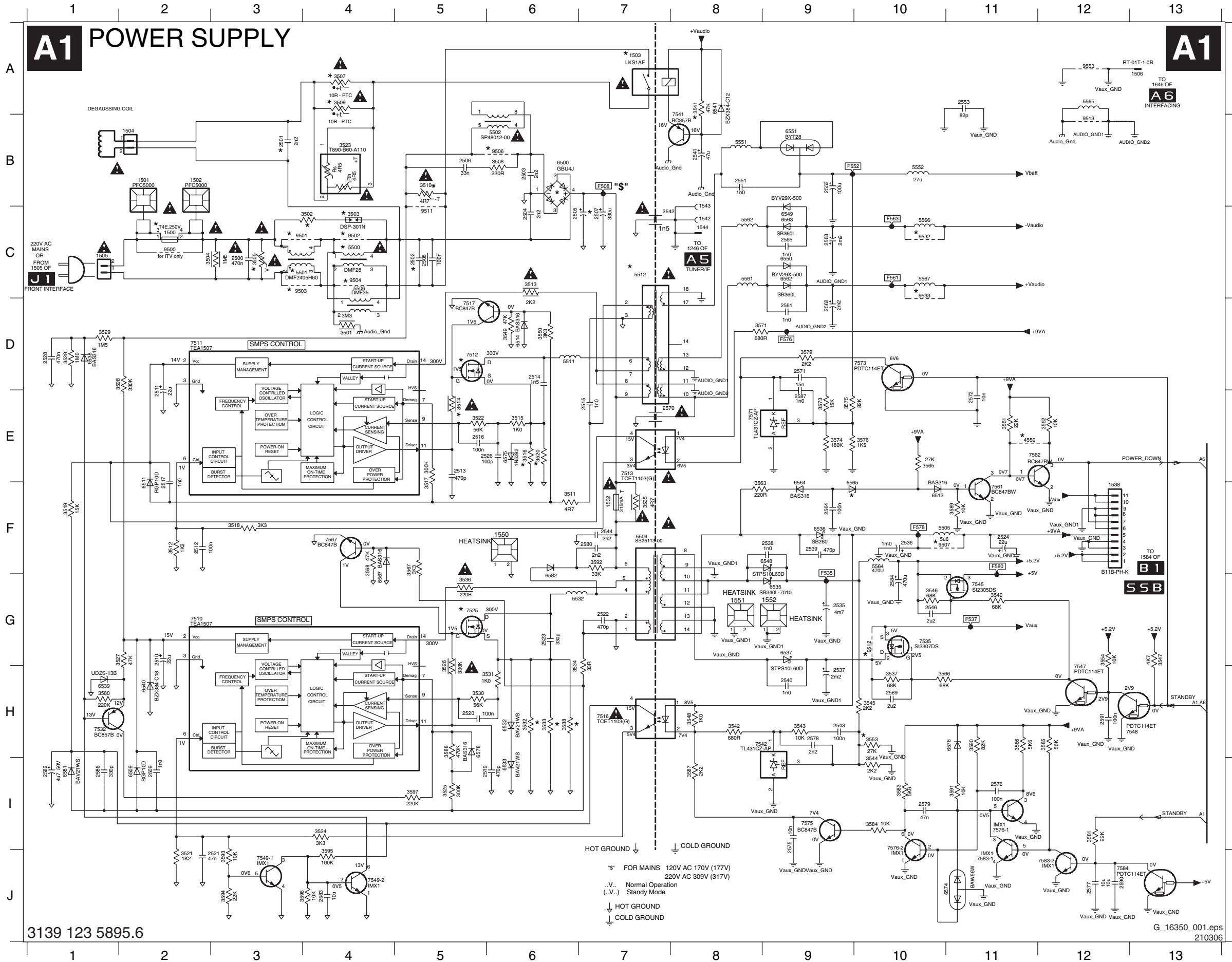
Supply Lines Overview

SUPPLY LINE OVERVIEW



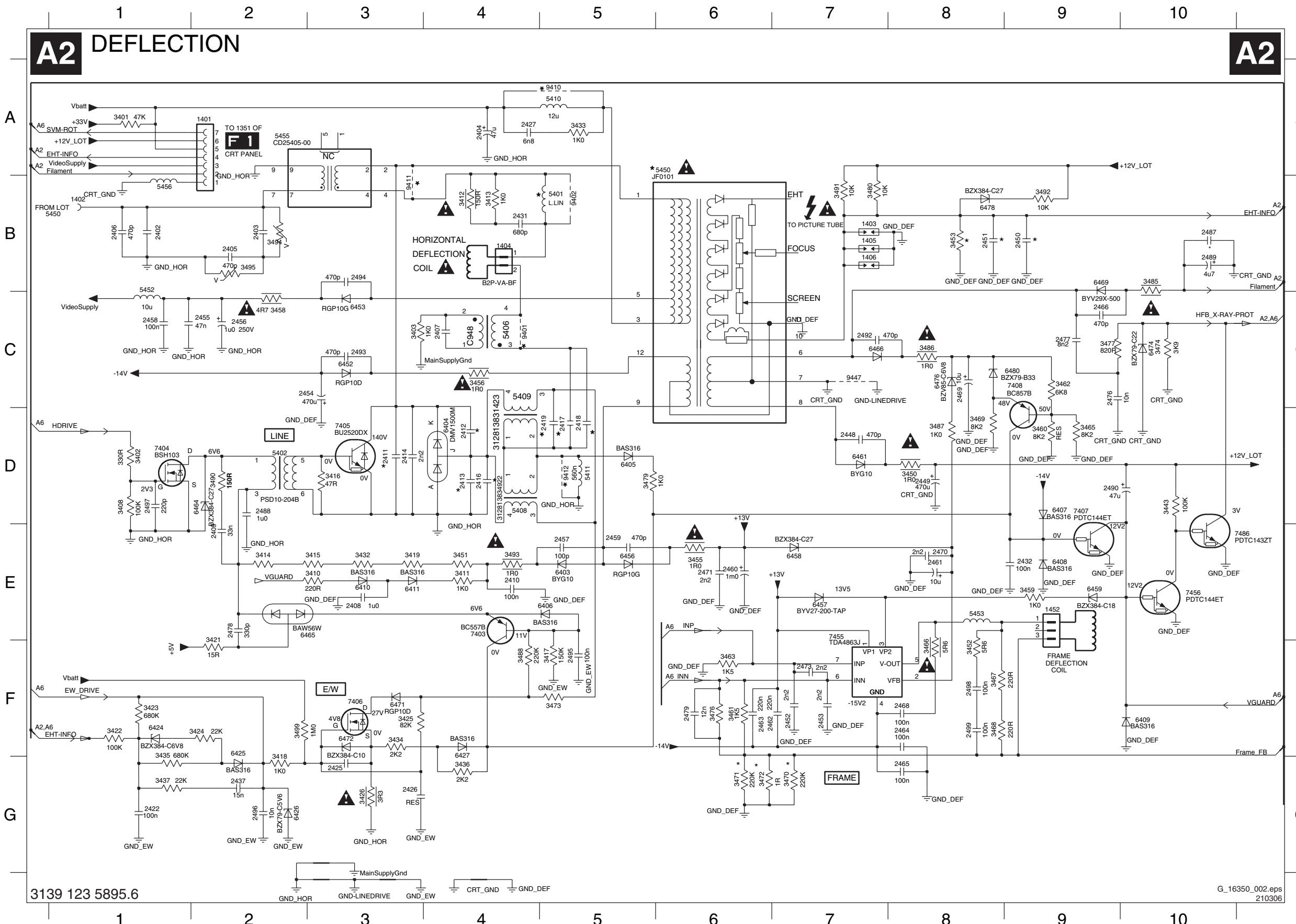
7. Circuit Diagrams and PWB Layouts

LSP: Power Supply



1500 C2	3545 H10	9507 F10
1501 B2	3546 G10	9511 C5
1502 B2	3547 G13	9512 G10
1503 A7	3548 H8	9513 B12
1504 B2	3549 D6	9532 C10
1505 C1	3550 D6	9533 C10
1506 A13	3551 E11	9553 A12
1507 F2	3552 E12	F508 B7
1538 F2	3553 H10	F538 G9
1542 C8	3554 G12	F537 G11
1543 B8	3556 F8	F552 B9
1544 C8	3565 E10	F561 C10
1550 F6	3566 H10	F563 C10
1551 G8	3567 F5	F576 D9
1552 G8	3568 F4	F578 F10
2500 C3	3571 D8	F580 F11
2501 B3	3573 E9	
2502 C5	3574 E9	
2503 B6	3575 E9	
2504 C6	3576 E10	
2505 C6	3579 D9	
2506 B5	3580 H1	
2507 C7	3581 I12	
2508 C5	3583 I10	
2509 I2	3584 I10	
2510 G2	3585 H12	
2511 E2	3586 H11	
2512 F2	3587 I8	
2513 E5	3588 H5	
2514 D6	3589 F11	
2515 E7	3590 H11	
2516 E5	3591 I11	
2517 F2	3592 F7	
2519 I6	3593 J3	
2520 H5	3594 J3	
2521 J3	3595 J4	
2522 G7	3596 J4	
2523 G6	3597 I5	
2524 F11	3598 D2	
2526 E6	4550 E11	
2528 D1	5500 C4	
2535 G9	5501 C3	
2536 F10	5502 B6	
2537 H9	5504 F7	
2538 F9	5505 F10	
2539 F9	5506 C4	
2540 H9	5511 D6	
2541 B8	5512 C7	
2542 C7	5532 G6	
2543 H9	5551 B8	
2544 F7	5552 B10	
2546 G10	5561 C8	
2551 B8	5562 C8	
2552 B9	5564 F10	
2553 A11	5565 A12	
2561 D9	5566 C10	
2562 D9	5567 C10	
2563 C9	6500 B6	
2564 F9	6509 I2	
2565 C9	6511 F2	
2570 E7	6512 F10	
2571 D9	6514 D6	
2572 E11	6532 H6	
2575 I9	6533 I6	
2576 I11	6535 G9	
2577 J12	6536 F9	
2578 H9	6537 G9	
2579 I10	6538 D1	
2580 F7	6539 H1	
2582 I1	6540 H2	
2583 J4	6541 A8	
2584 G10	6548 F9	
2586 I1	6549 C9	
2587 E9	6550 C9	
2589 H10	6551 B9	
2590 J12	6562 C9	
2591 H12	6563 C9	
3501 D4	6564 F9	
3502 C4	6565 F9	
3503 C4	6567 G4	
3504 C2	6574 J11	
3505 C3	6575 E6	
3507 A4	6576 H11	
3508 B6	6578 H5	
3509 A4	6581 I1	
3510 B5	6582 F6	
3511 F6	7510 G2	
3512 F2	7511 D2	
3513 C6	7512 D5	
3514 E5	7513 E7	
3515 E6	7516 H7	
3516 E6	7517 D5	
3517 F5	7525 G5	
3518 F3	7532 H1	
3519 F1	7535 G10	
3520 E6	7541 B8	
3521 J2	7542 H8	
3522 E5	7545 G11	
3523 B4	7547 G12	
3524 I4	7548 H13	
3525 I5	7549-1 J3	
3526 H5	7549-2 J4	
3527 G2	7561 F11	
3528 D1	7562 E11	
3529 D1	7567 F4	
3530 H5	7571 E8	
3531 H6	7573 D10	
3532 H6	7575 I9	
3533 H6	7576-1 I11	
3534 H6	7576-2 I10	
3535 F7	7583-1 J11	
3536 G5	7583-2 J12	
3537 H10	7584 J12	
3538 H6	9500 C2	
3540 G11	9501 C3	
3541 A8	9502 C4	
3542 H8	9503 C3	
3543 H9	9504 C4	
3544 I10	9506 B6	

LSP: Deflection

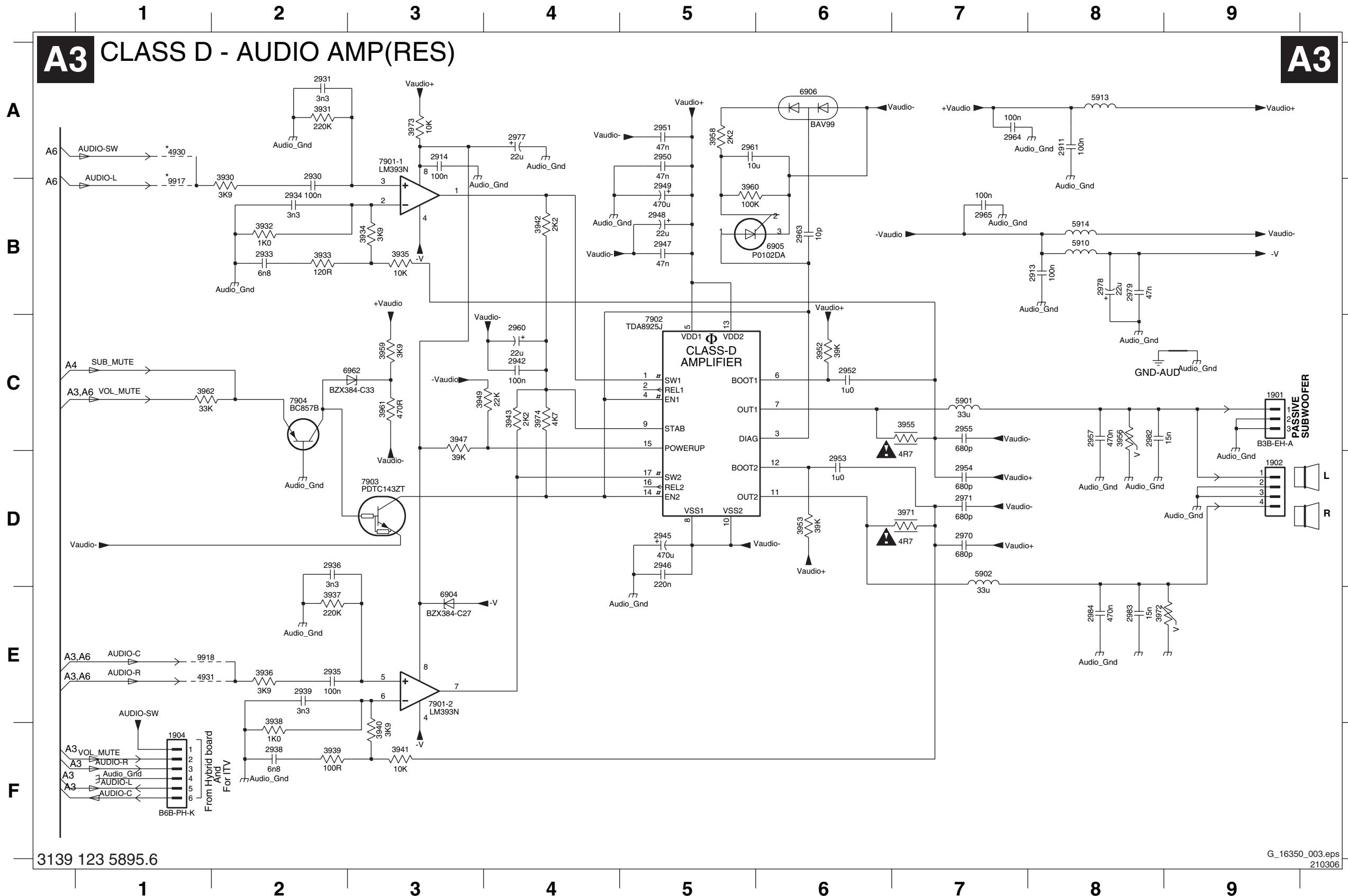


1401 A2	3426 G3	7406 F3
1402 B1	3432 E3	7407 D9
1403 B7	3433 A5	7408 C9
1404 B4	3434 F3	7455 E7
1405 B7	3435 G1	7456 E10
1406 B7	3436 G4	7486 E10
1452 E9	3437 G1	9401 C4
2402 B1	3443 D10	9402 B5
2403 B2	3450 D8	9410 A5
2404 A4	3451 E4	9411 B3
2405 B2	3452 F8	9412 D5
2407 C4	3455 E6	9447 C7
2408 E3	3456 C4	
2409 E2	3458 C2	
2410 E4	3459 E9	
2411 D3	3460 D9	
2412 D4	3461 F6	
2413 D9	3462 C9	
2414 D3	3463 F6	
2416 D4	3465 D9	
2417 D5	3466 F8	
2418 D5	3467 F8	
2419 D5	3468 F8	
2422 G1	3469 D8	
2425 G3	3470 G7	
2426 G3	3471 G6	
2427 A4	3472 G6	
2431 B4	3473 F5	
2432 E9	3474 C10	
2437 G2	3476 F6	
2448 D7	3477 C9	
2449 D8	3479 D5	
2450 B9	3480 B7	
2451 B8	3485 B10	
2452 F7	3486 C8	
2453 F7	3487 D8	
2454 C2	3488 F4	
2455 C2	3490 D2	
2456 C2	3491 B7	
2457 E5	3492 B9	
2458 C1	3493 E4	
2459 E5	3494 B2	
2460 E6	3495 B2	
2461 E8	3499 F2	
2462 F7	5401 B5	
2463 F6	5402 D2	
2464 F8	5406 C4	
2465 G8	5408 D4	
2466 C9	5409 C4	
2467 E8	5410 A5	
2468 C8	5411 D5	
2470 E8	5450 A6	
2471 E6	5452 C1	
2473 F7	5453 E8	
2476 C9	5455 A2	
2477 C9	5456 B1	
2478 E2	6403 E5	
2479 F6	6404 D4	
2487 B10	6405 D5	
2488 D2	6406 E5	
2489 B10	6407 D9	
2490 D9	6408 E9	
2492 C7	6409 F10	
2493 C3	6410 E3	
2494 B3	6411 E3	
2495 F5	6424 F1	
2496 G2	6425 F2	
2497 D1	6426 G2	
2498 F8	6427 F4	
2499 F8	6452 C3	
3401 A1	6453 C3	
3402 D1	6456 E5	
3403 C3	6457 E7	
3408 D1	6458 E7	
3410 E3	6459 E9	
3411 E4	6461 D7	
3412 B4	6464 D2	
3413 B4	6465 E3	
3414 E2	6466 C7	
3415 E3	6469 B9	
3416 D3	6471 F3	
3417 F5	6472 F3	
3418 G2	6474 C10	
3419 E3	6476 C8	
3421 F2	6478 B8	
3422 F1	6480 C9	
3423 F1	7403 E4	
3424 F2	7404 D1	
3425 F3	7405 D3	

3139 123 5895.6

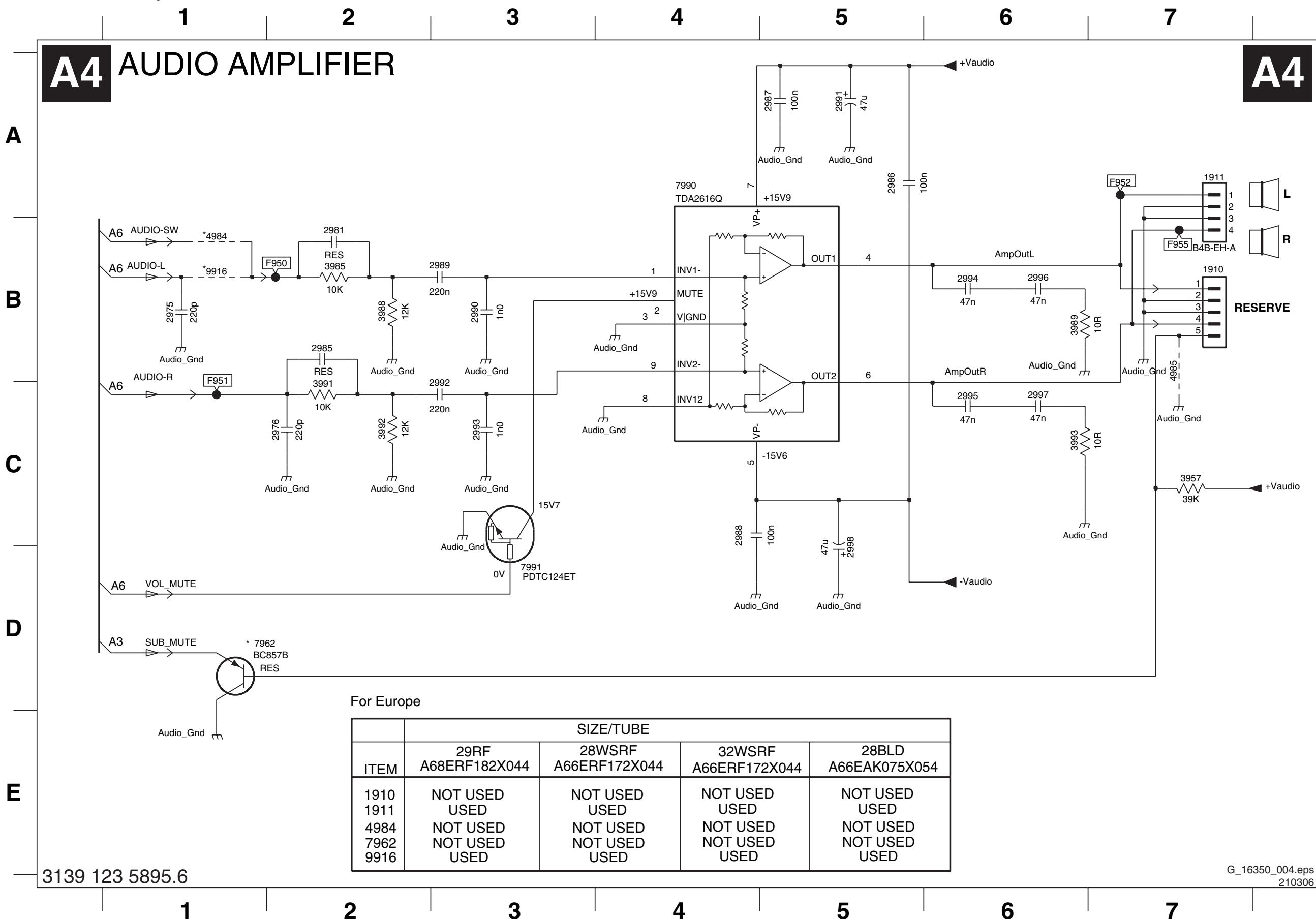
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LSP: Class D Audio Amplifier (Res)



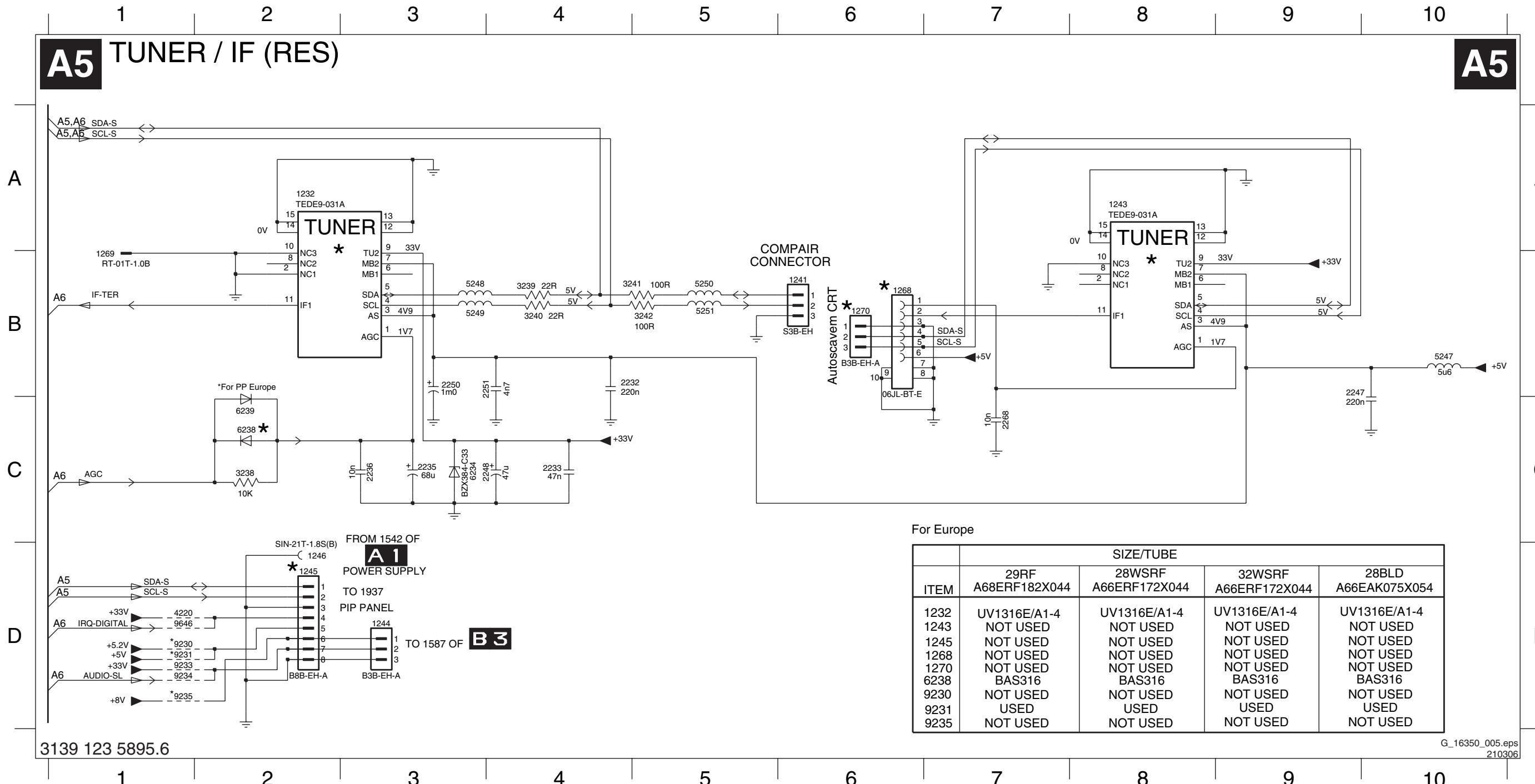
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2911 A8	6905 B6
2913 B8	6906 A6
2914 A3	6962 C3
2930 B2	7901-1 A3
2931 A2	7901-2 E3
2933 B2	7902 C5
2934 B2	7903 D3
2935 E2	7904 C2
2936 D2	9917 B1
2938 F2	9918 E1
2939 E2	
2942 C4	
2945 D5	
2946 D5	
2947 B5	
2948 B5	
2949 B5	
2950 A5	
2951 A5	
2952 C6	
2953 D6	
2954 D7	
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2961 A5	
2963 B6	
2964 A7	
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2970 D7	
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2982 C8	
2983 E8	
2984 E8	
3930 B2	
3931 A2	
3932 B2	
3933 B2	
3934 B3	
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3936 E2	
3937 E2	
3938 F2	
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3940 F3	
3941 F3	
3942 B4	
3943 C4	
3947 C3	
3949 C3	
3952 C6	
3953 D6	
3955 C7	
3956 C8	
3958 A5	
3959 C3	
3960 B5	
3961 C3	
3962 C1	
3971 D7	
3972 E8	
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4931 E1	
5901 C7	
5902 D7	
5910 B8	

LSP: Audio Amplifier

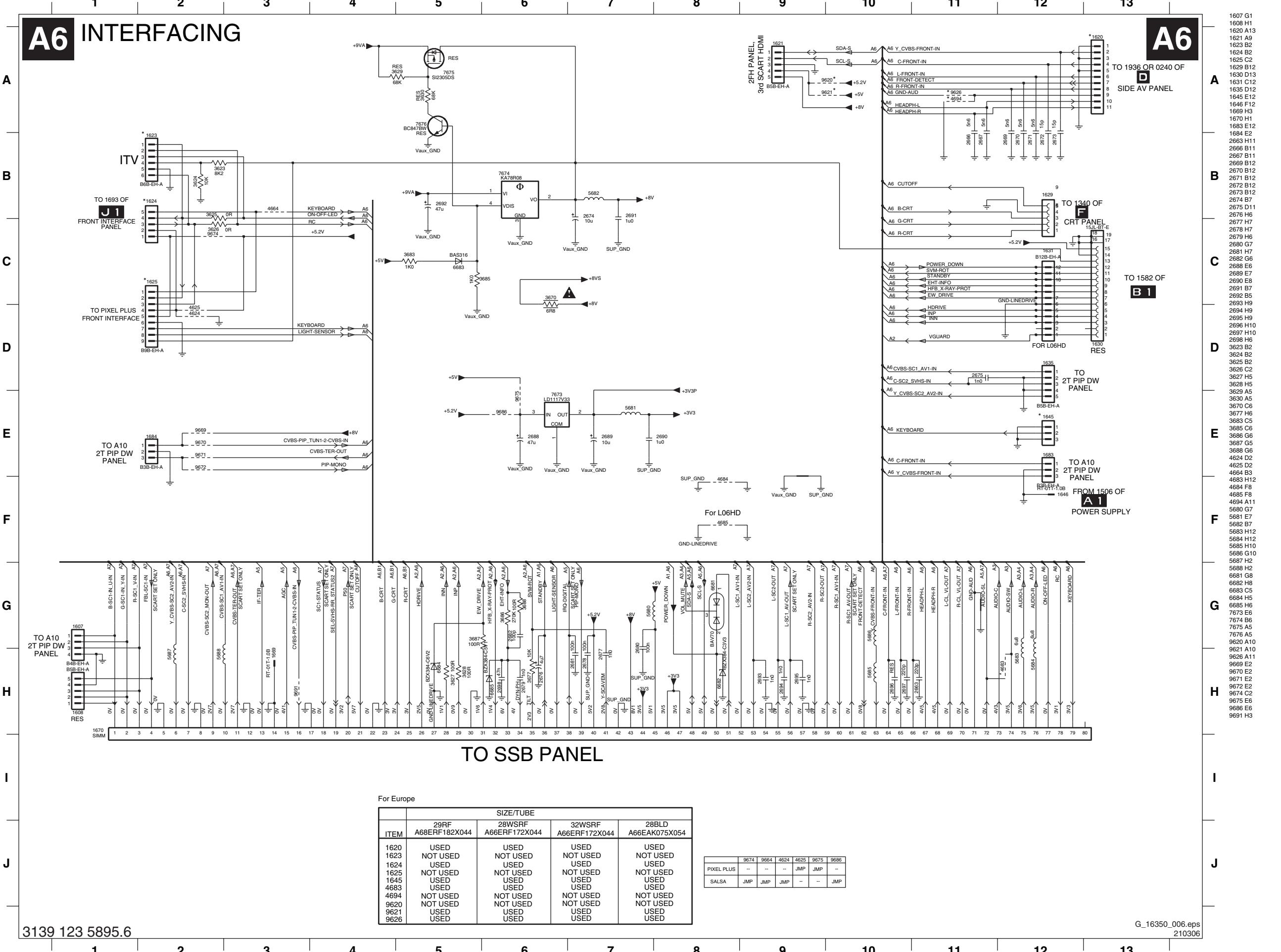


LSP: Tuner IF (Res)

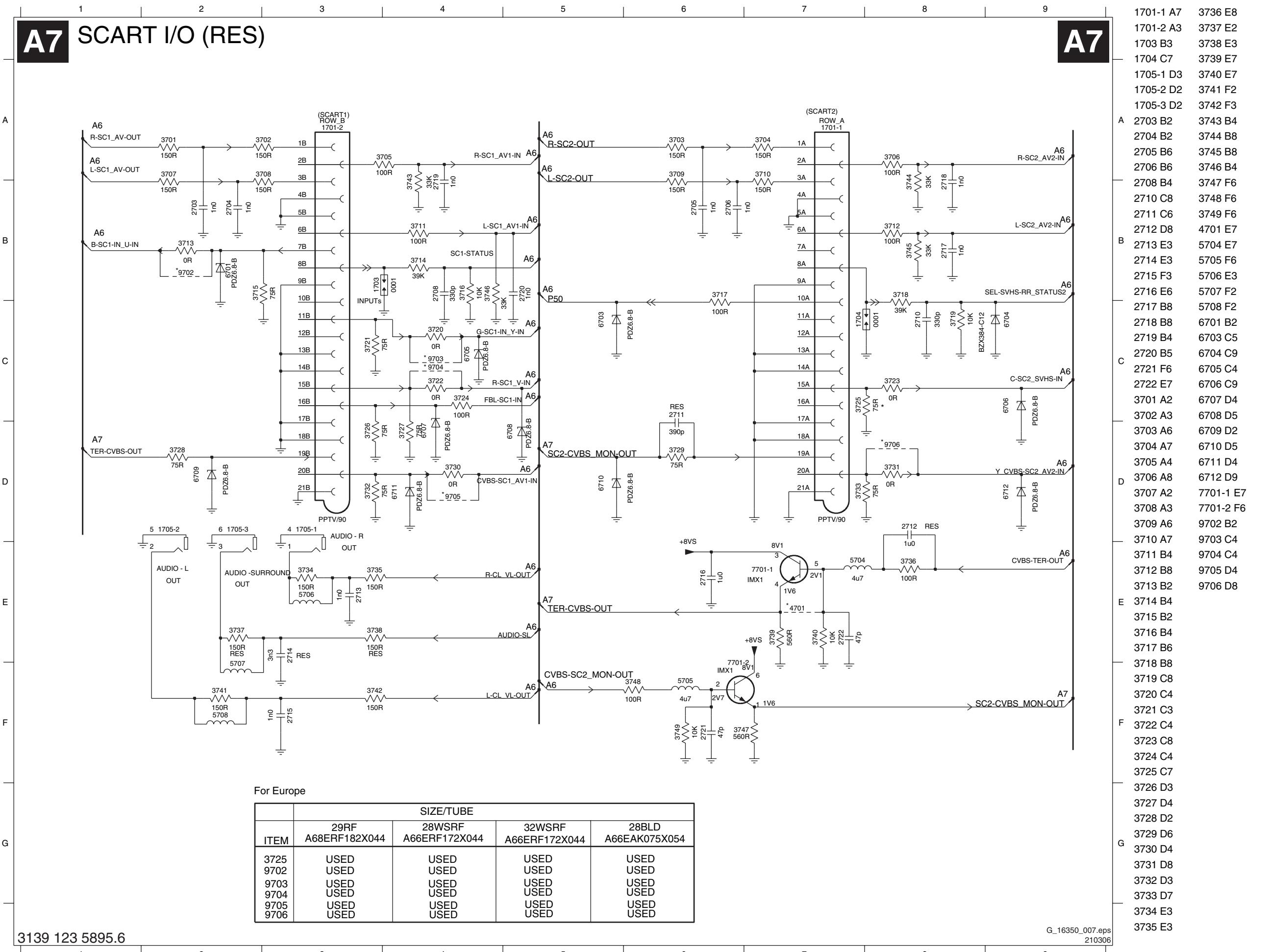
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 1241 B6 1244 D3 1246 D2 1269 B1 2232 B4 2235 C3 2247 B9 2250 B3 2268 C7 3239 B4 3241 B4 4220 D1 5248 B3 5250 B5 6234 C3 6239 C2 9231 D1 9234 D1 9646 D1



LSP: Interfacing

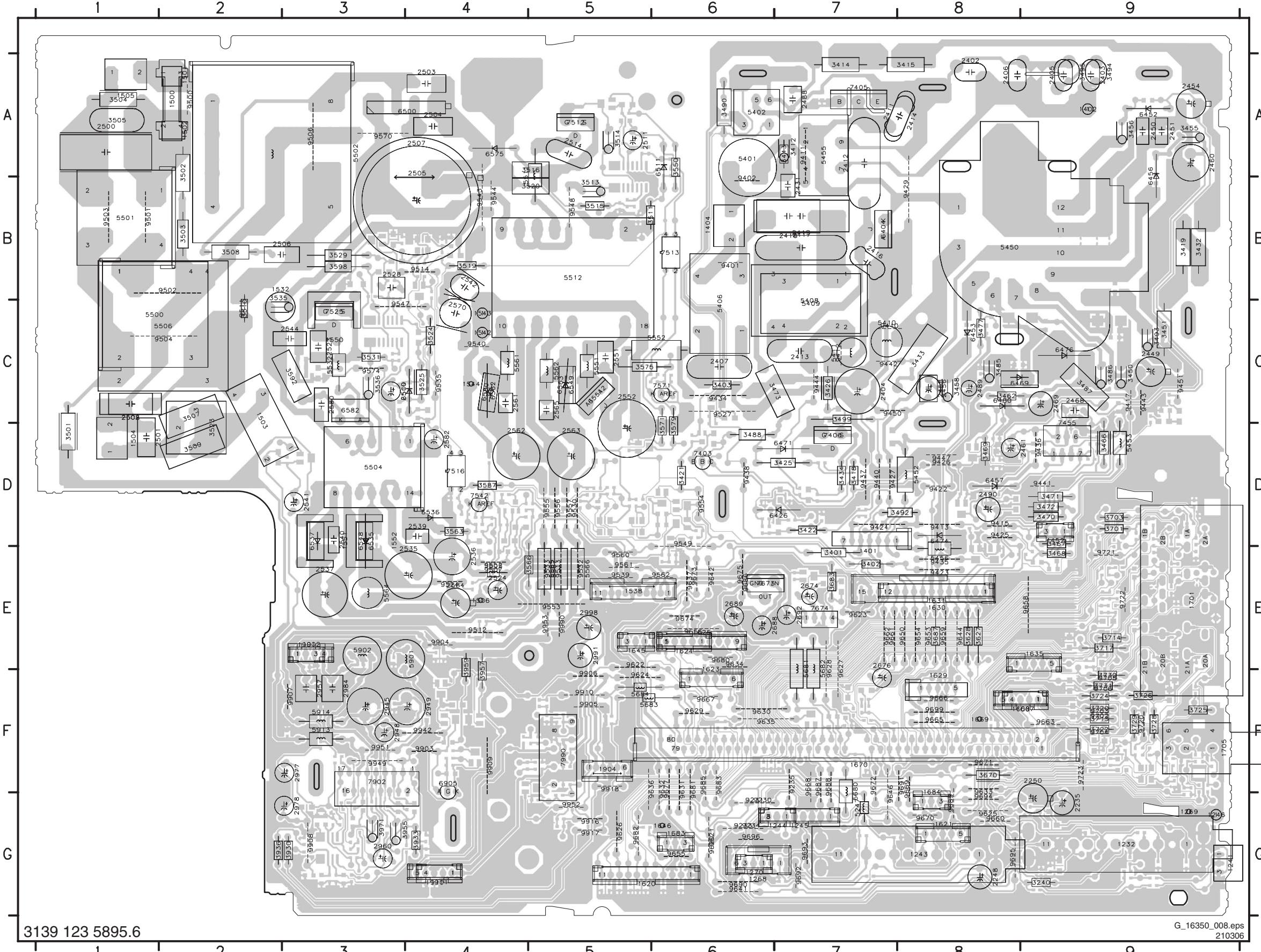


LSP: SCART I/O (Res)



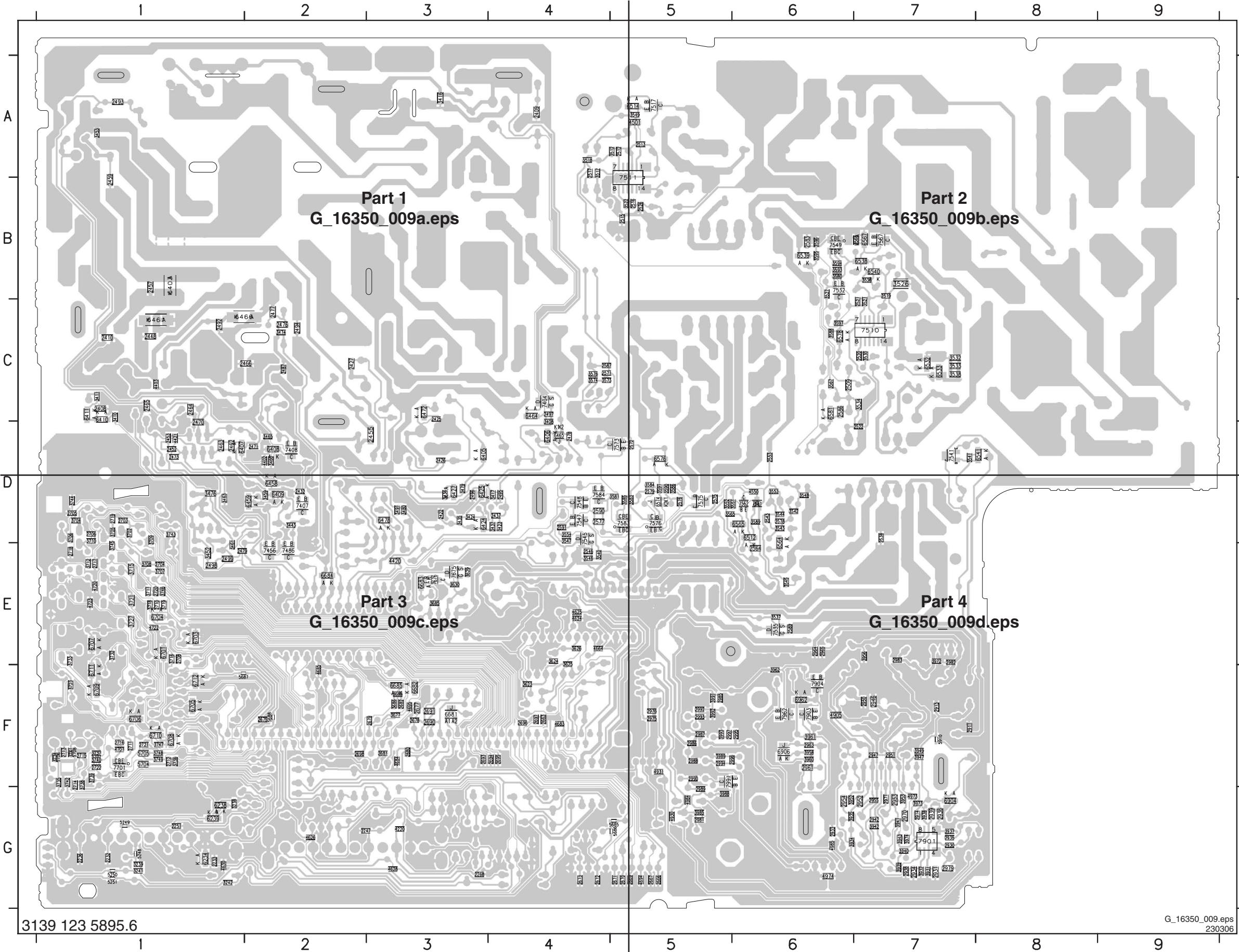
Layout LSP (Top Side)

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 1241 G9 1401 E7 1504 D1 1550 C3 1624 E6 1669 F8 1904 F5 2404 C7 2416 B7 2454 A9 2489 C8 2506 B3 2528 B3 2544 C3 2580 C3 2991 E3 3414 B9 3432 B9 3462 C8 3473 C7 3493 C9 3505 A1 3515 B2 3531 C3 3579 D6 3687 E8 3724 F9 9410 C7 9671 F8
 1243 G8 1402 A9 1505 A1 1551 D3 1625 E6 1670 F7 1910 G4 2405 A9 2417 B7 2456 C8 2490 D8 2507 A4 2535 E4 2551 C5 2582 D4 2948 F3 2998 E5 3415 A8 3433 C8 3466 D9 3477 C8 3494 A9 3507 C2 3516 A5 3535 B2 3587 D4 3701 D9 3725 F9 9411 A7 9672 F7
 1244 G7 1404 B6 1506 E4 1552 D3 1629 F8 1683 G6 1911 G4 2406 A8 2418 B7 2458 C8 2500 A1 2508 C1 2536 E4 2552 C5 2584 E4 2949 F4 3240 G9 3418 D7 3434 D7 3467 D9 3485 C8 3495 A9 3508 B2 3519 B4 3536 C3 3592 C3 3703 D9 3726 F9 9412 C7 9673 E6
 1245 G7 1452 D9 1532 B2 1607 F9 1630 E8 1684 F9 2235 G9 2419 B7 2460 A9 2501 D1 2510 C3 2537 E3 2561 C4 2674 F3 2957 F3 3401 E7 3419 B9 3450 C9 3468 E9 3486 C9 3499 C7 3509 D2 3520 B5 3550 A6 3598 B3 3713 F9 3728 F9 9413 D8 9674 E6
 1246 G9 1500 A2 1538 E5 1608 F9 1631 E8 1701 E9 2248 G8 2411 A7 2431 B7 2461 D9 2502 C1 2511 A5 2539 D4 2676 E7 2960 G3 3402 E7 3421 D6 3451 C9 3469 D8 3487 C9 3501 D1 3510 C2 3523 D2 3563 D4 3627 E8 3714 E9 3729 F9 9415 D8 9675 E6
 1268 G6 1501 A2 1542 C4 1620 G5 1635 E9 1705 F9 2250 F9 2412 A7 2449 C9 2468 C9 2503 A4 2514 A5 2540 D3 2563 D5 2688 E7 2977 F3 3403 C6 3422 D7 3455 A9 3470 D9 3488 D6 3502 A2 3511 B5 3524 C4 3566 E5 3628 E8 3717 E9 3730 F9 9417 C9 9677 F6
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 3930 G3 9423 E8 9681 F6
 3933 G4 9424 D7 9682 G5
 3936 G2 9425 D8 9683 F6
 3955 G3 9426 D8 9685 F6
 3957 F4 9427 D7 9686 E6
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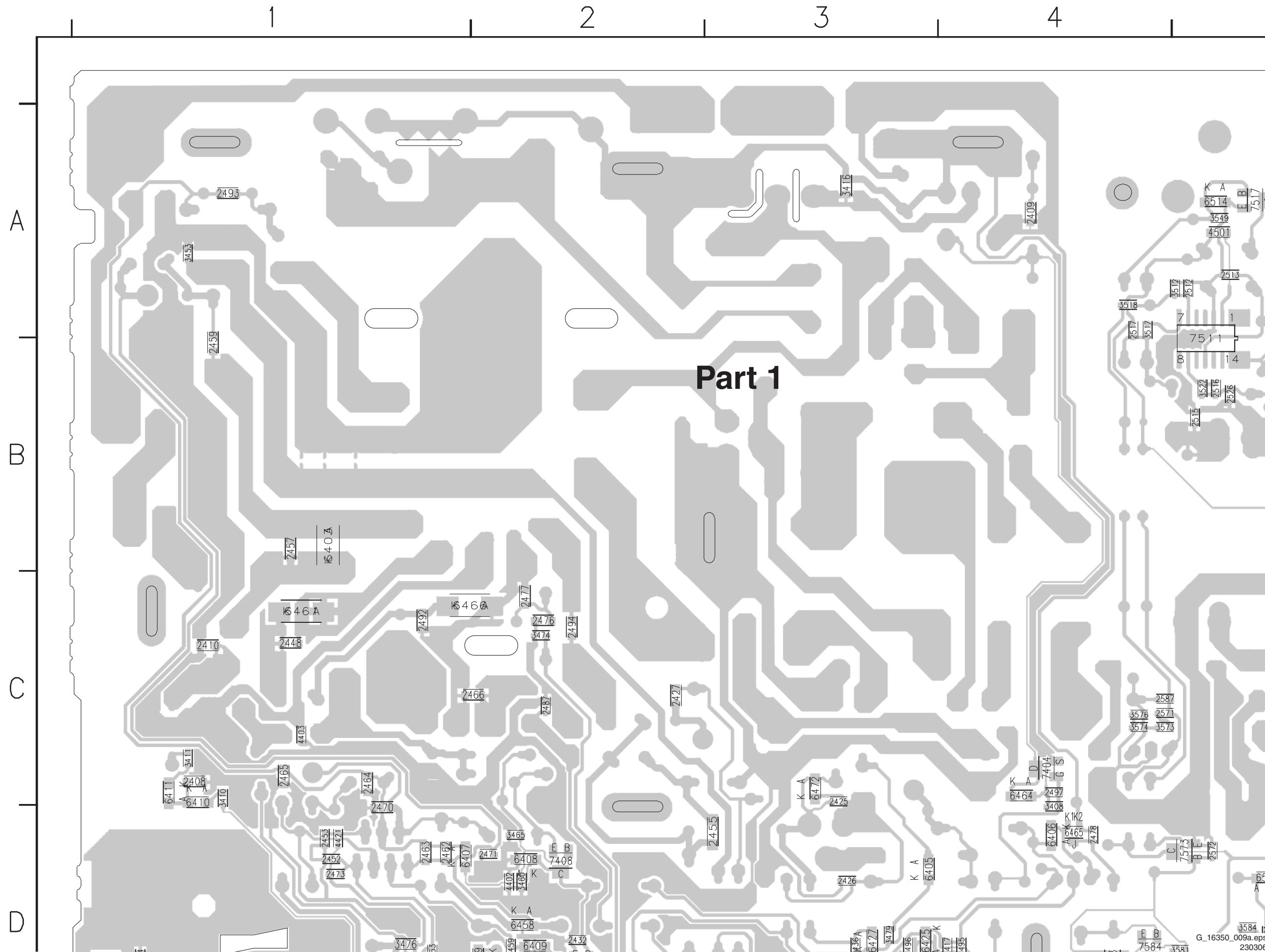
Layout LSP (Overview Bottom Side)

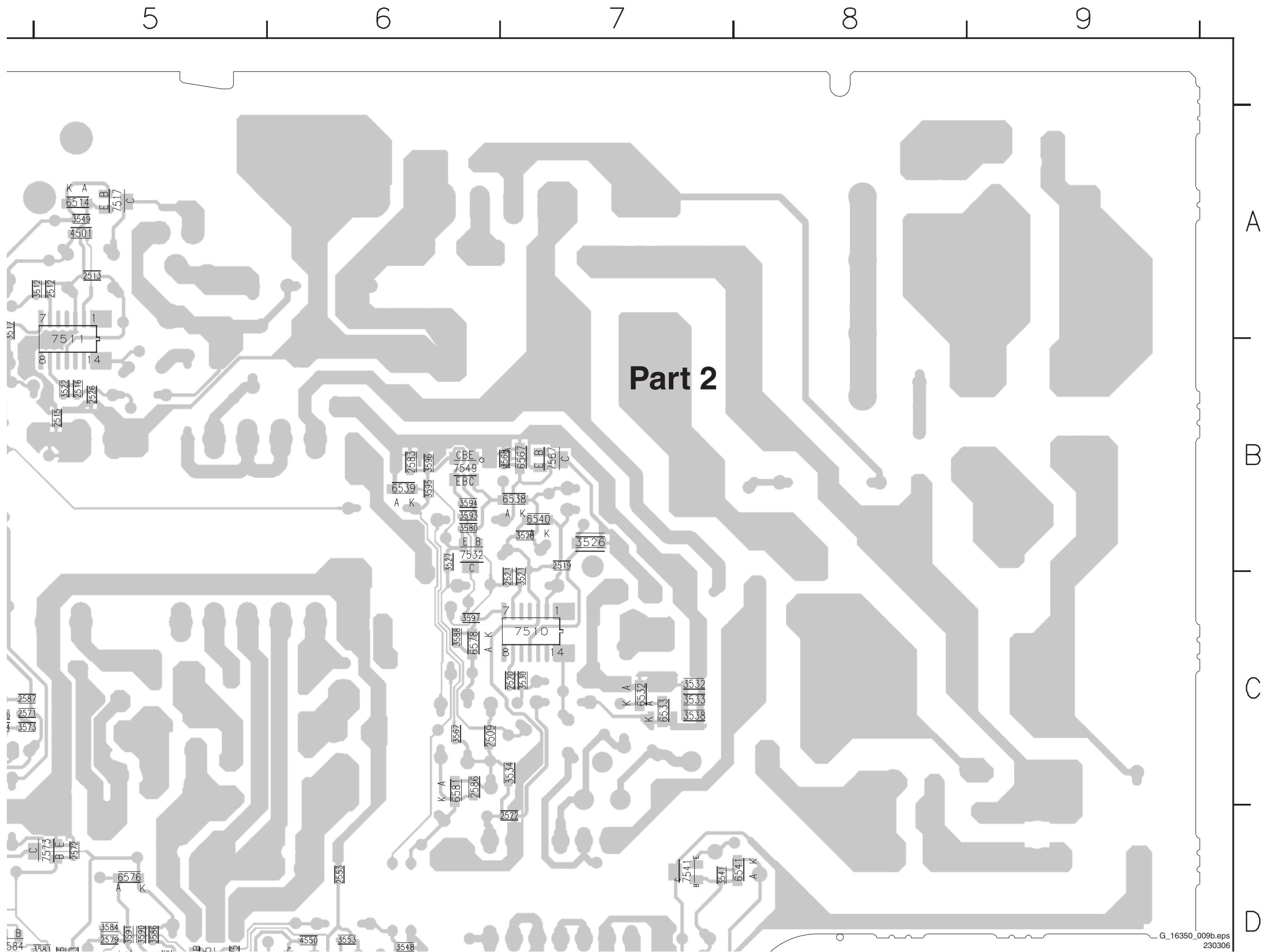
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2233 G1	2409 A4	2432 D2	2457 B1	2466 C2	2478 D4	2495 D4	2512 A5	2520 C7	2546 E4	2576 D5	2587 C4	2667 G5	2675 F2	2682 F3	2696 F4	2706 D1	2714 F1	2720 E1	2930 G7	2938 G7	2951 F7	2963 F6	2976 F5	2986 F5	2993 F5	3239 G1	3416 A3	3437 D4	3740 F1	6461 C1
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2268 G3	2426 D3	2453 D1	2464 C1	2476 C2	2493 A1	2499 E1	2517 A4	2538 D7	2572 D5	2583 B6	2663 F4	2672 G4	2680 F3	2694 F4	2704 E1	2712 F1	2718 E1	2913 F7	2935 G7	2947 F7	2955 G7	2971 G7	2983 E7	2990 F5	2997 F5	3410 C1	3435 D3	3459 D2	3744 D1	6472 C3



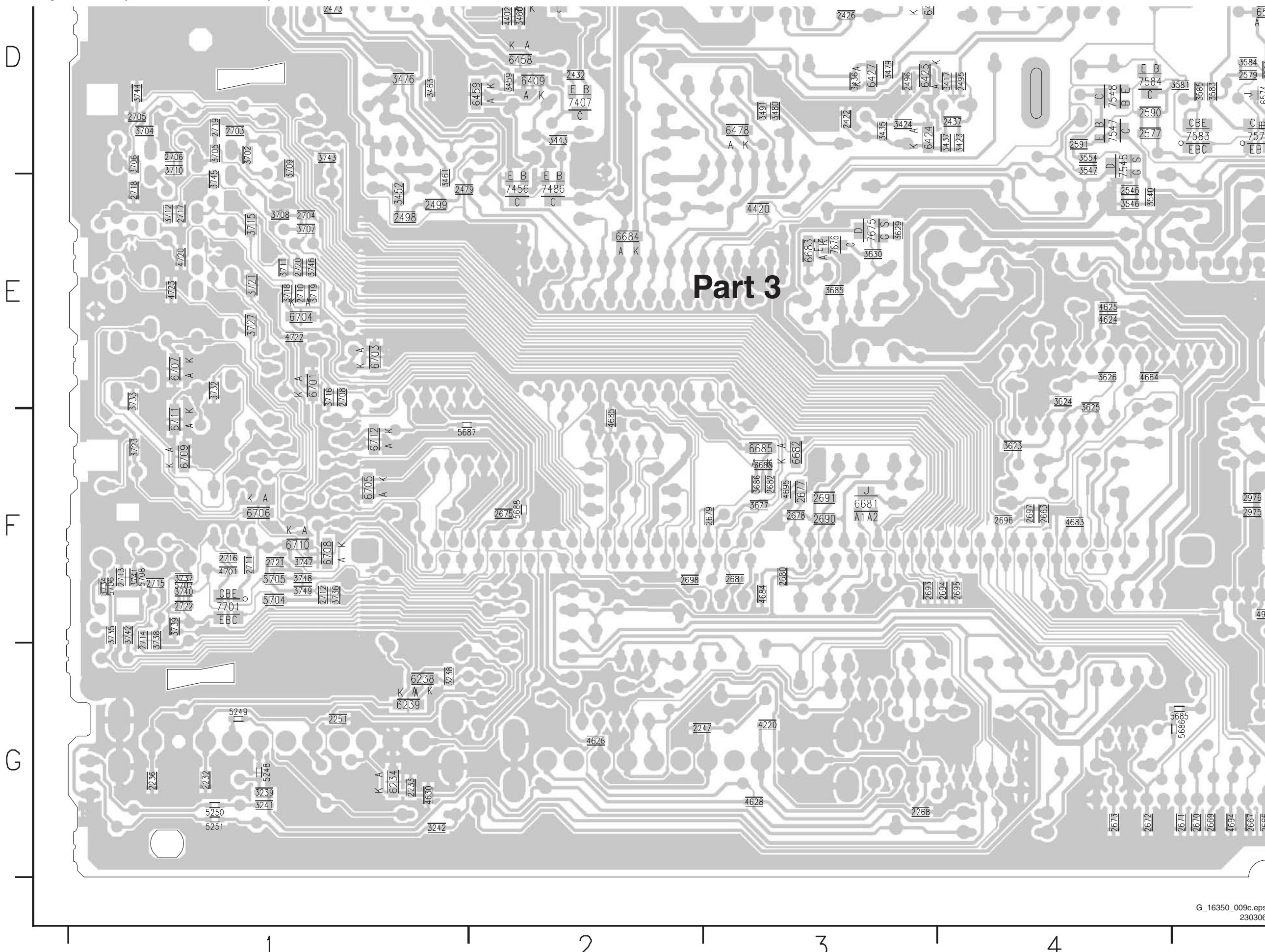
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Layout LSP (Part 1 Bottom Side)

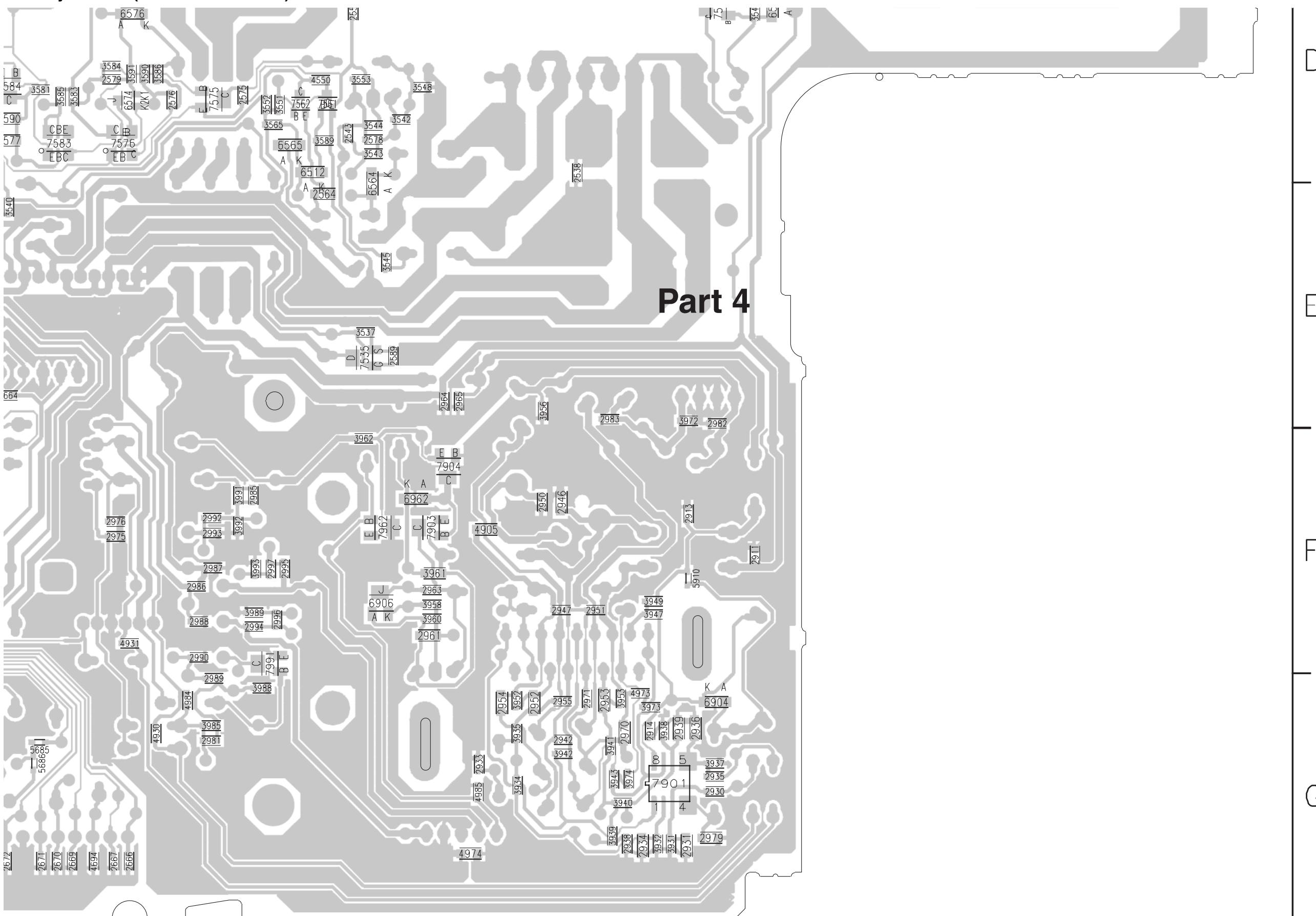


Layout LSP (Part 2 Bottom Side)

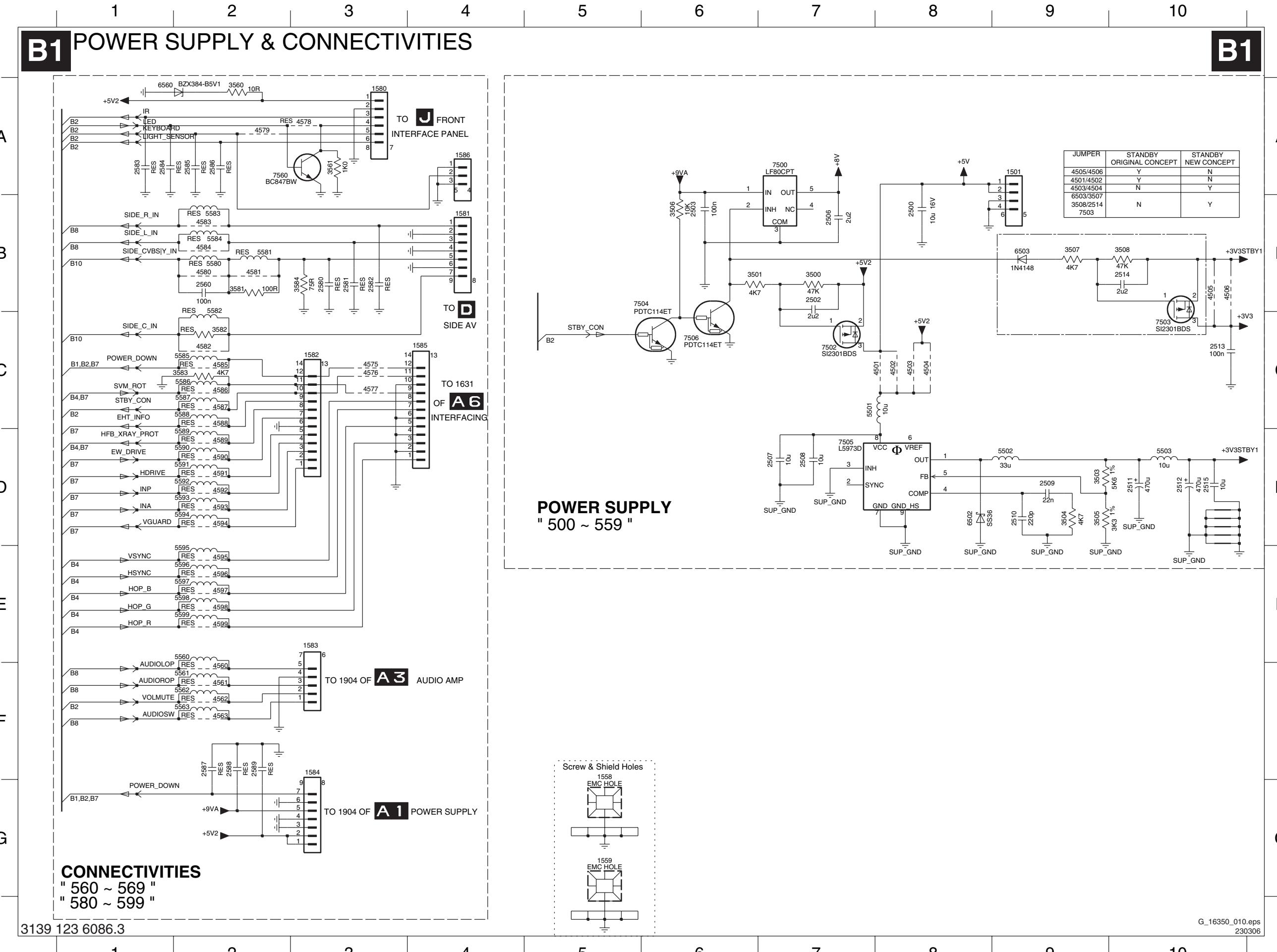
Layout LSP (Part 3 Bottom Side)



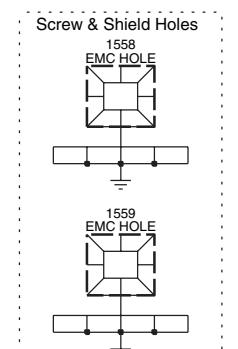
Layout LSP (Part 4 Bottom Side)



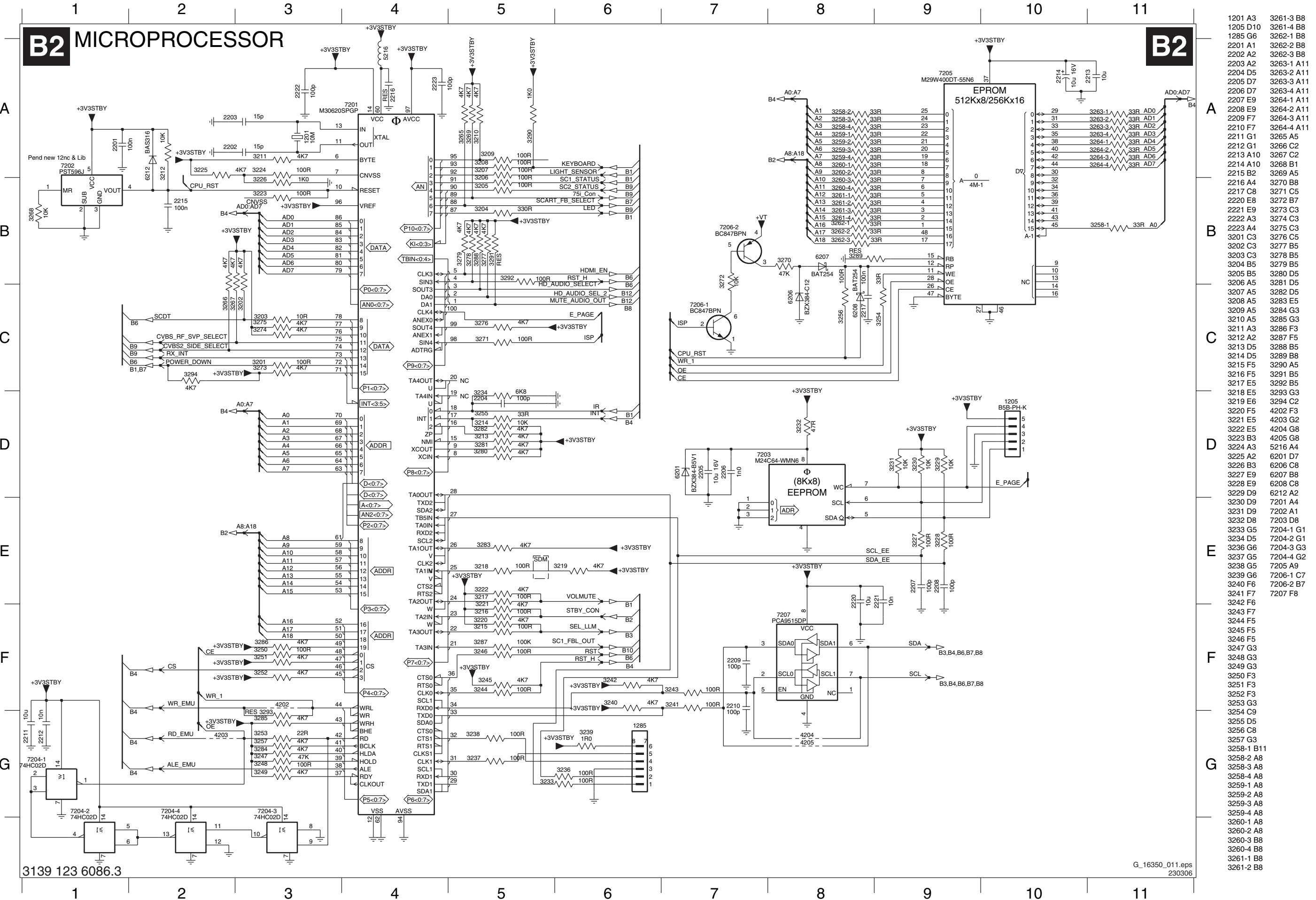
SSB: Power Supply & Connectivities



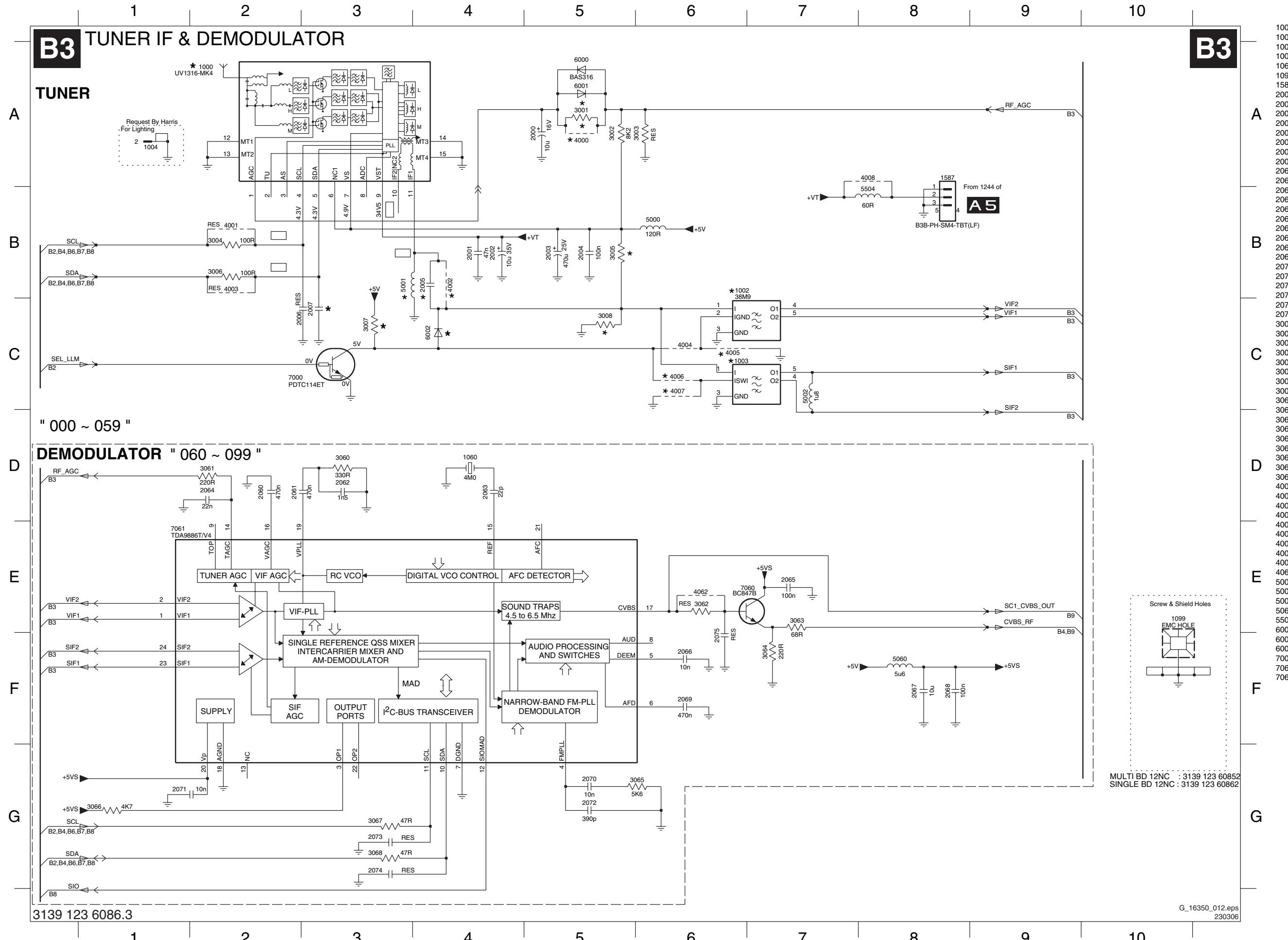
1501 A9	5582 C2
1558 F5	5583 B2
1559 G5	5584 B2
1580 A3	5585 C2
1581 B4	5586 C2
1582 C3	5587 C2
1583 E3	5588 C2
1584 F3	5589 D2
1585 C4	5590 D2
1586 A4	5591 D2
2500 B8	5592 D2
2502 B7	5593 D2
2503 B6	5594 D2
2506 B7	5595 E2
2507 D7	5596 E2
2508 D7	5597 E2
2509 D9	5598 E2
2510 D9	5599 E2
2511 D10	6502 D8
2512 D10	6503 B9
2513 C10	6560 A1
2514 B10	7500 A7
2515 D10	7502 C7
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2580 B3	7504 B5
2581 B3	7505 D7
2582 B3	7506 C6
2583 A1	7560 A2



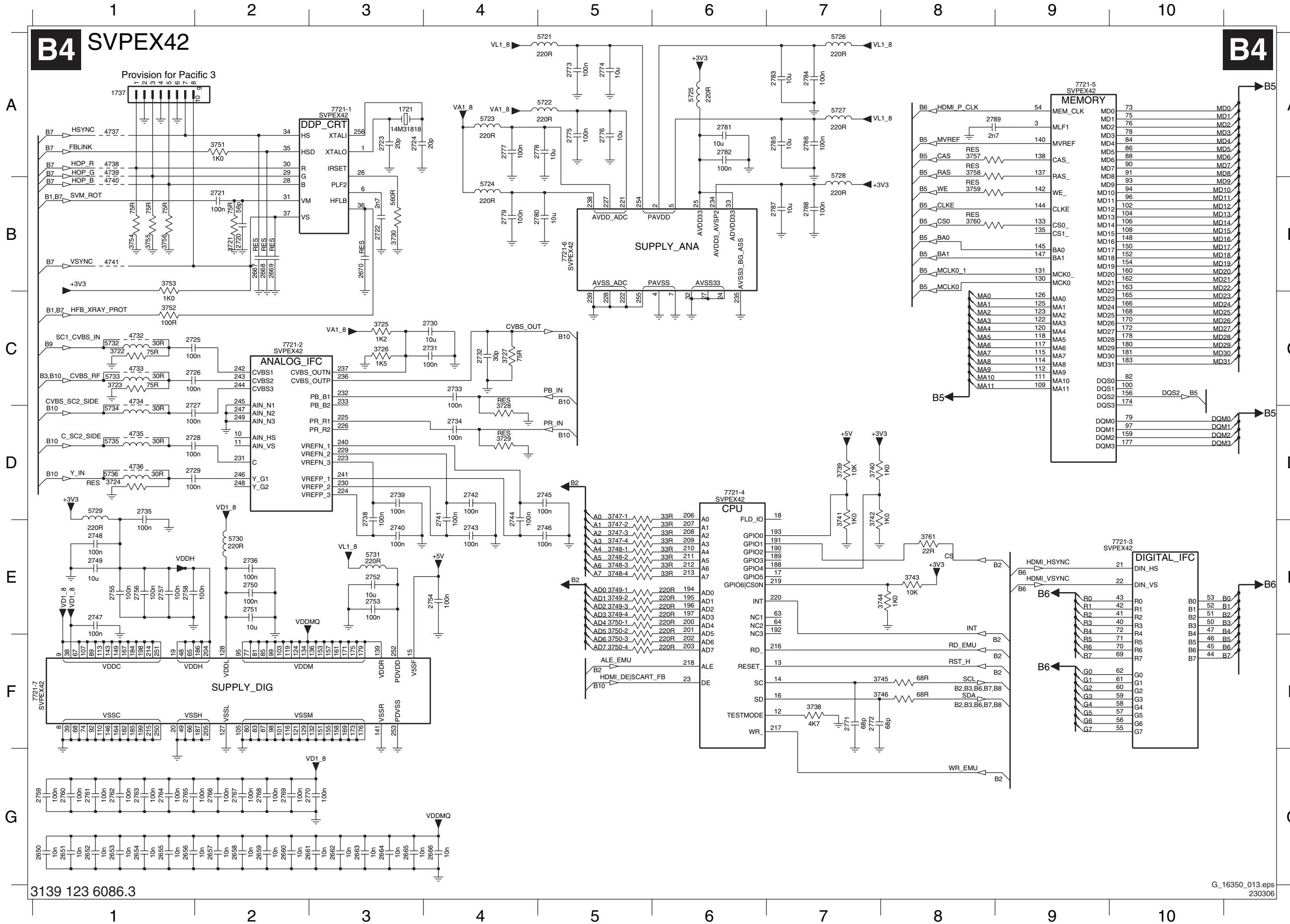
SSB: Micro Processor

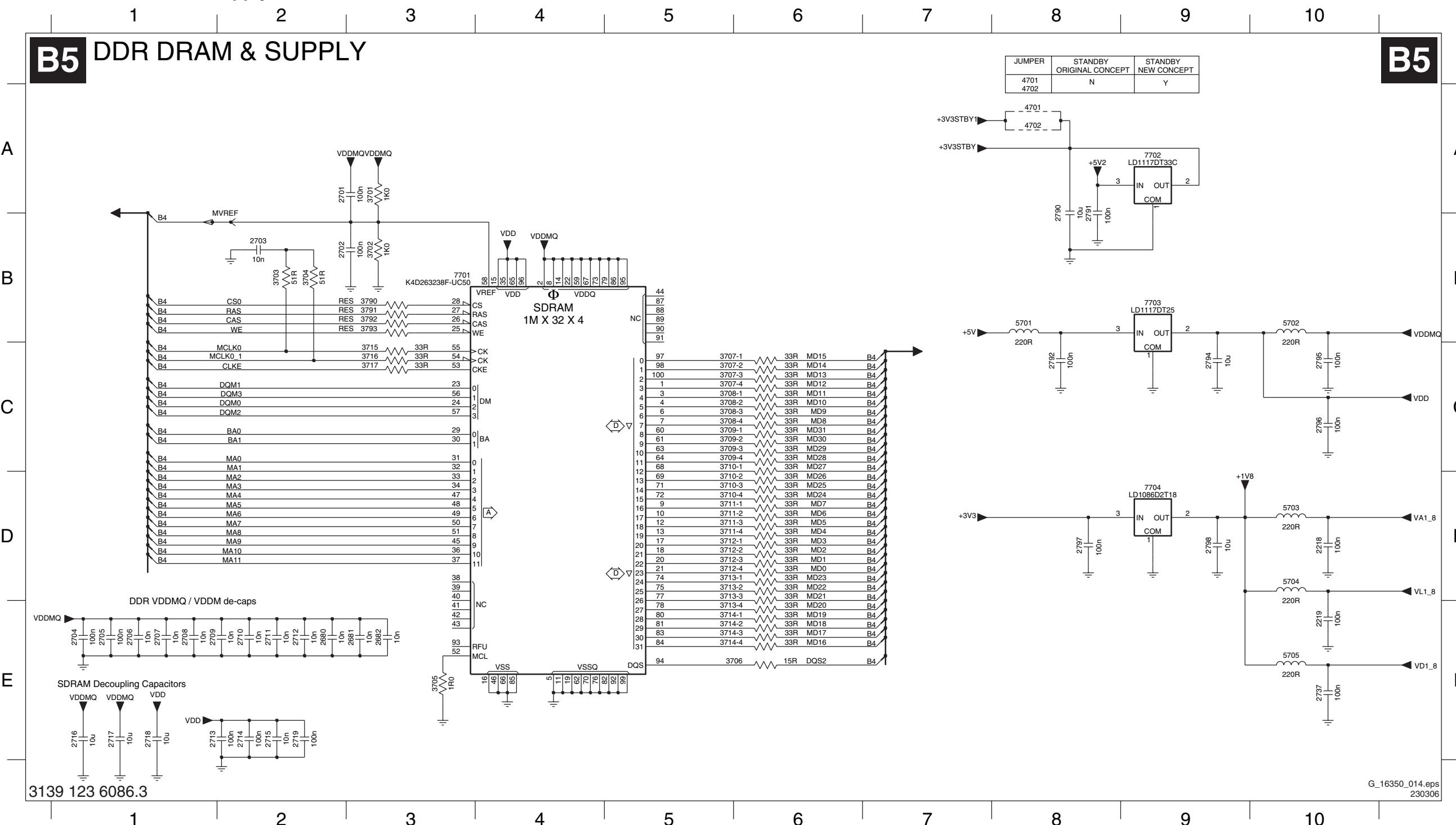


SSB: Tuner IF & Demodulator



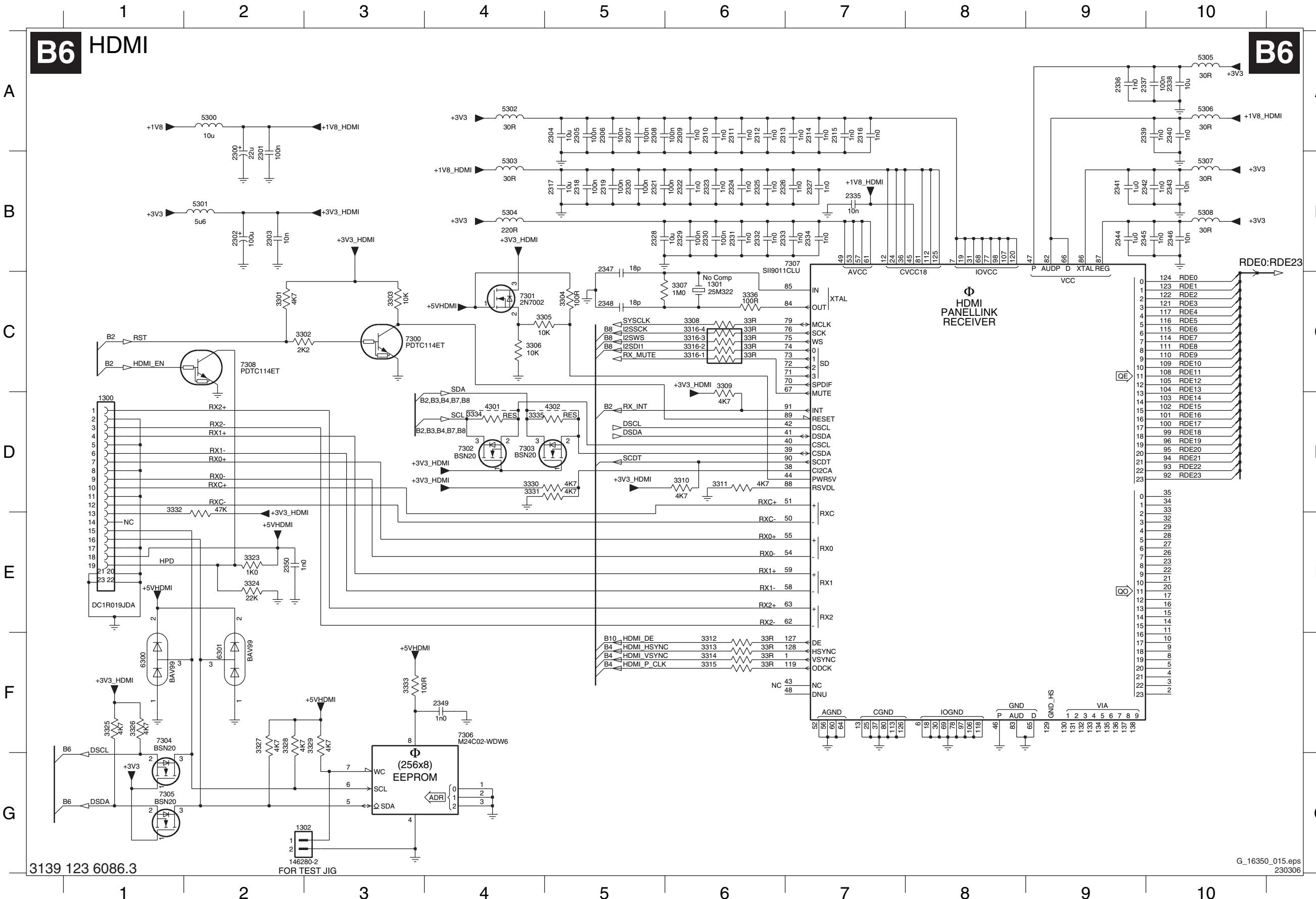
SSB: SVPEX42



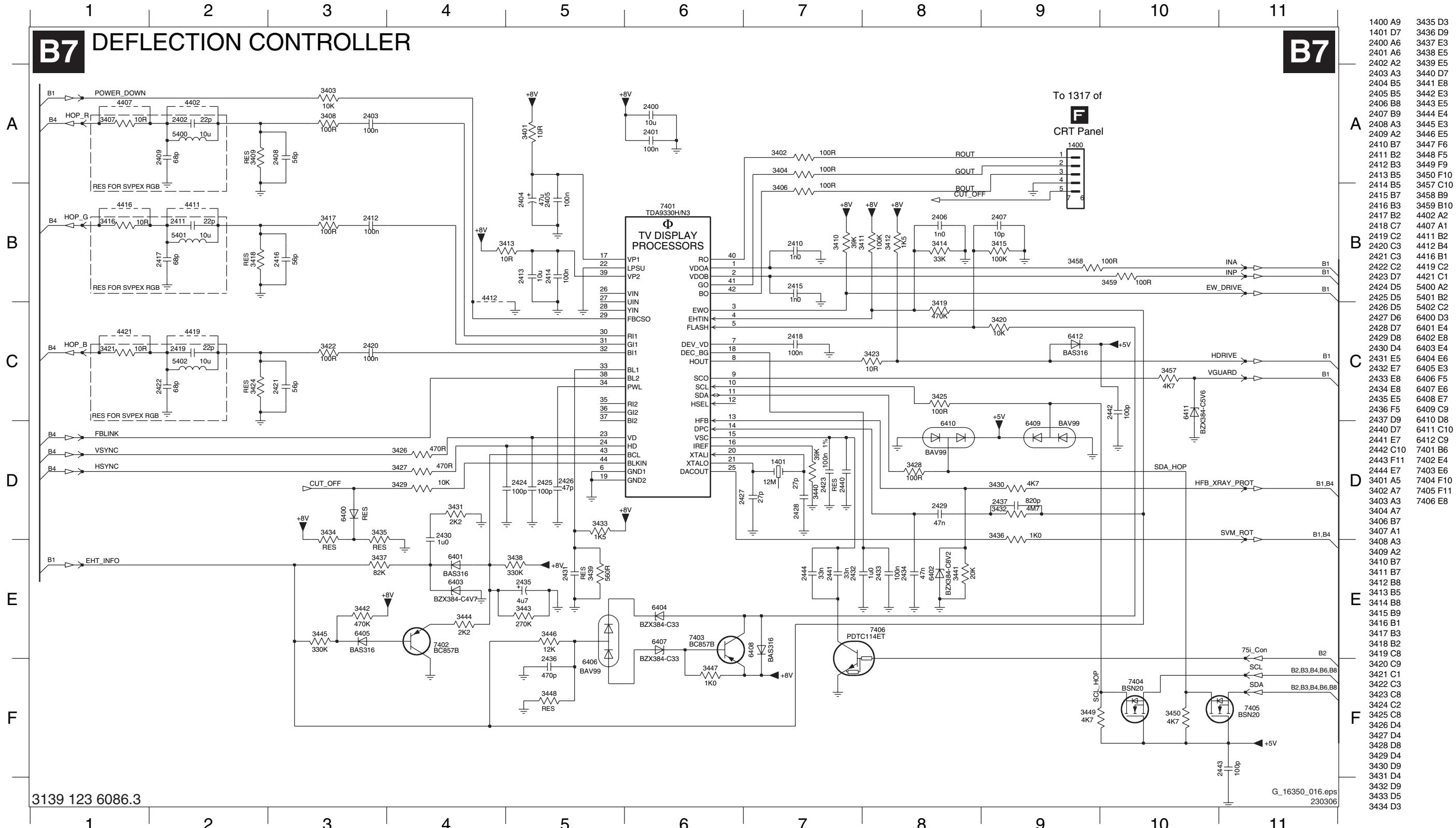
SSB: DDR DRAM & Supply

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2682 E3	3790 B3
2701 A2	3791 B3
2702 B2	3792 B3
2703 B2	3793 B3
2704 E1	4701 A8
2705 E1	4702 A8
2706 E1	5701 B8
2707 E1	5702 B10
2708 E1	5703 D10
2709 E1	5704 D10
2710 E2	5705 E10
2711 E2	7701 B3
2712 E2	7702 A9
2713 E1	7703 B9
2714 E2	7704 D9
2715 E2	
2716 E1	
2717 E1	
2718 E1	
2719 E2	
2737 E10	
2790 A8	
2791 A8	
2792 C8	
2794 C9	
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2797 D8	
2798 D9	
3701 A3	
3702 B3	
3703 B2	
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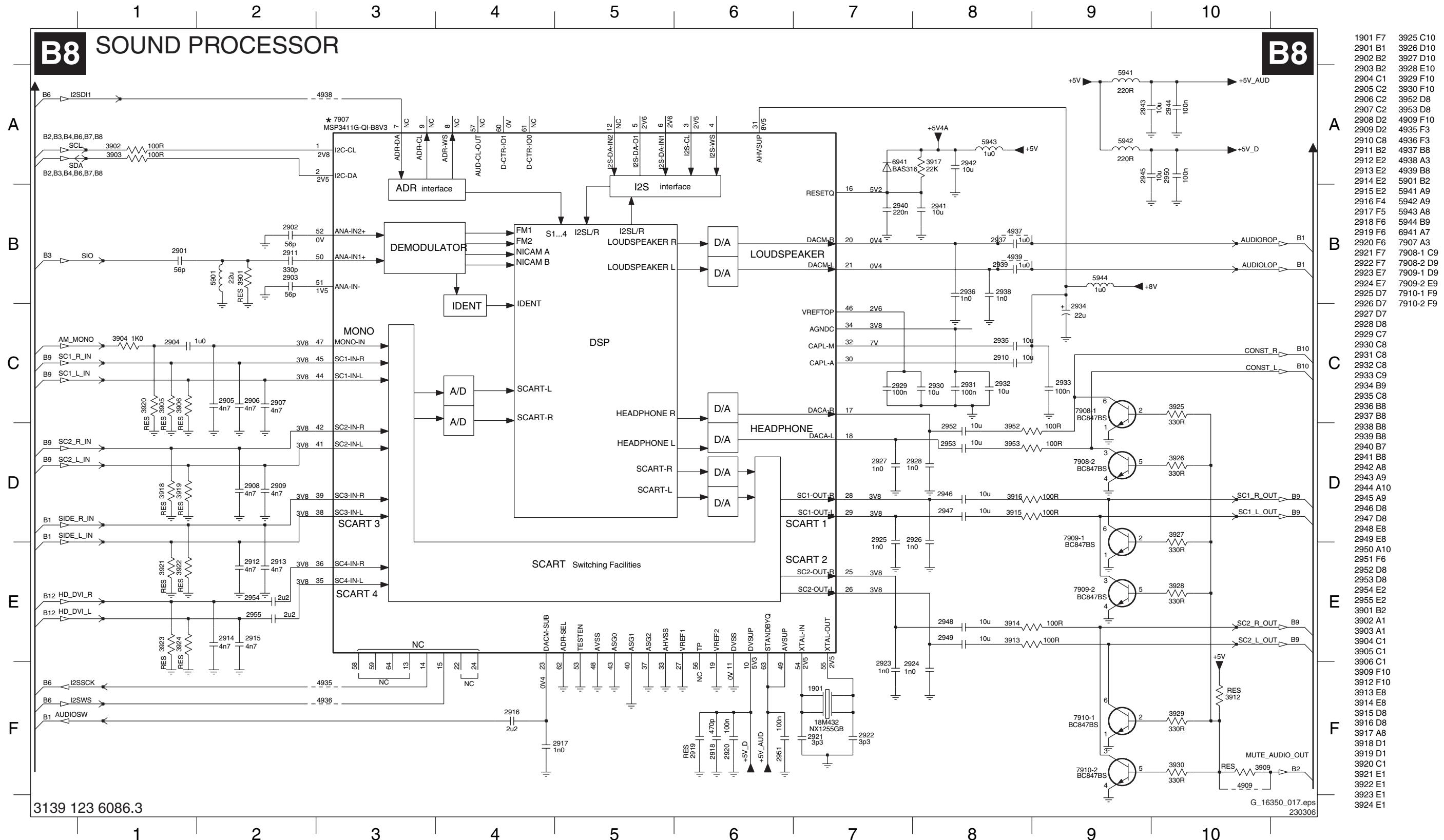
SSB: HDMI



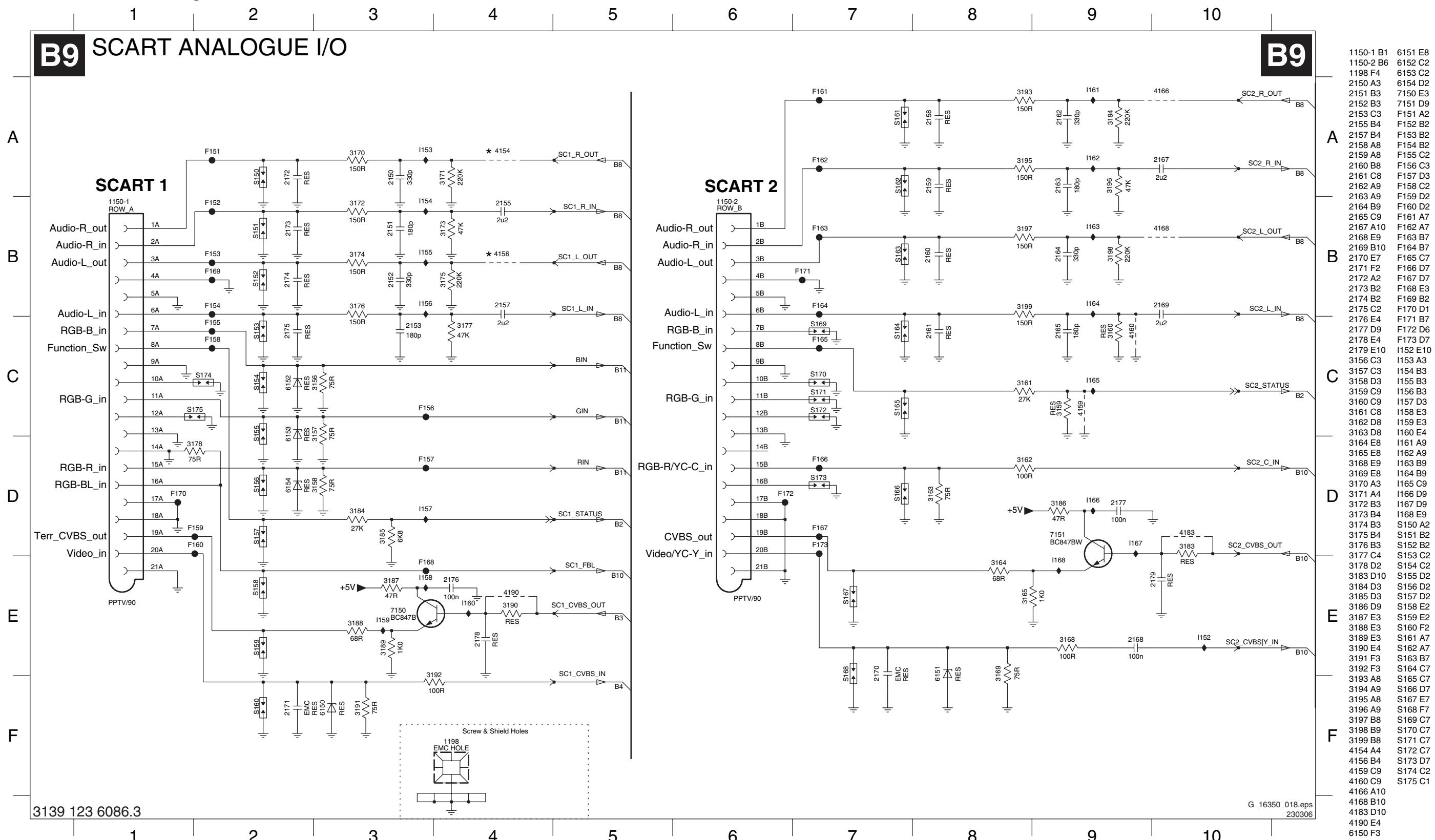
SSB: Deflection Controller



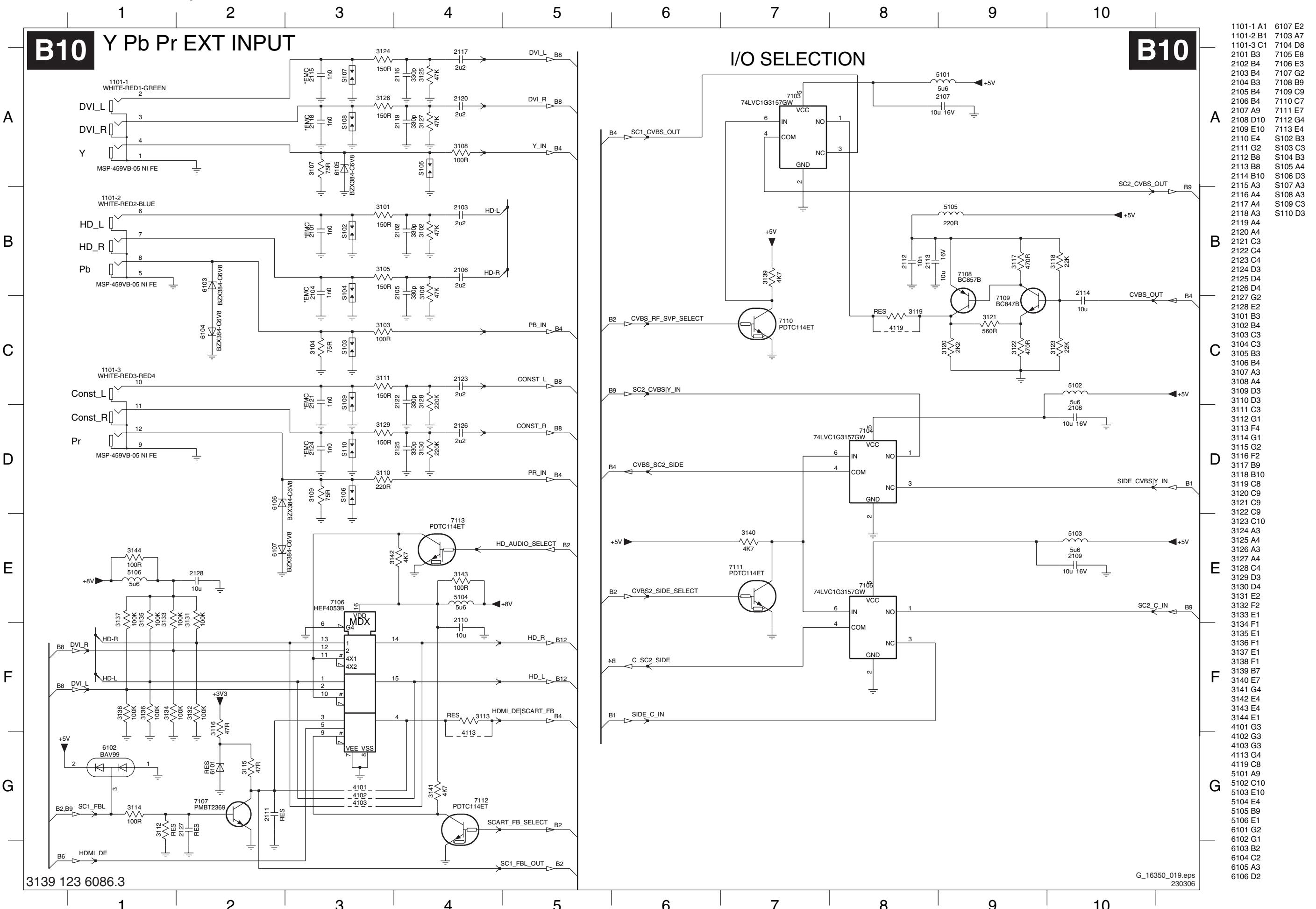
SSB: Sound Processor



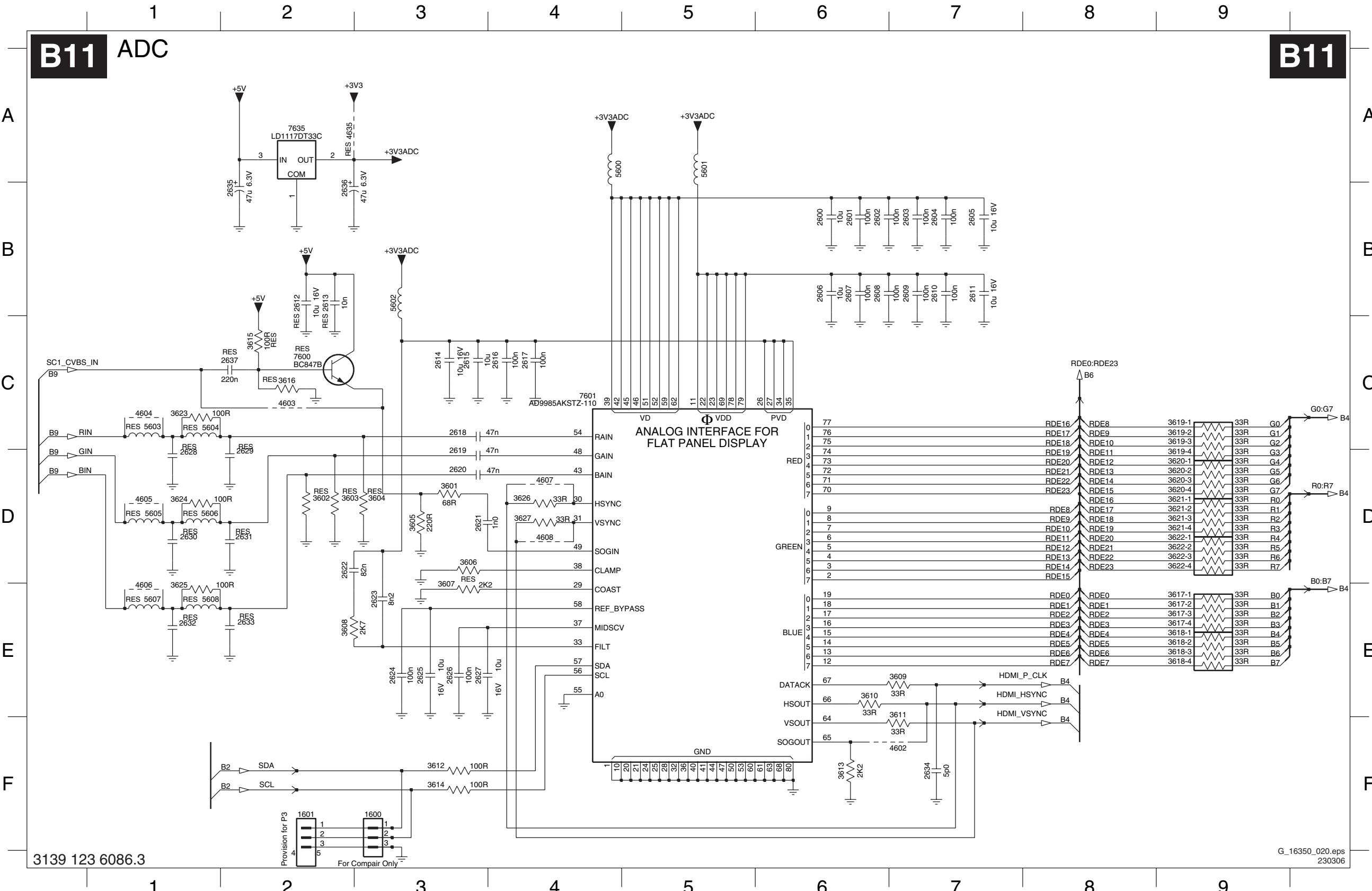
SSB: SCART Analogue I/O



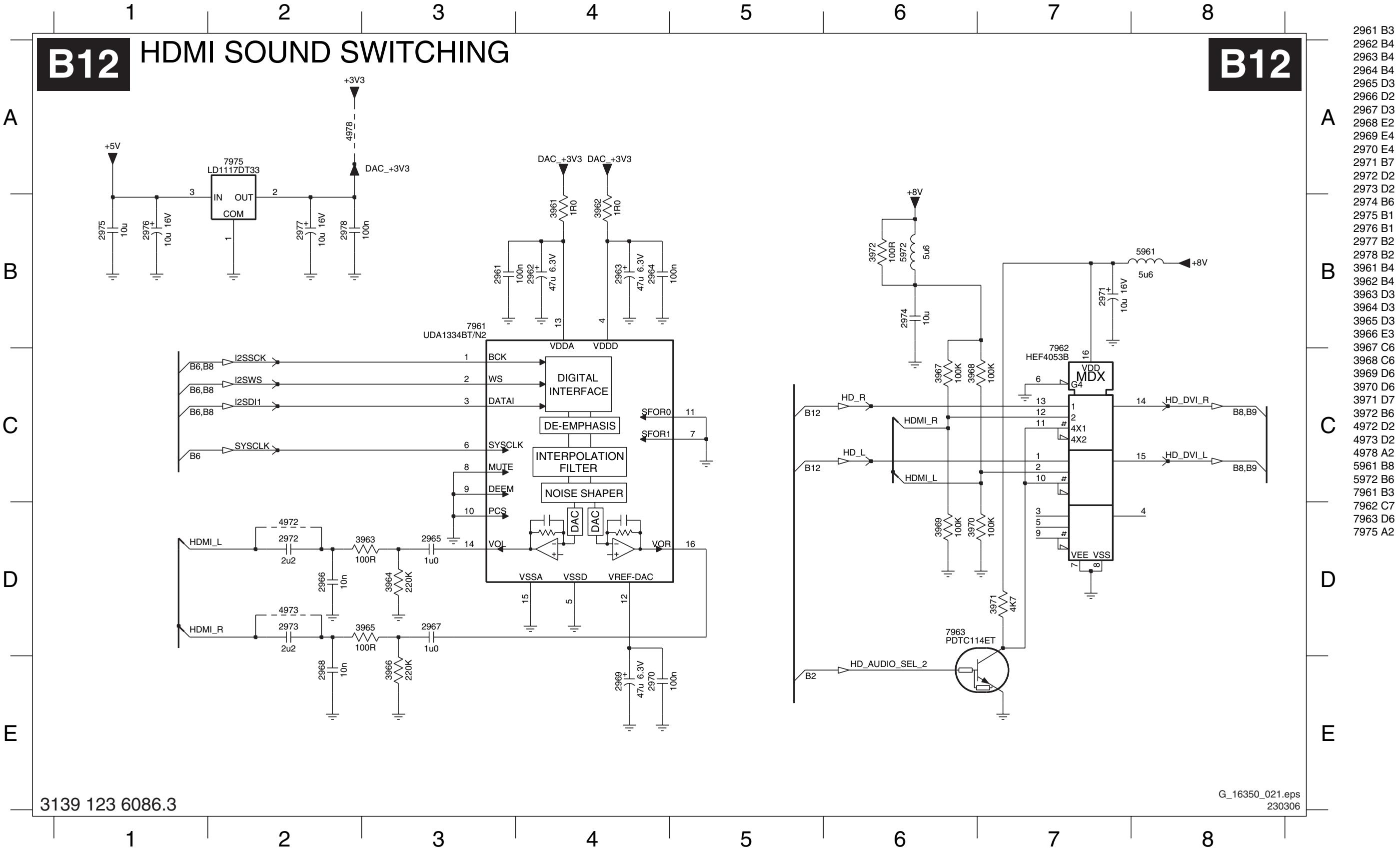
SSB: Y, Pb, Pr, Ext. Input



SSB: ADC

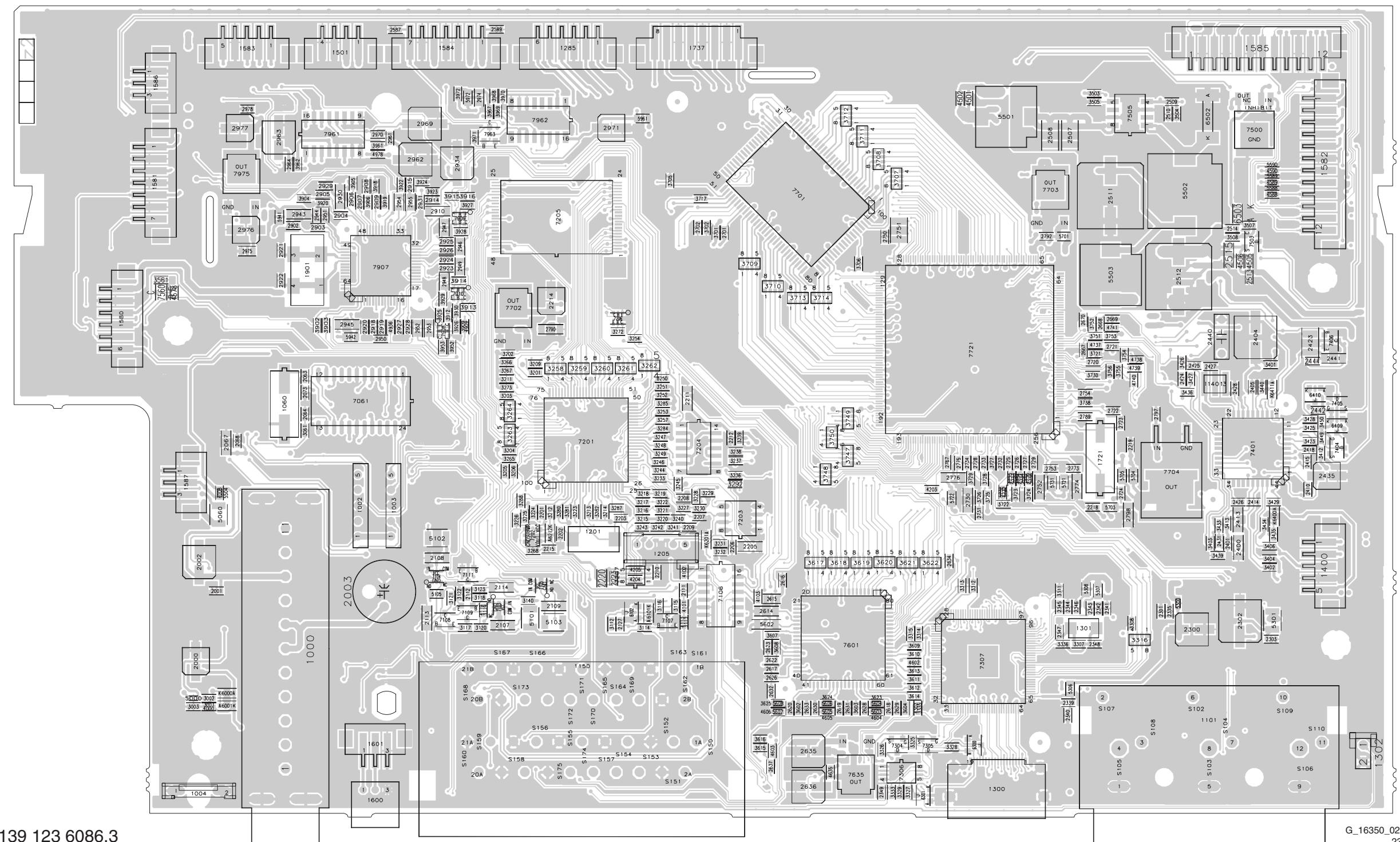


SSB: HDMI Sound Switching



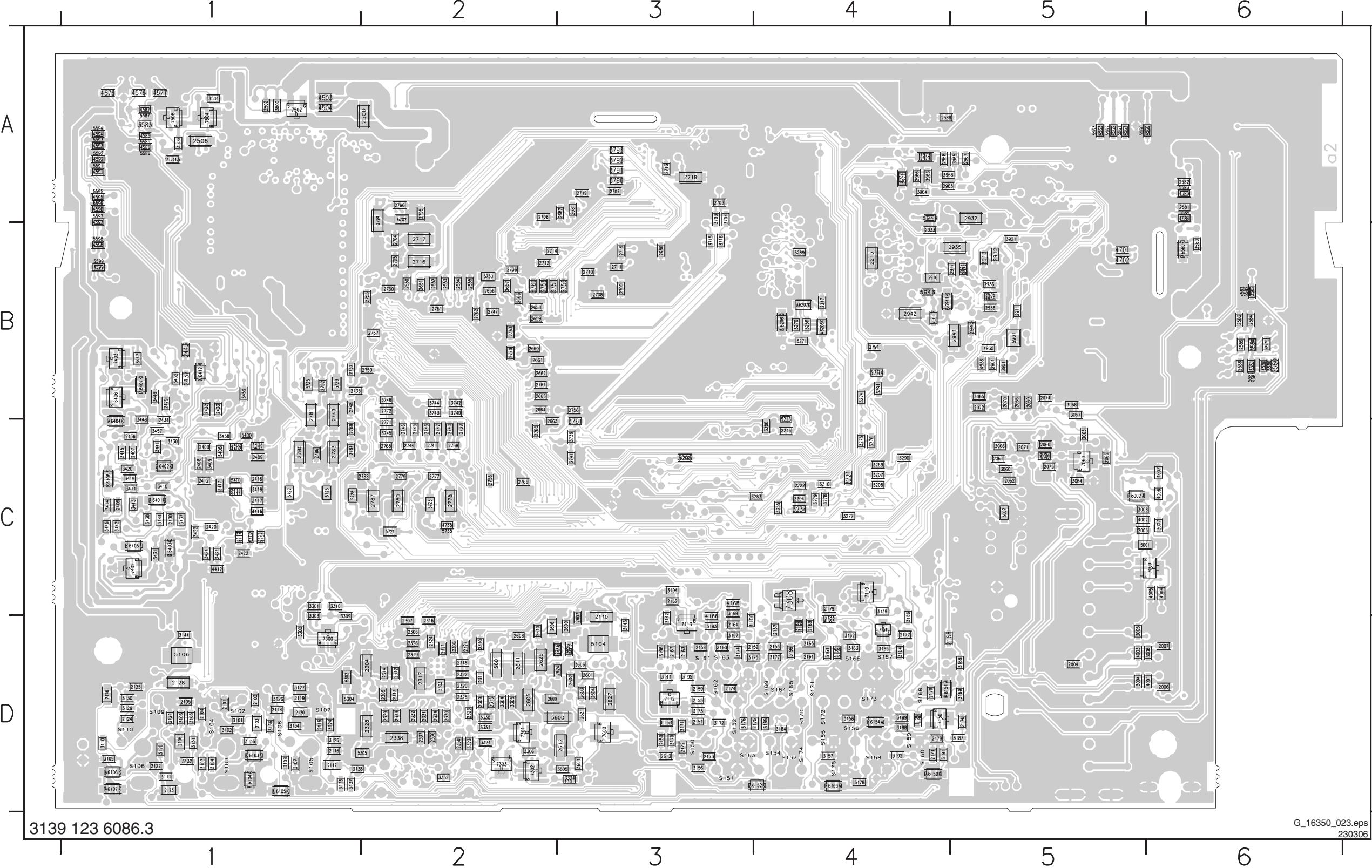
Layout SSB (Top Side)

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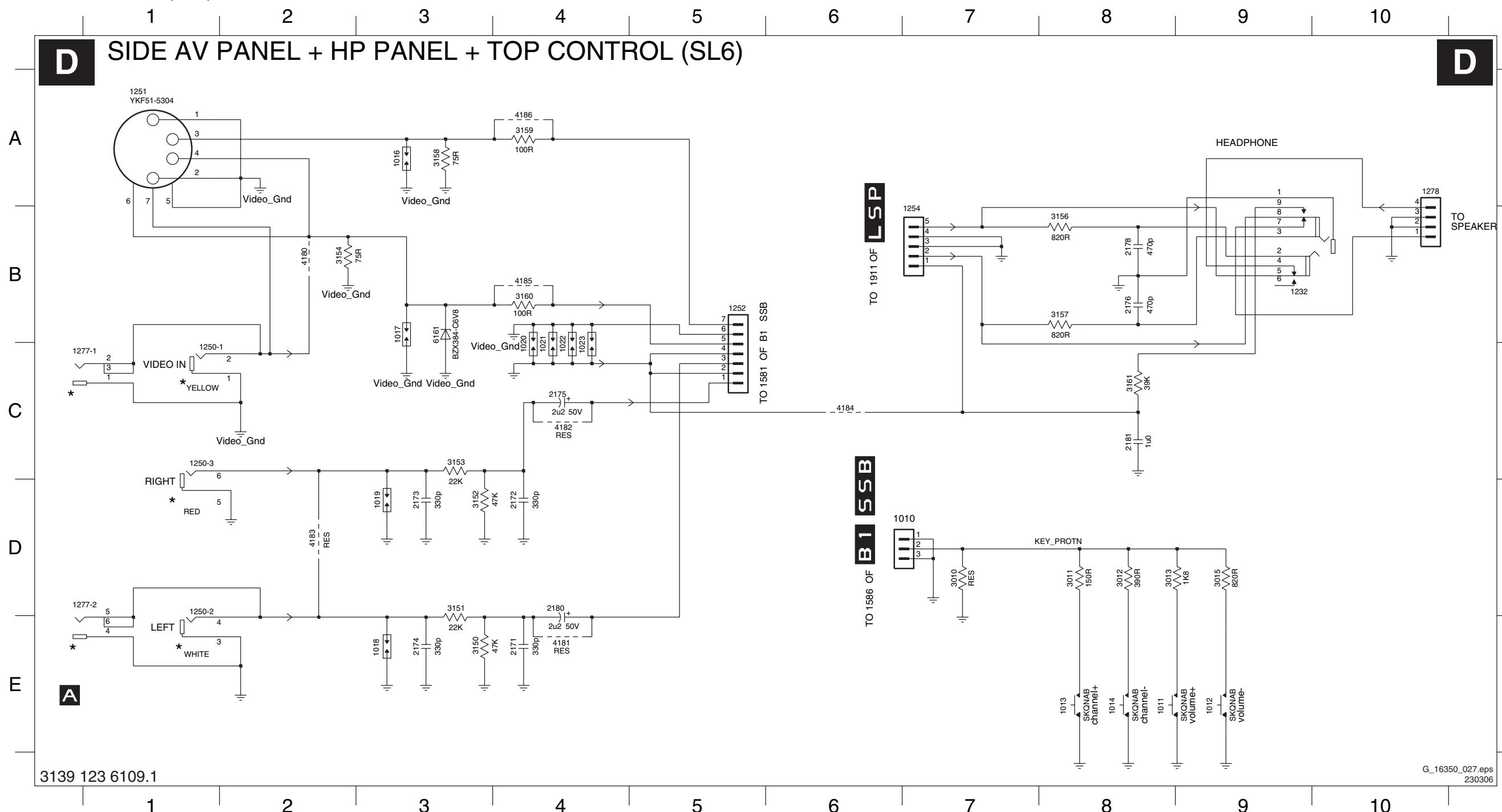


Layout SSB (Bottom Side)

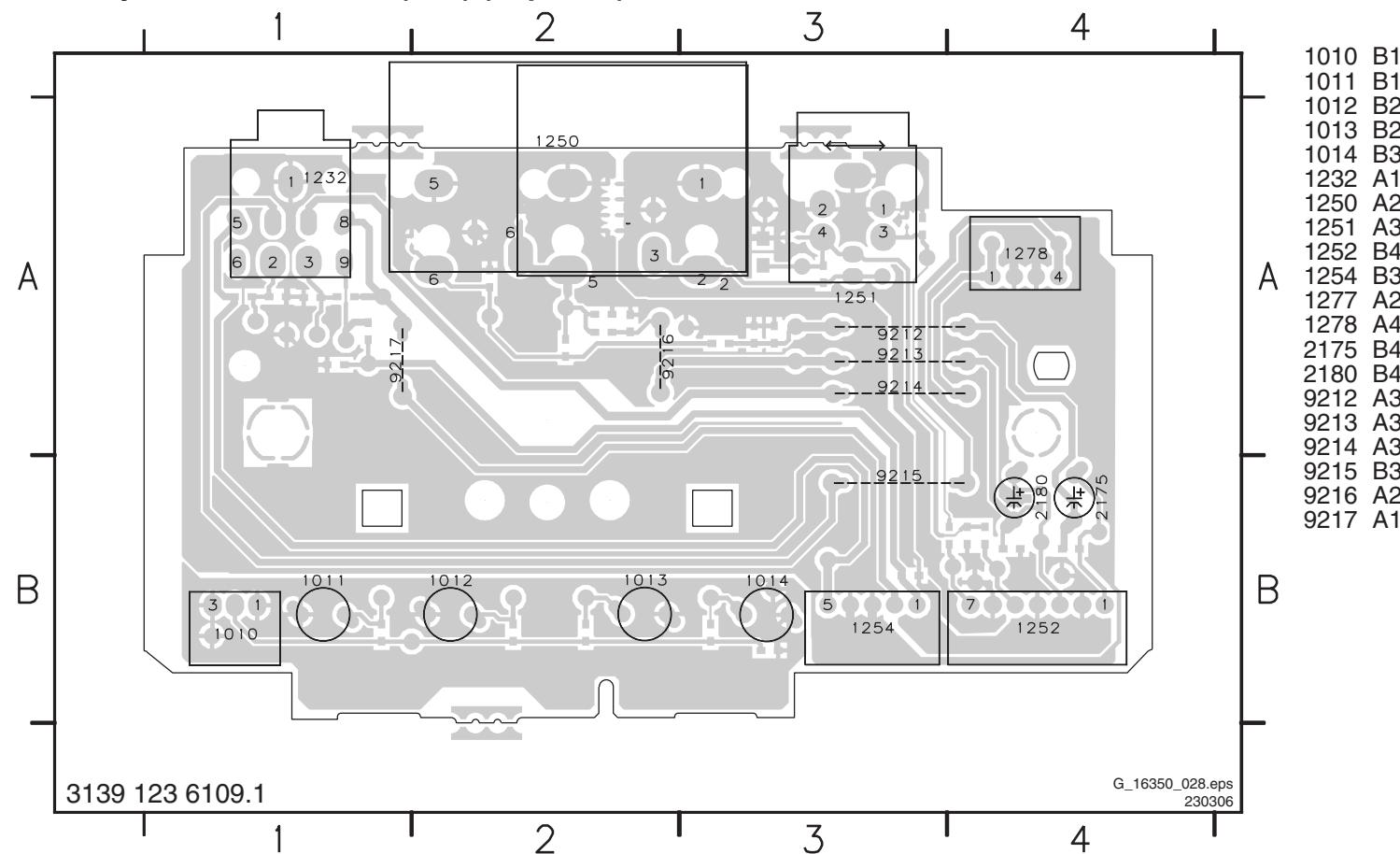
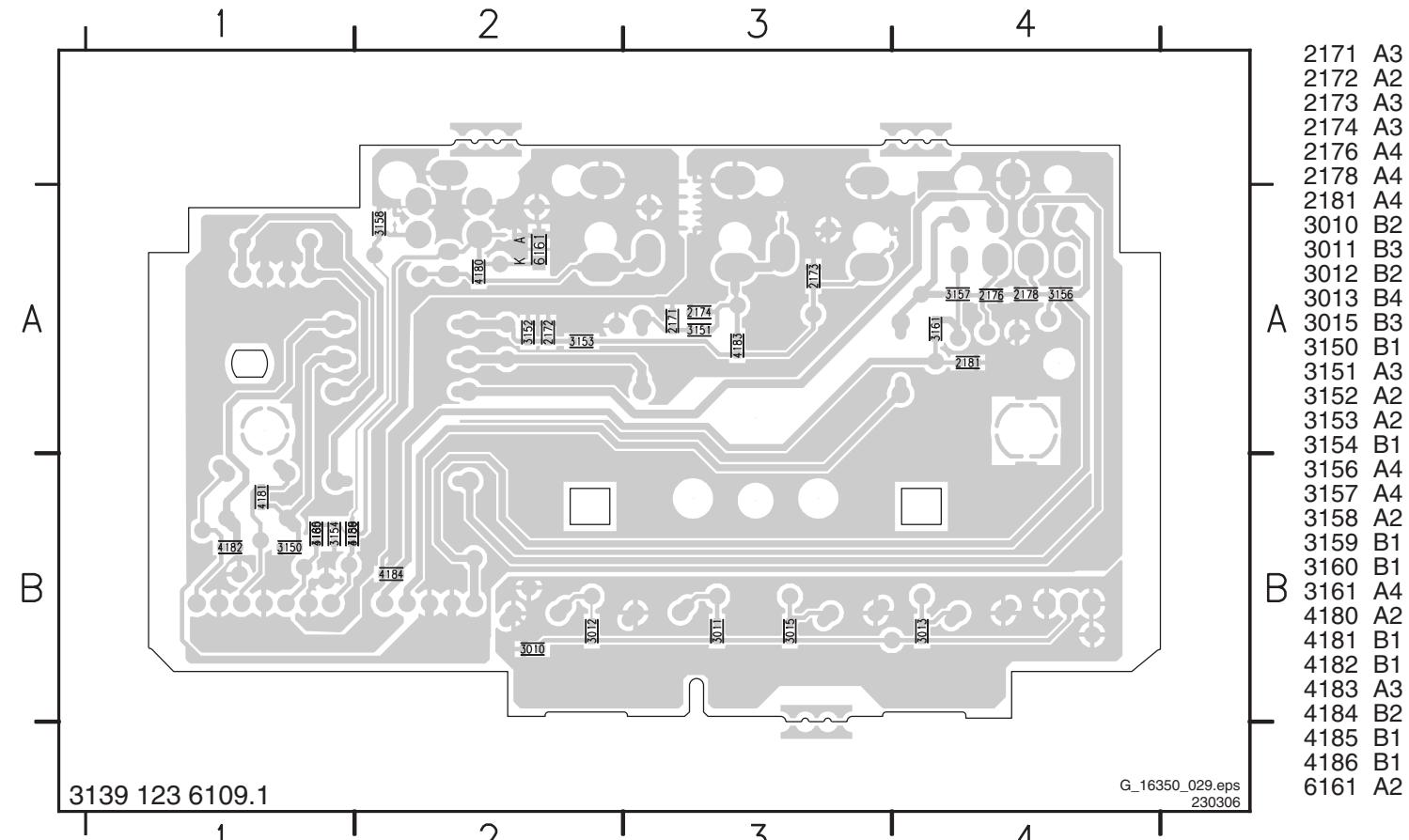
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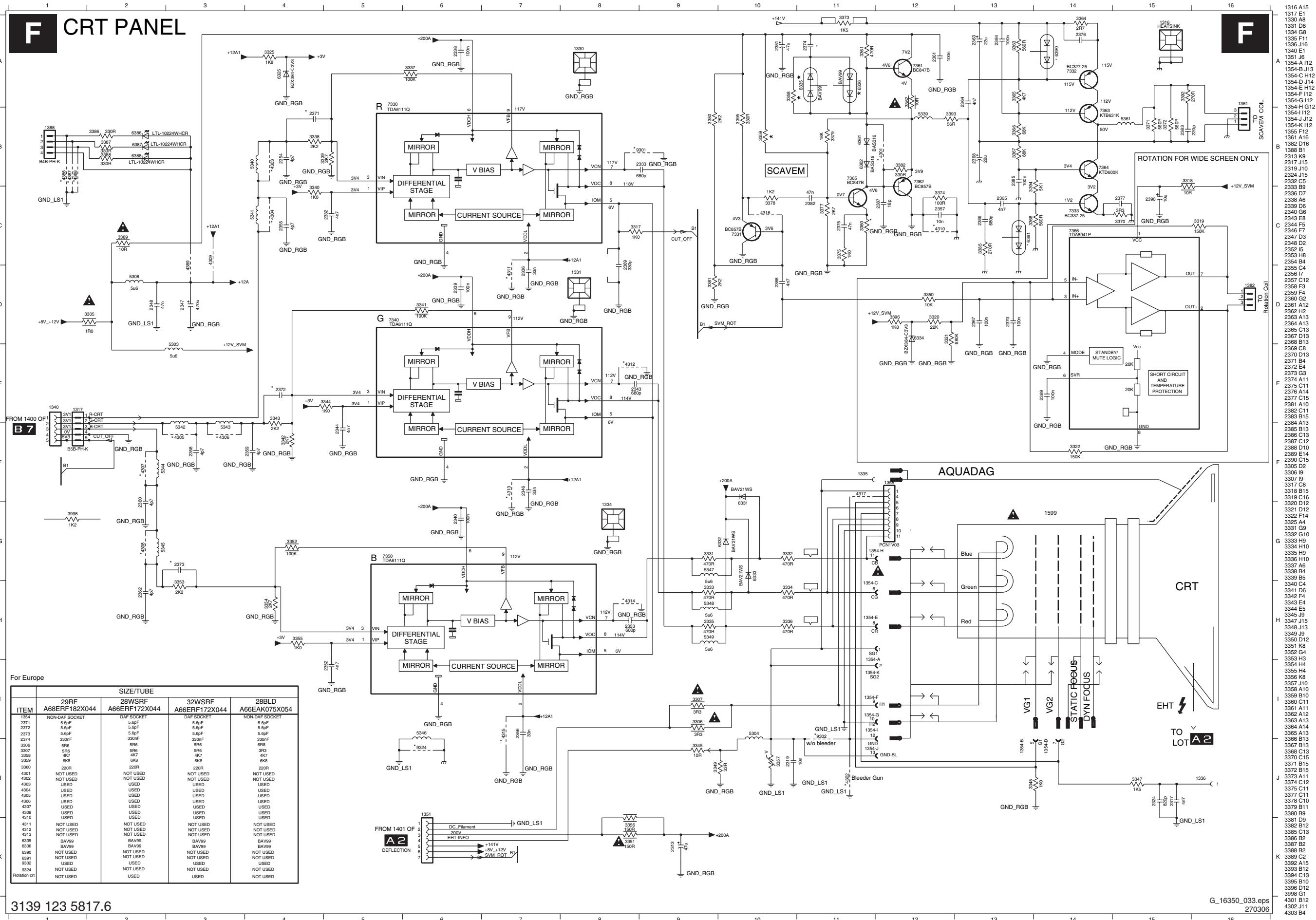
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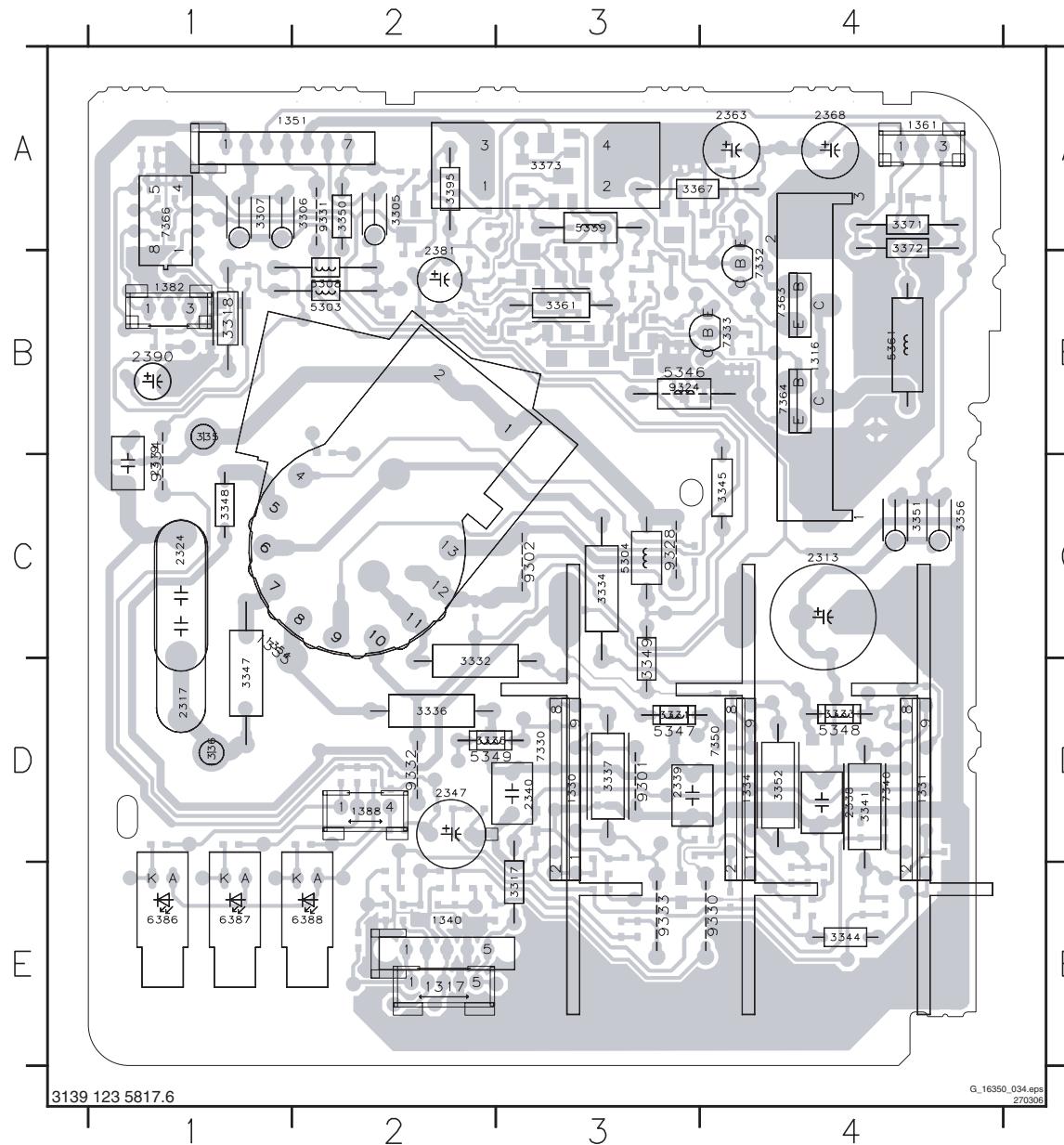
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3160 B4
3161 C8
4180 B2
4181 E4
4182 C4
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6161 B3

Layout Side I/O Panel (SL6) (Top Side)**Layout Side I/O Panel (SL6) (Bottom Side)**

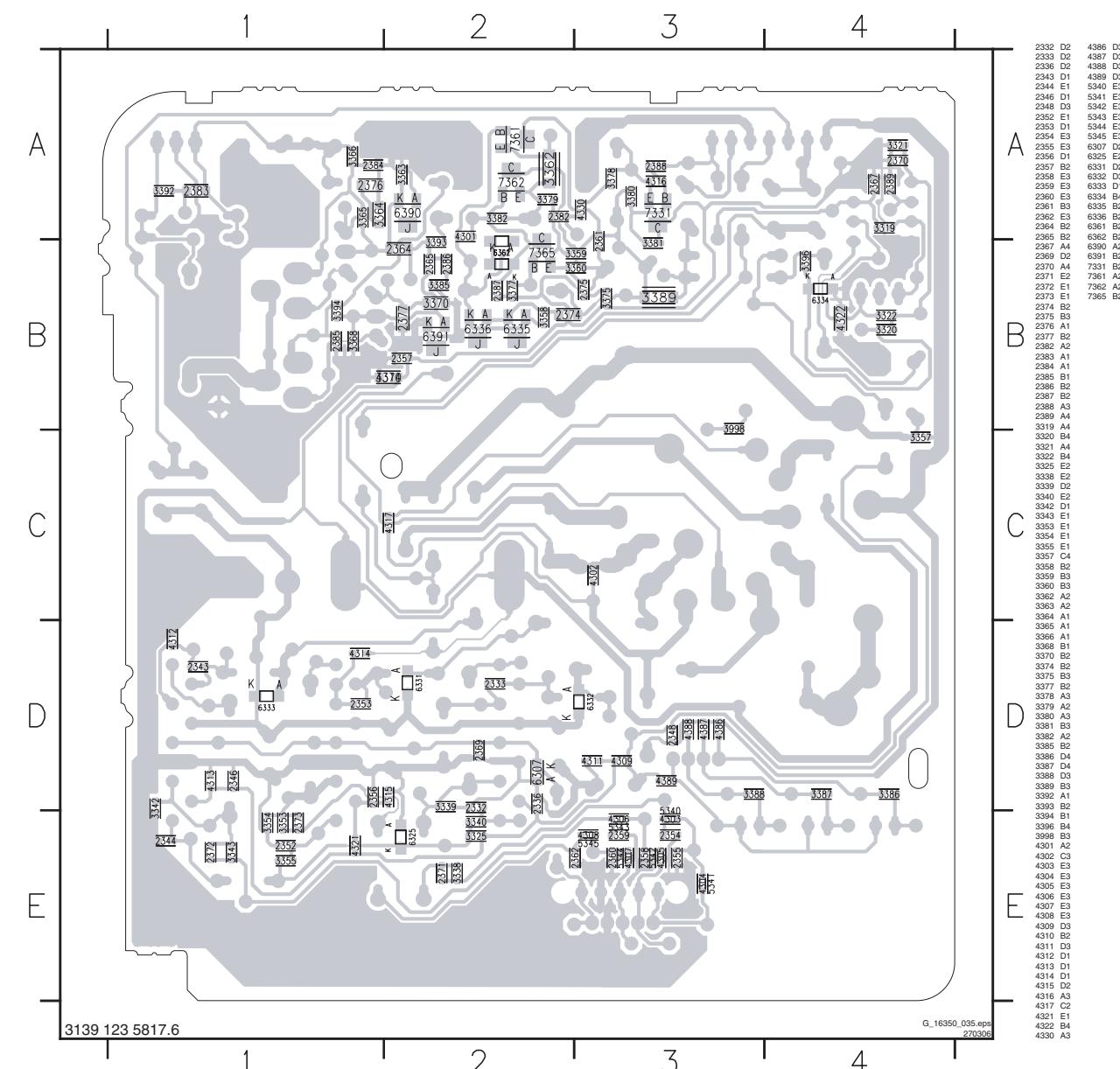
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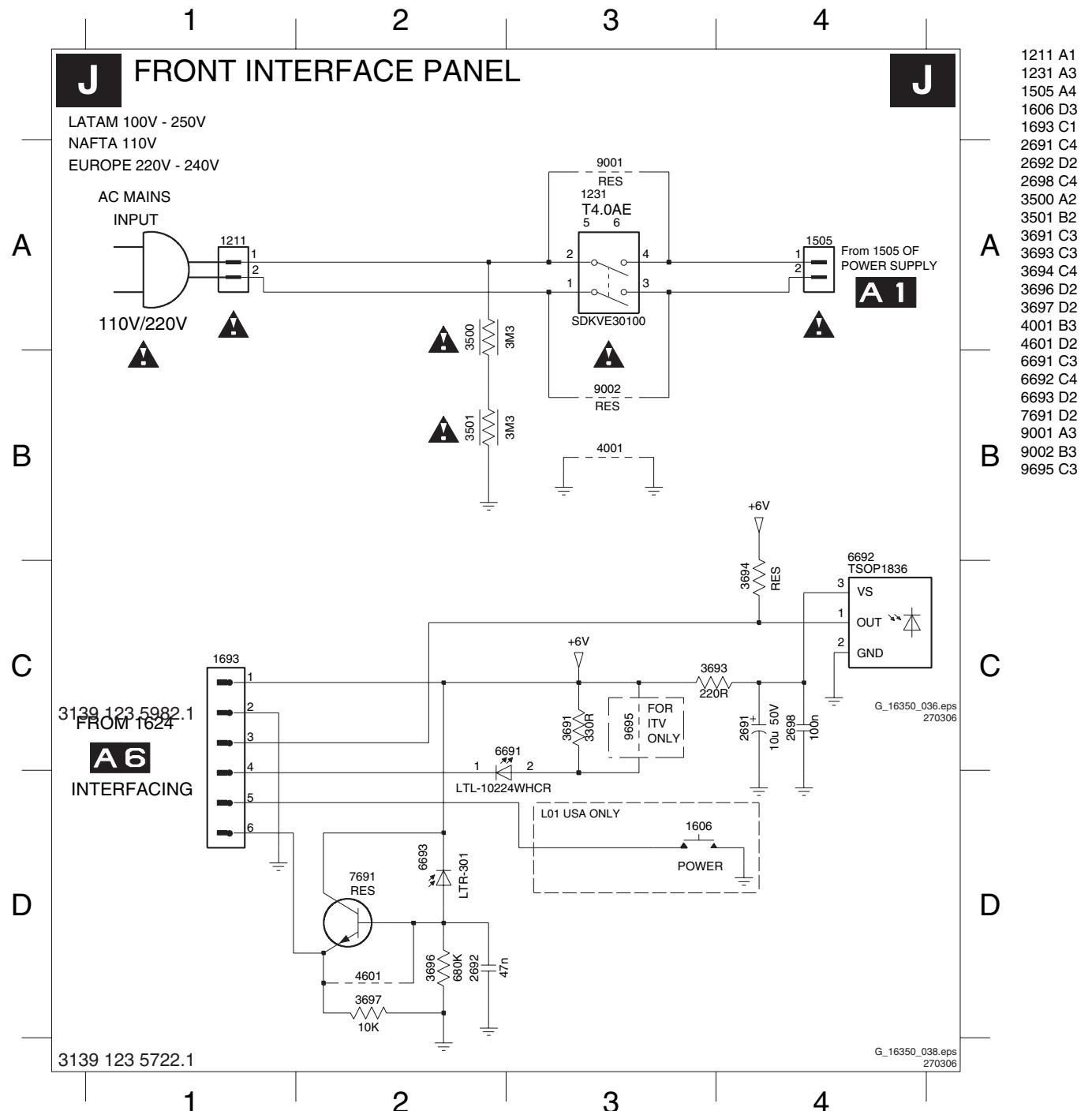
Layout CRT Panel (Top Side)



Layout CRT (Bottom Side)



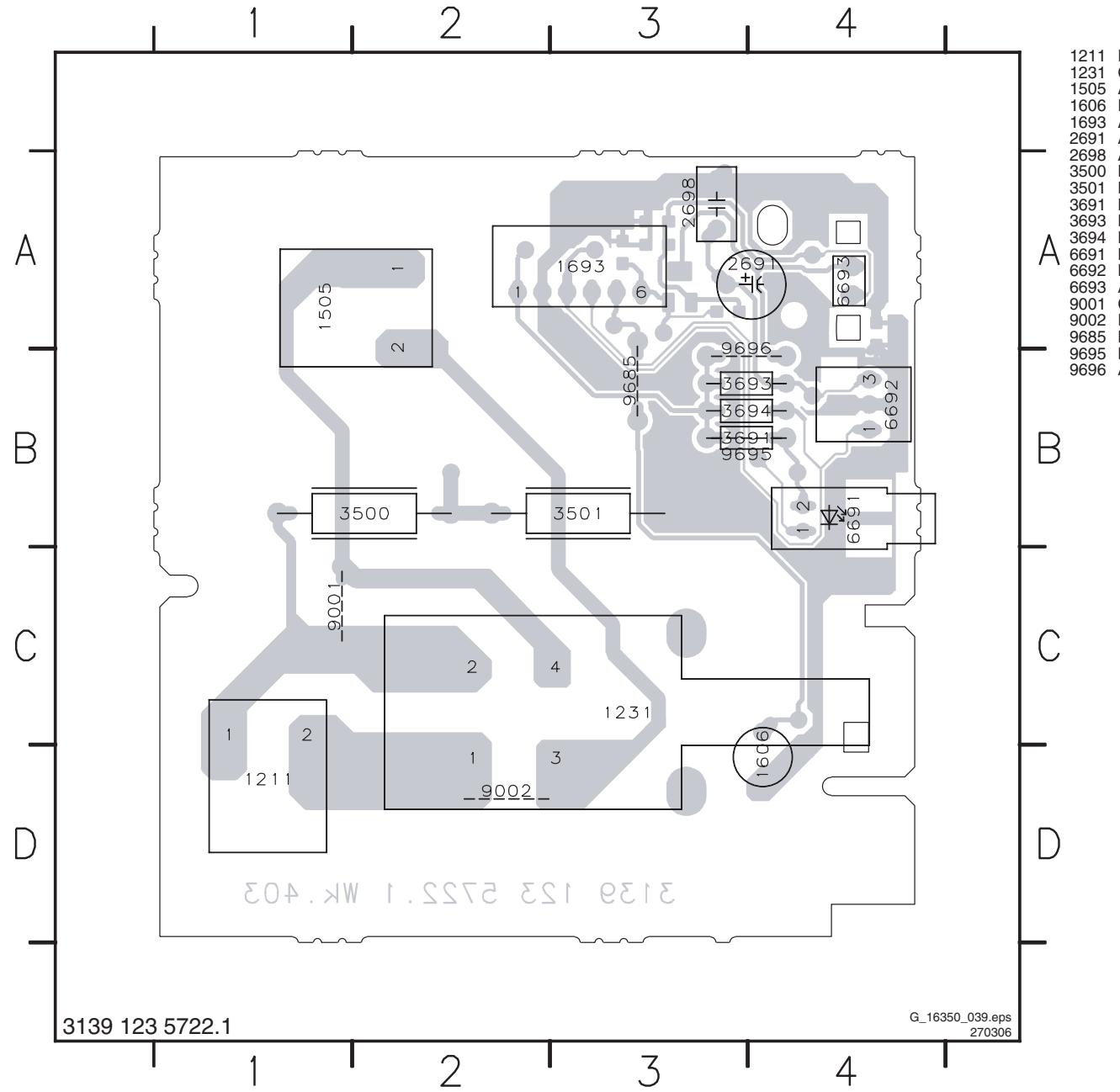
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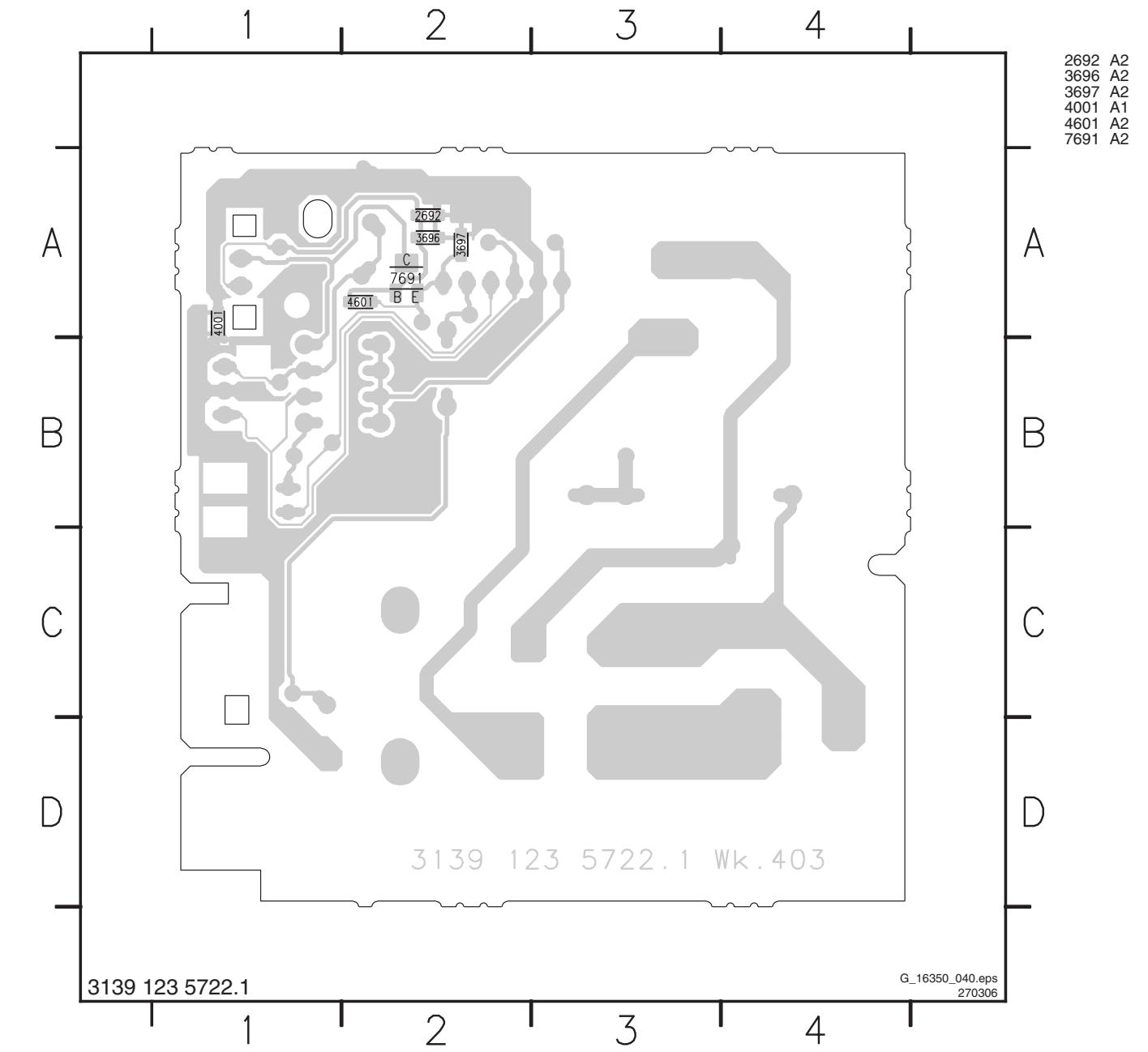
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3693 C3
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3696 D2
3697 D2
4001 B3
4601 D2
6691 C3
6692 C4
6693 D2
7691 D2
9001 A3
9002 B3
9695 C3

Layout Front Interface Panel (SL6) (Top Side)



Layout Front Interface Panel (SL6) (Bottom Side)



8. Alignments

Index of this chapter:

- 8.1 General Alignment Conditions
- 8.2 Hardware Alignments
- 8.3 Software Alignments
- 8.4 Option Settings

8.1 General Alignment Conditions

8.1.1 Default Alignment Settings

Perform all electrical adjustments under the following conditions:

- Power supply voltage: 230 V_{AC} / 50 Hz ($\pm 10\%$).
 - Connect the set to the mains via an isolation transformer with low internal resistance.
 - Allow the set to warm up for approximately 20 to 30 minutes.
 - Measure voltages and waveforms in relation to chassis ground (with the exception of the voltages on the primary side of the power supply).
- Caution:** never use heatsinks as ground.
- Test probe: 100 : 1, $R_i > 10$ Mohm, $C_i < 3.5$ pF.
 - Use an isolated trimmer/screwdriver to perform alignments.

Perform all electrical adjustments with the following default settings (for all CRTs):

- Choose "Soft" picture mode with the "Smart Picture" button on the remote control.
- Set "Dynamic Contrast" and "Active Control" to "off" (if either one of them is present).
- Set "Brightness" to aligned value unless otherwise specified.
- Set "Contrast value" to 99.

8.1.2 Adjustment Sequence

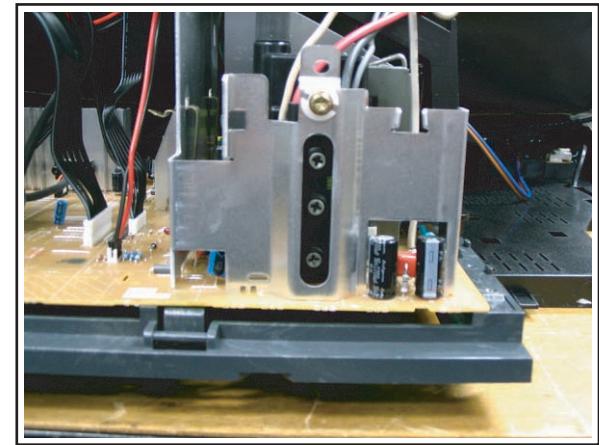
Use the following adjustment sequence:

1. Set the correct TV-set OPTIONS as described in paragraph "Options". After storing, re-start the set.
2. Rough adjustment of VG2 and FOCUS (potentiometers in "midway" positions; N.B.: wrong positions may cause error messages because of H or V-protection mechanism).
3. RF-AGC alignment.
4. Rough adjustment of GEOMETRY.
5. Allow the set to warm up.
6. Precise adjustment of VG2 and FOCUS.
7. Precise adjustment of GEOMETRY.
8. PIP alignments (if present).
9. COLOUR alignments.
10. Other software alignments.

8.2 Hardware Alignments

Notes:

- The Service Alignment Mode (SAM) is described in chapter 5 "Service Modes, Error Codes, and Fault Finding".
- Use the cursor-, menu-, and OK-buttons of the remote control (RC) transmitter for navigation.



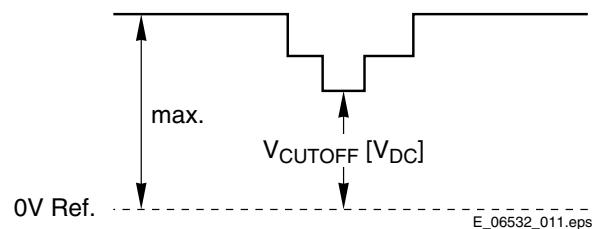
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Figure 8-1 Focus 1, 2, and Screen Vg2 adjustment

8.2.1 Vg2 Adjustment

In the frame-blanking period of the R, G, and B signals applied to the CRT, the video processor inserts a measuring pulse with different DC levels. Measure the black level pulse during the vertical flyback at the RGB cathodes of the CRT (pin 8 = R, 6 = G, 11 = B).

1. Connect the RF output of a pattern generator to the antenna input. Input a "black" picture (blank screen on CRT without any OSD info) test pattern.
 2. In the SAM mode, set the "Normal Red", "Normal Green" and "Normal Blue" values to "0" for "White Tone".
 3. Disable the black current loop (via the AKB bit).
 4. Use the MENU key to enter the "user" menu, select "Picture", and set "Brightness" and "Contrast" to "0".
 5. Set the oscilloscope to 20 V/div and the time base to 20 us/div. Use external triggering on the vertical pulse.
- Caution:** use a trigger point on the "cold" side!
6. Ground the scope on the CRT panel ("cold" side) and connect a 10:1 probe to one of the cathodes of the picture tube socket (see circuit diagram F).
 7. Measure at cathodes on the picture tube socket the DC-level of the measuring pulse (1st full line after the frame blanking) with respect to earth; N.B.: R = pin 8, G = pin 6, B = pin 11 of CRT socket.
 8. Select the pin with the highest level found and adjust V_cutoff by means of the Vg2-potmeter (lowest-one) on the Line Output Transformer (LOT) to 145 +/- 5 V_{DC} (for all screen sizes).
 9. Reset "Contrast" and "Brightness" to their original values.



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Figure 8-2 Waveform Vg2 alignment

8.2.2 Focus alignment

The LOT has the following outline:

- Focus 1 (F1) = Static alignment (red wire).
- Focus 2 (F2) = Dynamic alignment (white wire).

1. Use an external video pattern generator to input a "circle" or "crosshatch" test pattern to the set.
2. Choose "Natural" picture mode with the "Smart Picture" button on the remote control transmitter.
3. Adjust the "dynamic focus 2" potentiometer (in the middle on the LOT) until the horizontal lines at the centre of the screen are of minimum width without introducing a visible haze.
4. Adjust the "static focus 1" potentiometer (highest of the LOT) until the horizontal lines at the sides of the screen are of minimum width without introducing a visible haze.
5. Repeat these two steps to achieve the best result.

8.3 Software Alignments

Put the set in the SAM (see the "Service Modes, Error Codes and Fault Finding" section). The SAM menu will now appear on the screen. The different alignment parameters are described further on.

Notes:

- All changes to menu items and alignments must be stored manually.
- If an empty EAROM (permanent memory) is detected, all settings are set to pre-programmed default values, so the set must be re-aligned.

8.3.1 Tuner

AGC

1. Set an external pattern generator to a colour bar video signal and connect the RF output to the aerial input of the TV. Set the amplitude to 10 mV and the frequency to 475.25 MHz. Use system PAL B/G if possible, otherwise match the system of your generator with the received signal in the set.
2. Put the set in the SAM mode.
3. Select via the TUNER menu, the AGC sub-menu.
4. Connect a DC multi-meter to pin 1 of the tuner (F235, AGC pin).
5. Adjust the AGC until the voltage at pin 1 (F235, AGC pin) of the tuner is 3.3 V (+/- 0.1 V). The value can be incremented or decremented by pressing the right/left CURSOR button on the RC.
6. After alignment, save the value(s) with the STORE command in the SAM main menu.

IF PLL OFFSET

No adjustments needed: default value (which can not be changed) is "0".

8.3.2 Geometry

Notes:

- Set an **external** pattern generator to a crosshatch video signal and connect the RF output to the aerial input of the TV. Set the amplitude at least 1 mV_{RMS} (60 dB μ V) and the frequency to 475.25 MHz. Use system PAL B/G if possible, otherwise match the system of your generator with the received signal in the set.
- Use the default alignment settings, but set "Brightness" to "32".
- For wide screen models, set to "wide screen" mode, for "classic" models, set to "4:3".
- After alignment, save the value(s) with the STORE command in the SAM main menu.

Service tip: When the set is equipped with a rotation coil, use this menu item to check its correct alignment. If alignment is not correct, go to the user MENU, choose FEATURES, and select ROTATION. With the use of a crosshatch test pattern, align it to a correct horizontal picture.

Alignment

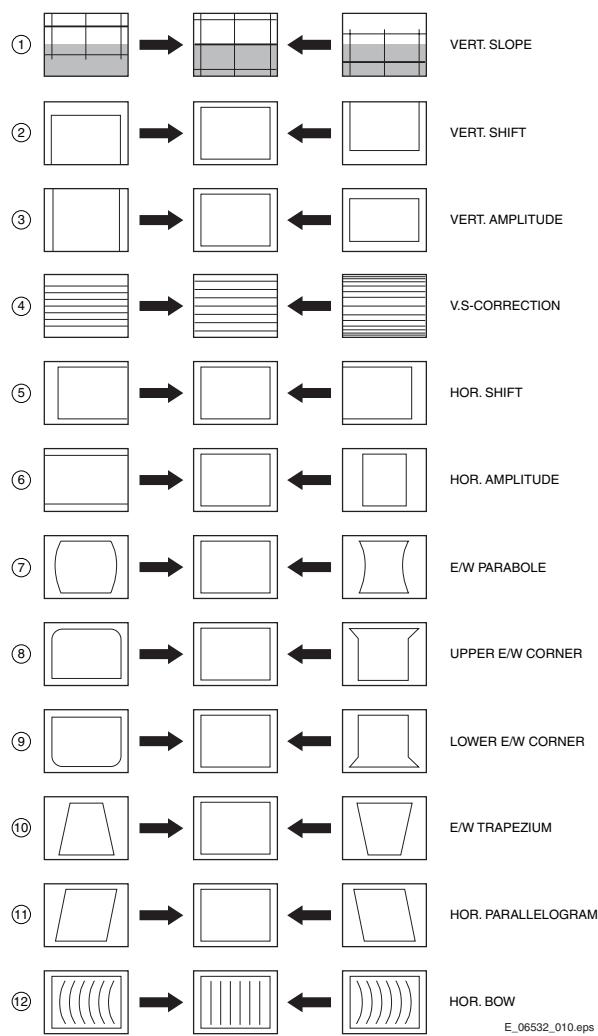


Figure 8-3 Geometry Alignments

Select "Pixel Plus" mode and put a test picture on the screen. Use the following software regulations to modify the vertical parts of the screen geometry (see Figure "Geometry Alignments" for a general idea of what you should see on the screen):

1. VSH (Vertical Shift): Align for the vertical picture centre, range from 0 to +63.
2. VAM (Vertical Amplitude): Compensating for any gain error in amplifier, adjust range from 0 to +63 to the proper amplitude.
3. VSC (Vertical S-Correction): Align for equal height of the blocks in the top, the bottom and the middle, range from 0 to +63.
4. VS (Vertical Slope). First activate menu item SBL (Service Blanking = ON), so the lower part of the test picture is no longer visible. Then adjust the Vertical Slope. Align for the horizontal line in the middle of the test picture to line up with the boundary between the (still visible) upper part of the screen and the (invisible) lower part of the screen. Range from 0 to +63. After this, switch the SBL to OFF again.

Next, align the following horizontal screen geometry settings:

1. HSH (Horizontal Shift): Adjust for the horizontal centre of the screen, range from 0 to +63.

2. EWW (East-West Width): This sets the (overall) horizontal size of the picture on the screen. Range from 0 to +63.
3. EWP (East-West Parabola): Align for the vertical lines of the test picture to be straight lines. Range from 0 to +63.
4. HB (Horizontal Bow): Align the EW parabola to be symmetrical, range from 0 to +63.
5. HP (Horizontal Parallel): Align for straight vertical lines on the picture sides, range from 0 to +63.
6. UCP (Upper Corner Parabola): Align for the vertical lines in the upper corners of the screen to be straight. Range from 0 to +63.
7. LCP (Lower Corner Parabola): Align for the vertical lines in the lower corners of the screen to be straight. Range from 0 to +63.
8. EWT (East-West Trapezium): Align for equal length of the horizontal lines in the upper and lower parts of the screen. Range from 0 to +63.

Now, select "Double Lines" mode, and again align the following vertical screen geometry setting:

1. VS (Vertical Slope); First activate menu item SBL (Service Blanking = ON), so the lower part of the test picture is no longer visible. Then adjust the Vertical Slope. Align for the horizontal line in the middle of the test picture to line up with the boundary between the (still visible) upper part of the screen and the (invisible) lower part of the screen. Range from 0 to +63. After this, switch the SBL to OFF again.

8.3.3 White Tone

In the WHITE TONE sub menu, the colour values for the different colour temperatures can be changed.

The colour temperature mode (NORMAL, DELTA COOL, DELTA WARM) can be selected per colour (R, G, and B) with the RIGHT/LEFT cursor keys. The mode or value can be changed with the UP/DOWN cursor keys.

First, the values for the NORMAL colour temperature must be selected. Then the offset values for the DELTA COOL and DELTA WARM mode can be selected. Note that the alignment values are non-linear.

Alignment

Normally, no adjustments are needed.

If the white tone alignment values used in CSM of the TV set do not give the required result, use the following alignment method:

1. Set the external pattern generator to a 100% white pattern, and connect its RF output to the aerial input of the TV. Set the amplitude to at least 1 mV_{RMS} (60 dBuV) and the frequency to 475.25 MHz. Use system PAL B/G if possible, otherwise match the system of your generator with the received signal in the set.
2. Set "Smart Picture" to "Natural".
3. Set "Dynamic NR" to "off".
4. Put the set in the SAM mode.
5. Select via the WHITE TONE menu, the PATTERN sub-menu.
6. Set PATTERN to "on".
7. Set NORMAL GREEN to "0".
8. Measure with the colour analyser (Minolta CA100 Colour Analyser or equivalent), calibrated with the spectra, on the centre of the screen.
9. Adjust with the cursor left/right command the Red and Blue register for the right xy-coordinates (see the table below).
10. Repeat the white tone adjustment also for the colour temperatures COOL and WARM.

Table 8-1 White tone alignment (with colour analyser)

White D mode	Temperature	DUV	x	y
Normal	13100 K	+0.004	264 +/- 4	279 +/- 4
Cool	18300 K	+0.005	256 +/- 5	264 +/- 5
Warm	6500 K	+0.005	314 +/- 5	324 +/- 5

8.3.4 Sound

No adjustments needed. Use the given default values:

- AF-M = 250
- A2T = 400
- AT = 2

8.3.5 Smart Settings

No adjustments needed.

8.4 Option Settings

8.4.1 Introduction

The microprocessor communicates with a large number of I²C ICs in the set. To ensure good communication and to make digital diagnosis possible, the microprocessor has to know which ICs to address. The presence / absence of these specific ICs (or functions) is made known by the option codes.

Notes:

- After changing the option(s), save them with the STORE command.
- All changes are disregarded when the OPTIONS submenu is left without using the STORE command.
- The new options setting is only active after the TV is switched "off" and "on" again with the Mains switch (the EAROM is then read again).

8.4.2 Changing Options

Options are used to control the presence / absence of certain features and hardware. There are two ways to change the option settings. All changes in the option settings are saved by selecting STORE and pressing the CURSOR RIGHT key. Some changes will only take effect after the set has been switched OFF and ON with the mains switch (cold start).

Changing Multiple Options by Changing Option Byte Values

Option Bytes (OB) makes it possible to set all options very fast. An option byte represents a number of different options. All options are controlled via option bytes (OB1 to OB13; each "OB" number represents 16 bits; bit numbers that are not used are omitted in the second column). Select an Option Byte you want to change with the CURSOR UP/DOWN keys, and key in the new value. See the table for more details. An explanation per option is listed in paragraph "Option Bit Definition".

Changing a Single Option

It is also possible to change an option one at a time. Therefore, select the option with the CURSOR UP/DOWN keys and change its setting with the LEFT/RIGHT keys.

8.4.3 Option Settings

Table 8-2 Option codes in detail, at bit level

In the table below, you will find the option settings.

Option byte & bit table for restoring the TV set to its original Factory Default Options via the NVM Editor in the SAM menu			
		Model number	
		29PT952/1/12	32PW955/1/12
OP1	Description of feature/option to be switched ON or OFF		
bit 7 (msb)	OP_PHILIPS_TUNER	1	1
bit 6		0	0
bit 5		0	0
bit 4		0	0
bit 3	OP_ACI	1	1
bit 2	OP_UK_PNP	0	0
bit 1	OP_VIRGIN_MODE	1	1
bit 0 (lsb)		0	0
	Total DEC Value	138	138
	Total HEX Value	8A	8A
OP2			
bit 7 (msb)		0	0
bit 6		0	0
bit 5		0	0
bit 4		0	0
bit 3	OP_TILT	0	1
bit 2		0	0
bit 1		0	0
bit 0 (lsb)		0	0
	Total DEC Value	0	8
	Total HEX Value	00	08
OP3			
bit 7 (msb)		0	0
bit 6		0	0
bit 5		0	0
bit 4		0	0
bit 3		0	0
bit 2	OP_WIDE_SCREEN	0	1
bit 1	OP_WSSB	0	1
bit 0 (lsb)		0	0
	Total DEC Value	0	6
	Total HEX Value	00	06
OP4			
bit 7 (msb)	OP_COMPRESS_16_9	1	0
bit 6		0	0
bit 5		0	0
bit 4		0	0
bit 3		0	0
bit 2		0	0
bit 1		0	0
bit 0 (lsb)		0	0
	Total DEC Value	128	0
	Total HEX Value	80	00
OP5			
bit 7 (msb)		0	0
bit 6		0	0
bit 5		0	0
bit 4		0	0
bit 3		0	0
bit 2		0	0
bit 1		0	0
bit 0 (lsb)		0	0
	Total DEC Value	0	0
	Total HEX Value	00	00
OP6			
bit 7 (msb)		0	0
bit 6		0	0
bit 5		0	0
bit 4		0	0
bit 3		0	0
bit 2		0	0
bit 1	OP_LIGHT_SENSOR	1	1
bit 0 (lsb)		0	0
	Total DEC Value	2	2
	Total HEX Value	02	02
OP7			
bit 7 (msb)		0	0
bit 6		0	0
bit 5		0	0
bit 4	OP_SS_DEMO_EU	1	1
bit 3		0	0
bit 2		0	0
bit 1		0	0
bit 0 (lsb)		0	0
	Total DEC Value	16	16
	Total HEX Value	10	10

8.4.4 Option Bit Definition

OP_PHILIPS_TUNER: Philips Tuner.

Function: Choose the tuner type that is configured in the hardware.

Values: OFF= Disabled, Other (non-Philips) tuner is used. ON= Enabled, Philips compatible tuner is used.

OP_ACI: Automatic Channel Installation.

Function: Disable/Enable automatic channel installation.

Values: OFF= Disabled Automatic Channel Installation. ON= Enabled Automatic Channel Installation.

Note: Download present program when ACI is ON.

OP_UK_PNP: UK Plug and Play.

Function: Disable/Enable UK's default Plug and Play setting.

Values: OFF= Disabled, UK's default Plug and Play setting is not available. ON= Enabled, UK's default Plug and Play setting is available.

Note: When UKPNP and VMOD are ON at the initial set-up, LANGUAGE= ENGLISH, COUNTRY= GREAT BRITAIN and after auto store is complete, VMOD will be set automatically to OFF while UKPNP remain ON.

OP_VIRGIN_MODE: Virgin Mode.

Values: OFF= Disabled, cannot access virgin mode. ON= Enabled, can access virgin mode.

Function: Disable/Enable virgin mode.

Note: Plug and Play menu item will be displayed to perform installation at the initial start up of the TV when VIRGIN MODE is ON and after installation is done, VIRGIN MODE will be automatically set to OFF.

OP_TILT: Rotation Tilt.

Function: Change the tilt level of picture tube.

Values: OFF= Disabled, menu item ROTATION is not available. ON= Enabled, menu item ROTATION is available (WS = 1; 4 : 3 = 0).

OP_WIDE_SCREEN: Screen size 16x9.

Function: Disable/Enable Screen size 16x9.

Values: OFF= Disabled. Screen size 16x9 is not available.

ON= Enabled. Screen size 16x9 is available.

OP_WSSB: Wide Screen Signalling Bit.

Function: Disable/Enable Wide screen Signalling bit function.

Values: OFF= Disabled. ON= Enabled (WS = 1; 4 : 3 = 0).

OP_COMPRESS_16_9: Compression enable/disable bit.

Function: Disable/Enable screen compression function.

Values: OFF= Disabled. ON= Enabled (compression = 1; no compression = 0).

OP_LIGHT_SENSOR: Light sensor on/off switching bit.

Function: Disable/Enable Light sensor.

Values: OFF= Disabled. Light sensor mode disabled. ON= Enabled. Light sensor mode available.

OP_SS_DEMO_EU: Split Screen Demo.

Function: Disable/Enable Split Screen Demo.

Values: OFF= Disabled. Split Screen Demo is not available.

ON= Enabled. Split Screen Demo is available.

9. Circuit Descriptions, Abbreviation List, and IC Data Sheets

Index of this chapter:

- 9.1 Introduction
- 9.2 Small Signal Board
- 9.3 Software Upgrading
- 9.4 Abbreviation List
- 9.5 IC Data Sheets

Note: Only new circuits (circuits that have not been published recently) are mentioned. For the other circuits, see the A02 and the ES1 manuals.

9.1 Introduction

The L06.1E is a 100 Hz widescreen CRT based HD prepared TV set for the year 2006. Its CRT has a "RF" (Real Flat) screen, with 29 and 32 inch screen formats.

The set has the following styling: SL6.

Pixel Plus technology is used for improved picture quality.

This TV set is based on the LSP of the ES1 (which in its turn is based on the A02).

It has a new SSB. The SSB is no longer directly attached to the LSP, but has been moved to a separate position in the TV set.

Flatcables are used for the connections between the LSP and the SSB.

9.2 Small Signal Board

For a description of the deflection circuits, the correction circuits and the X-ray protection circuits of the LSP, see the ES1 manual. For a description of the remaining LSP circuits, see the A02 manual, on which the ES1 was based.

For a description of the new SSB, see the circuit diagrams in chapter 6 and the general description below.

9.1.1 General description of the SSB

The L06.1E chassis has a new SSB, with respect to the SSB used in the ES1 chassis. This SSB is based on the Trident and a new processor Reneas (instead of the Painter processor used in the L05 chassis).

The main functionalities of the SSB are:

- RF tuner, RF/AV decoder, 3D comb filter, AV interface for: 2 SCART connectors, 1 Side AV (for SVHS and headphone); HD YPbPr/LR; HDMI/DVI plus LR Audio,
- Up-scaler (uses PQ registers in Trident),
- Audio decoder/processor,
- Deflection processor,
- System controller with Teletext processor (128 MB memory, shared between video and Teletext - 1200 pages).

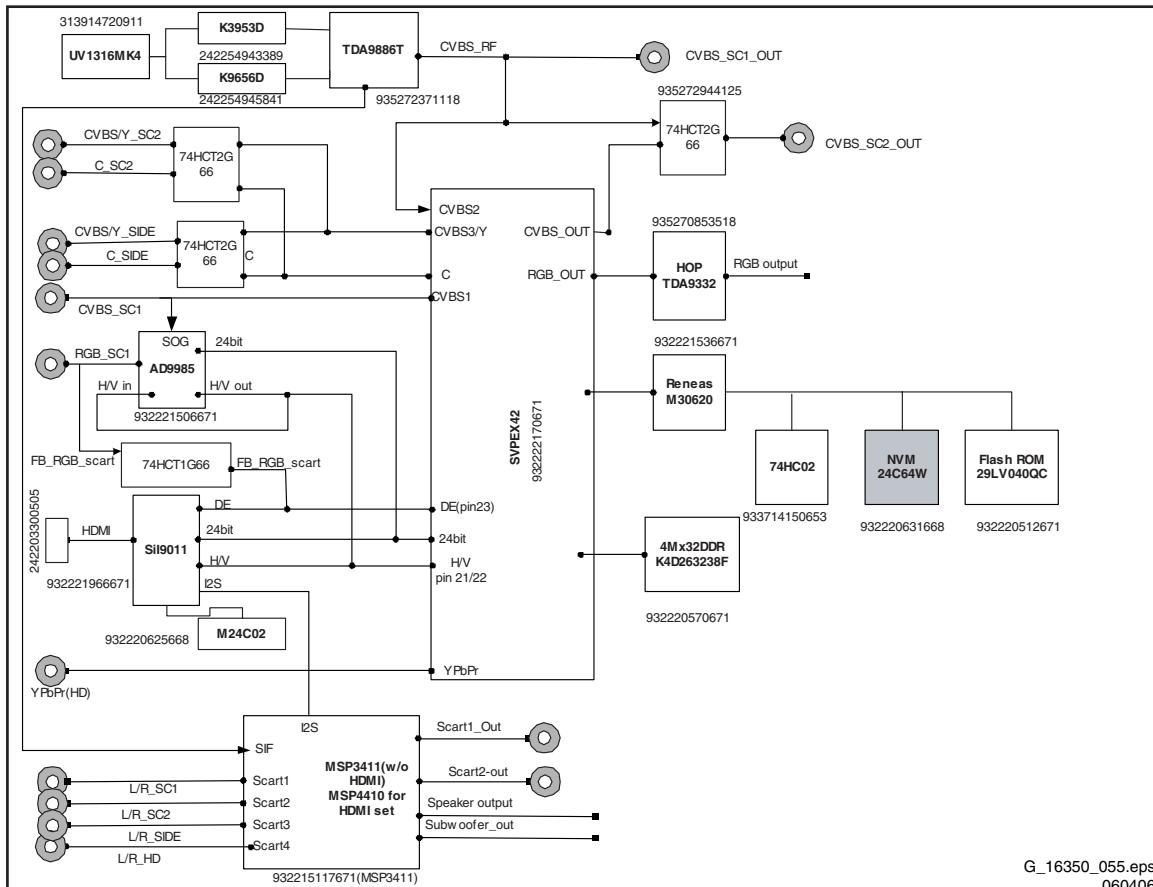


Figure 9-1 SSB architecture

9.3 Software Upgrading

In this chassis, you can **upgrade** the software via the IAP tool (In Application Programming). This offers the possibility, to replace the entire SW image without having to remove the flash-RAM from its socket. You can find more information on this in Chapter 5 ("Service Modes, Error Codes, and Faultfinding") in the paragraph "IAP Tool".

9.4 Abbreviation List

0/6/12	SCART switch control signal on A/V board. 0 = loop through (AUX to TV), 6 = play 16:9 format, 12 = play 4:3 format
2CS	2 Carrier Sound
A2	Commonly known as 2 Carrier Sound (2CS) system
AC	Alternating Current
ACI	Automatic Channel Installation: algorithm that installs TV channels directly from a cable network by means of a predefined TXT page
ADC	Analogue to Digital Converter
ADOC	Analogue Digital One Chip
AFC	Automatic Frequency Control: control signal used to tune to the correct frequency
AGC	Automatic Gain Control: algorithm that controls the video input of the feature box
AM	Amplitude Modulation
ANC	Automatic Noise Reduction; One of the algorithms of Auto TV
AP	Asia Pacific
AR	Aspect Ratio: 4 by 3 or 16 by 9
ASD	Automatic Standard Detection
AUDIO-SL	Audio Surround Left
AV	Audio Video
AVL	Automatic Volume Level control
B-SC1-IN	Blue SCART1 in
B-SC2-IN	Blue SCART2 in
B-TXT	Blue teletext
B/G	Monochrome TV system. Sound carrier distance is 5.5 MHz
BBD	Black Bar Detection
BCL	Beam Current Limiter
BC-PROT	PROtection signal to the microprocessor in case of a too high Beam Current.
BLC-INFO	BLack Current INFO.
BLD	BLack Level Detection.
BS	BLack Stretch.
BTSC	Broadcast Television Standard Committee; Multiplex FM stereo sound system, originating from the USA and used e.g. in LATAM and AP-NTSC countries.
C	Centre channel (audio) or Chroma; The NTSC/PAL/SECAM video signal contains two parts that make up what we see on the display; the luminance (or intensity) part and the colour (or chroma) part
CBA	Circuit Board Assembly (or PWB)
CL	Constant Level: audio output to connect with an external amplifier
CLUT	Colour Look Up Table
ComPair	Computer aided rePair
CRT	Cathode Ray Tube (or picture tube)
CSM	Customer Service Mode
CTI	Colour Transient Improvement; Manipulation of the steepness of the chroma transients
CVBS	Composite Video Blanking and Synchronisation
CVBS-EXT	CVBS signal from external source (VCR, VCD, etc.)
CVBS-INT	CVBS signal from Tuner
CVBS-MON	CVBS monitor signal
CVBS-TER-OUT	CVBS terrestrial out
CVI	Component Video Input

D/K	Monochrome TV system. Sound carrier distance is 6.5 MHz. D= VHF-band, K= UHF-band	FBL-TXT FBX	Fast Blanking Teletext Feature BoX; Part of the small signal board /separate module which contains 100 Hz processing, extra features and AutoTV algorithms (FBX6= based on PICNIC, FBX7= based on PICNIC and Eagle, FBX8= based on PICNIC, Eagle, and Columbus)
DAC	Digital to Analogue Converter		
DAF	Dynamic Astigmatism and Focusing; a method to keep the electron spot round and focused during the whole scan		
DBE	Dynamic Bass Enhancement; extra low frequency amplification	FE	Front End; Tuner and RF part together
DC	Direct Current	FLASH	FLASH memory
DCC	Dynamic Contrast Control	Field	Each interlaced broadcast FRAME is composed of two Fields, each Field consists of either Odd or Even lines
DC-filament	Filament supply voltage	Filament	Filament of CRT
DEGAUSS	Control line. Logic LOW to enable CRT degaussing. Logic HIGH to disable the CRT degaussing.	FLASH	FLASH memory
DFU	Directions For Use: owner's manual	FM	Field Memory / Frequency Modulation
DNR	Digital Noise Reduction; Noise reduction feature of the set / Dynamic Noise Reduction	FM-Radio	Radio receiver that can receive the FM Band 87.5 - 108 MHz
DNR	Digital Noise Reduction; Noise reduction feature of the set / Dynamic Noise Reduction	FMR	FM Radio
DOP	Digital Output Processor (Part of ADOC which takes care of RGB control and deflection)	Frame	A complete TV picture comprising all lines (625/525)
DPL	Dolby Pro Logic	FRAMEDRIVE -	Differential frame (vertical) drive signal (negative)
DPL	Dolby Pro Logic	FRAMEDRIVE +	Differential frame (vertical) drive signal (positive)
DRAM	Dynamic RAM; dynamically refreshed RAM	FRC	Frame Rate Converter
DRAM	Dynamic RAM; dynamically refreshed RAM	FRONT-C	Front input chrominance (SVHS)
DS	Digital Scan	FRONT-DETECT	Control line for detection of headphone insertion, Service Mode jumper, power failure detection
DSP	Digital Signal Processing	FRONT-Y_CVBS	Front input luminance or CVBS (SVHS)
DST	Dealer Service Tool; Special remote control designed for dealers to enter e.g. service mode (a DST-emulator is available in ComPair)	FTV	Flat TeleVision
DTS	Digital Theatre Sound	G	Green
DVD	Digital Versatile Disc	G-SC1-IN	Green SCART1 in
DVI(-d)(-i)	Digital Visual Interface (d=digital only) (i=integrated); A digital video interface to a display, designed to replace the analogue YPbPr or RGB interface	G-SC2-IN	Green SCART2 in
DW	Double Window	G-TXT	Green teletext
DYN-FASE-COR	Dynamic phase correction, to correct the phase of the H-drive	Gb/s	Giga bits per second
EEPROM	Electrically Erasable and Programmable Read Only Memory	H	H_sync to the module
EHT	Extreme High Tension; the voltage between the cathode and the shadow mask that accelerates the electrons towards the screen (around 25 kV)	H-2FH	Horizontal sync input for the 2fH source
EHT-INFO	Extra High Tension INFOrmation, used for contrast reduction, vertical and horizontal amplitude correction, beam current protection, and flash detection	H-A50	Horizontal Acquisition 1fH: horizontal sync pulse coming out of the HIP
EMI	Electro Magnetic Interference; Leakage of high-frequency radiation from a transmission medium	H-D100	Horizontal Drive 2fH; Horizontal sync pulse coming out of the Feature Box
EPG	Electronic Program Guide; System used by broadcasters to transmit TV guide information (= NextView)	H-DRIVE	Horizontal Drive
EPLD	Erasable Programmable Logic Device	H-FLYBACK	Horizontal Flyback
EU	EUrope	H-OUT	H_sync output of the module / Horizontal Output pulse
EW	East West, related to horizontal deflection of the set	HA	Horizontal Acquisition; horizontal sync pulse
EW-DRIVE	East -West correction drive signal.	HD	High Definition: 720p, 1080i, 1080p
EXT	EXTernal (source), entering the set by SCART or by cinches (jacks)	HDMI	High Definition Multimedia Interface, digital audio and video interface
FBL	Fast Blanking: DC signal accompanying RGB signals	HEADPHONE-L HEADPHONE-R	Stereo headphone (Left) signal output. Stereo headphone (Right) signal output.
FBL-SC1-IN	Fast blanking signal for SCART1 in	HFB	Horizontal Flyback Pulse; Horizontal sync pulse from large signal deflection
FBL-SC2-IN	Fast blanking signal for SCART2 in	HP	Head Phone
		HW	Hardware
		I	Monochrome TV system. Sound carrier distance is 6.0 MHz. VHF- and UHF-band
		IAP	IAP Tool (In Application Programming): used to upload software to a TV set without having to remove flash ROMs
		I ² C	Inter IC bus (also called IIC)
		I ² S	Inter IC Sound bus
		IC	Integrated Circuit
		IDRIVE-	Vertical drive -
		IDRIVE+	Vertical drive +
		IF	Intermediate Frequency

IF-TER	IF signal from main tuner	MPIP	Multi Picture in Picture; Commercial feature showing several frozen or moving pips
IIC	Inter IC bus (also called I2C)		MultiPleX
Interlaced	Scan mode where two fields are used to form one frame. Each field contains half the number of the total amount of lines. The fields are written in "pairs", causing line flicker.	MSP	Multi-standard Sound Processor: ITT sound decoder
IO	In/Out	MUTE	MUTE Line
IR	Infra Red	NAFTA	North American Free Trade Association: Trade agreement between Canada, USA and Mexico
IROM	Internal ROM (inside the microcontroller)	NC	Not Connected
IRQ	Interrupt ReQuest	NDF	No vertical Deflection; Vertical fly back protection
ITV	Institutional TV	NHF	No Horizontal deflection; Horizontal fly back protection
JTAG	Joint Test Action Group; Definition for a standardised serial test interface	NICAM	Near Instantaneously Companded Audio Multiplexing; This is a digital sound system, mainly used in Europe
KEYB	Front panel keyboard		Negative Temperature Coefficient, non-linear resistor (resistance decreases if temperature increases)
KEYBOARD	Input line. Carries the voltage value of the corresponding tact switch on TOP-control or FRONT-control keypad	NTC	National Television Standard Committee. Colour system used mainly in North America and Japan.
L	Left audio channel		Colour carrier NTSC M/N = 3.579545 MHz, NTSC 4.43 = 4.433619 MHz (this is a VCR norm, it is not transmitted off-air)
L/L'	Monochrome TV system. Sound carrier distance is 6.5 MHz. L' is Band I, L is all bands except for Band I	NTSC	Non Volatile Memory; IC containing data such as alignment values, preset stations
Last Status	The settings last chosen by the customer and read and stored in RAM or in the NVM. They are called at start-up of the set to configure it according to the customer's preferences		Open Circuit
LATAM	LA Tin AMerica	NVM	Option Byte
LCD	Liquid Crystal Display		Open Circuit
L-CL_VLOUT	REAR CINCH stereo output	O/C	On/Standby
LED	Light Emitting Diode	OB	Active-LOW control line. Logic LOW = red LED "on", HIGH = red LED "off"
LFE	Low Frequency Enhancement audio channel	OC	Option Byte
L-FRONT-IN	EXT3 stereo input	ON/OFF LED	On Screen Display
LIGHT-SENSOR	Ambient light intensity signal.	ON/STBY	Project 50; Communication protocol between TV and peripherals
LINE DRIVE	Horizontal (line) deflection drive signal (for the Line transistor)	ON-OFF-LED	Phase Alternating Line; Colour system mainly used in West Europe (colour carrier= 4.433619 MHz) and South America (colour carrier PAL M= 3.575612 MHz and PAL N= 3.582056 MHz)
LNA	Low Noise Adapter / Low Noise Amplifier	OP	Personal Computer
LOT	Line Output Transformer (also called FBT); The transformer in which the EHT is generated	OSD	Printed Circuit Board (or PWB)
LS	Loud Speaker	P50	Pulse Code Modulation
Ls, Rs	Left surround and Right surround channel (audio)	PAL	Pilot Signal
LSP	Large Signal Panel		Picture In Graphic
Lt, Rt	Left total and Right total in case of a Dolby ProLogic encoded signal (audio)	PC	Picture In Picture
LTI	Luminance Transient Improvement	PCM	Phase Locked Loop; Used for e.g. FST tuning systems. The customer can directly provide the desired frequency
LTP	Luminance Transient Processor	PILOT	Power On Reset; Signal to reset the µP
LUT	Look Up Table	PIG	Signal that informs the micro controller (painter) that set will switch "off"
LVDS	Low Voltage Differential Signalling, data transmission system for high speed and low EMI communication.	PIP	Scan mode where all scan lines are displayed in one frame at the same time, creating a double vertical resolution.
M/N	Monochrome TV system. Sound carrier distance is 4.5 MHz. M= 525 lines @ 60 Hz, N= 625 lines @ 50 Hz	PLL	Positive Temperature Coefficient, non linear resistor (resistance increases if temperature increases)
Mb/s / Mbps	Mega bits per second	POR	Picture Tube Panel
MCS	Multi Channel Sound: refers to Dolby Pro Logic Surround in ES1E ADOC	POR-FLASH	Printed Wiring Board (also called PCB or CBA)
MDO	Mode control data output	Progressive Scan	Pulse Width Modulation
MIPS	Microprocessor without Interlocked Pipeline-Stages; A RISC-based microprocessor		Quasi Split Sound
Mips	Million instructions per second	PTC	Right audio channel / Red
MMI	Multi Media Interface		
MOSFET	Metal Oxide Semiconductor Field Effect Transistor	PTP	
MPEG	Motion Pictures Experts Group	PWB	
MPIF	Multi Platform InterFace (Part of Salsa chipset, sister-chip of ADOC IC)	PWM	
		QSS	
		R	

RAM	Random Access Memory	SMPS	Switched Mode Power Supply
RC	Remote Control transmitter	SND	Sound
RC5 (6)	Remote Control system 5 (6), the signal from the remote control receiver	SNDL-SC1-IN	Sound left SCART1 in
RDS	Radio Data System (European); This is an MPX signal carried in FM radio channels (87.5 ... 108 MHz)	SNDL-SC1-OUT	Sound left SCART1 out
RESET	RESET signal	SNDL-SC2-IN	Sound left SCART2 in
RF	Real Flat (picture tube) or Radio Frequency	SNDL-SC2-OUT	Sound left SCART2 out
RGB	Red, Green, and Blue colour space; The primary colour signals for TV. By mixing levels of R, G, and B, all colours (Y/C) are reproduced	SNDR-SC1-IN	Sound right SCART1 in
RGBHV	Red, Green, Blue, Horizontal sync, and Vertical sync	SNDR-SC1-OUT	Sound right SCART1 out
RISC	Reduced Instruction Set Computer; A processor architecture based on ultra-high speed processing technology that uses a far simpler set of operating commands than a normal microprocessor does	SNDR-SC2-IN	Sound right SCART2 out
RMS	Root Mean Square value	SNDR-SC2-OUT	Sound right SCART2 out
ROM	Read Only Memory	SNDS-VL-OUT	Surround sound left variable level out
S	Surround channel or mono surround channel (audio)	SNDS-VR-OUT	Surround sound right variable level out
SALSA	System Application for Low Segment of Analogue TV	SNERT	Synchronous No parity Eight bit Reception and Transmission
SAM	Service Alignment Mode	SOG	Sync On Green
SAP	Secondary Audio Program; Generally used to transmit audio in a second language	SOPS	Self Oscillating Power Supply
SAW	Surface Acoustic Wave	SOUND-ENABLE	Control line to do hardware mute or un-mute of loudspeakers.
SC	SandCastle: two-level pulse derived from sync signals	SRAM	Static RAM
SCART	Syndicat des Constructeurs d'Appareils Radiorécepteurs et Téléviseurs; This is a 21-pin connector used in EU, that carries various audio, video, and control signals (it is also called Péritel connector)	SRAM	Static RAM
SCAVEM	Scan Velocity Modulation; Advanced beam control technology, which results in sharper edges on all images for outstanding clarity	SS	Small Screen
SC1-OUT	SCART output of the MSP audio IC	ST-BY	STandBY
SC2-B-IN	SCART2 Blue in	STANDBY (POR)	Signal coming from Main Supply informing the supply is switching "off"
SC2-C-IN	SCART2 chrominance in	STATUS	Status signal from pin 8 of SCART connector
SC2-OUT	SCART output of the MSP audio IC	STBY	STandBY
S/C	Short Circuit	SVHS	Super Video Home System
SCL	Serial Clock signal on I ² C bus	SW	Software or Subwoofer or Switch
SCL-F	Serial CLock signal on Fast I ² C bus	TBD	To Be Defined
SD	Standard Definition	THD	Total Harmonic Distortion
SDA	Serial Data line of I ² C bus	TIILT	PWM Output signal (variable DC level) to control the picture tilt from the DOP block of the ADOC.
SDA-F	Data Signal on Fast I ² C bus	TXT	Teletext; TXT is a digital addition to analogue TV signals that contain textual and graphical information (25 rows x 40 columns). The information is transmitted within the first 25 lines during the Vertical Blank Interval (VBI)
SDM	Service Default Mode	TXT-SW	Teletext switch
SDAM	Service Default / Alignment Mode	U-100	U signal 1fH (after Feature Box)
SDRAM	Synchronous DRAM	UART	Universal Asynchronous Receiver Transmitter
SECAM	SÉquence Couleur Avec Mémoire; Colour system mainly used in France and East Europe. The chroma is FM modulated and the R-Y and B-Y signals are transmitted line sequentially. Colour carriers= 4.406250 MHz and 4.250000 MHz	UBE	Ultra Bass Enhancement
SEL-SVHS-RR-	SVHS Selection Signal	µC	Microcontroller
STATUS2	Sound Intermediate Frequency	UI	User Interface
SIF	Single In-line Memory Module; 80-fold connector between LSP and SSB	UOC	Ultimate One Chip
SIMM	Single In-line Memory Module; 80-fold connector between LSP and SSB	µP	Microprocessor
SL	Single In-line Memory Module; 80-fold connector between LSP and SSB	UV	Colour difference signals
SLDP	Smart Local Dooling Prevention (HW and SW)	V	V_sync
SMC	Surface Mounted Component	V-100	V_sync from Feature Box (2fH)
		V-2FH	Vertical sync input for the 2fH source.
		VA50	Vertical Acquisition 1fH
		V-AMP	Vertical Amplitude DAC output
		V-BAT	Main supply for deflection (usually 141 V)
		VD-100	Vertical Drive 2fH; vertical sync pulse from deflection
		VD-NEG	One of the symmetrical drive signals for the DC frame output stage.
		VD-POS	One of the symmetrical drive signals for the DC frame output stage
		V-OSD	Vertical sync OSD
		VA	Vertical Acquisition
		VBI	Vertical Blanking Interval; Time during which the video signal is blanked when going from bottom to top of the display
		V-chip	Violence chip. Adds content filtering capabilities to NTSC video
		VCR	Video Cassette Recorder
		VD	Vertical Drive; Vertical sync pulse coming from the Feature Box
		VDS	Virtual Dolby Surround
		VERT	Vertical Output pulse

VESA	Video Electronics Standards Association
VGA	Video Graphics Array
VGND	Video ground
VGUARD	Vertical guard voltage
VIF	Video Intermediate Frequency
VL	Variable Level out; Processed audio output towards external amplifier
VOL (+/-)	Volume (+/-)
V-SYNC-VGA	V_sync on VGA connector
WD	Watch Dog
WE	Write Enable control line
WS	Wide Screen; Screens with an aspect ratio of 16:9
WSS	Wide Screen Signalling; Used by broadcasters to transmit e.g. PALPLUS and 16:9 Aspect Ratio
WST	World System Teletext
WXGA	1280x768 (15:9) or 1366x768 (16:9)
WYSIWYR	What You See Is What You Record: record selection that follows main picture and sound
XGA	Extended Graphics Array; 1024x768 (4:3)
XTAL	Quartz crystal
Y	Luminance signal
YPbPr	Component video (Y= Luminance, Pb/Pr= Colour difference signals B-Y and R-Y, other amplitudes w.r.t. to YUV)
Y/C	Y consists of luminance signal, blanking level and sync; C consists of chroma (colour) signal
Y-OUT	Luminance-signal
YUV	Colour space used by the NTSC and PAL video systems. Y is the luminance and U/V are the colour difference signals

9.5 IC Data Sheets

This section shows the internal block diagrams and pin layouts of ICs that are drawn as "black boxes" in the electrical diagrams (with the exception of "memory" and "logic" ICs). This is not applicable to this manual.

10. Spare Parts List

Sets Listed									
8670 000 23687 29PT9521/12									
8670 000 23759 32PW9551/12									
Set Level									
Various									
1099▲	9301 982 90314 W76ERF185X044 32"	2370	2238 586 59812	100nF 20% 50V 0603	2522	4822 126 13881	470pF 5% 50V		
1099▲	9301 991 30314 A68ERF185X044 29"	2371	4822 126 11663	12pF 5% 50V 0603	2523	4822 126 13682	100pF 5% 1kV		
1116	3139 267 20471 Side I/O + Control [D]	2372	4822 126 11663	12pF 5% 50V 0603	2526	2020 552 94427	100pF 5% 50V		
1160	3139 267 21621 Front Interf. assy [J]	2373	4822 126 11663	12pF 5% 50V 0603	2528	4822 121 51252	470nF 5% 63V		
8278	3139 131 07591 Cable 4p280 1100/2x2	2375	3198 017 34730	47nF 16V 0603	2535	2020 021 00092	4700μ 6.3V		
—	—	2381	4822 124 40433	47μF 20% 25V	2536	4822 124 81144	1000μF 16V		
5203▲	2422 549 00132 Coil degaus 29"WR	2382	4822 126 13193	4.7nF 10% 63V	2538	5322 126 11578	1nF 10% 50V 0603		
5203▲	3139 128 23971 Coil degaus HR32WR	2388	4822 126 13193	4.7nF 10% 63V	2539	4822 122 31177	470pF 10% 500V		
5205	3104 308 20961 Coil canceller	2389	2238 586 59812	100nF 20% 50V 0603	2541	4822 124 40433	47μF 20% 25V		
5213	2422 264 00491 Loudsp. 8Ω 10W FR	2390	4822 124 11947	10μF 20% 16V	2542	2020 554 90199	1.5nF 250V		
5214	2422 264 00491 Loudsp. 8Ω 10W FR	2396	2020 558 00039	220pF 10% 3.15kV	2543	2238 586 59812	100nF 20% 50V 0603		
—	—	2404	2038 035 13805	47μF 20% 160V	2544	4822 126 10206	2.2nF 10% 500V		
—	—	2406	2020 558 00039	220pF 10% 3.15kV	2546	2020 552 00183	2.2μF 10% 6.3V 0603		
—	—	2407	2022 333 00295	68nF 5% 400V	2551	4822 126 13449	1nF 10% 2kV		
5203▲	2422 549 00132 Coil degaus 29"WR	2408	4822 126 14043	1μF +80-20% 16V 0805	2552	2020 021 00112	150μ 160V		
5203▲	3139 128 23971 Coil degaus HR32WR	2409	4822 126 12105	33nF 5% 50V 0805	2553	4822 126 14226	82pF 5% 50V 0603		
5205	3104 308 20961 Coil canceller	2410	4822 126 14585	100nF 10% 0805 50V	2561	5322 122 32331	1nF 10% 100V		
5213	2422 264 00491 Loudsp. 8Ω 10W FR	2411	2020 558 90611	1.8nF 10% 2kV	2562	4822 124 12417	2200μF 20% 25V		
5214	2422 264 00491 Loudsp. 8Ω 10W FR	2412	4822 121 70617	10nF 5% 1.6kV	2563	4822 124 12417	2200μF 20% 25V		
—	—	2413	4822 121 10575	27nF 5% 1600V	2564	2222 580 15649	100nF 10% 50V 0805		
—	—	2417	4822 121 10653	22nF 5% 630V	2565	5322 122 32331	1nF 10% 100V		
—	—	2418	2022 333 00091	680nF 10% 250V	2570	2252 811 95017	470pF 10% 250V		
—	—	2419	2022 333 00163	360nF 5% 250V	2571	3198 017 31530	15nF 20% 50V 0603		
—	—	2419	2022 333 00087	510nF 5% 250V	2572	5322 126 11583	10nF 10% 50V 0603		
—	—	2419	2038 301 00312	1.2μF 5% 250V	2578	4822 126 14238	2.2nF 50V 0603		
—	—	2425	5322 126 11583	10nF 10% 50V 0603	2582	4822 124 22726	4.7μF 20% 35V		
—	—	2427	5322 122 31866	6.8nF 10% 63V	2583	2020 552 96637	10μF 10% 6.3V 0805		
—	—	2431	4822 126 13185	680pF 10% 500V	2584	2020 021 91528	560μF 20% 6V3		
—	—	2432	2238 586 59812	100nF 20% 50V 0603	2591	2020 552 00027	4.7μF 2% 6.3V 0603		
—	—	2448	2238 930 55618	470pF 200V	2676	4822 124 40769	4.7μF 20% 100V		
—	—	2449	4822 124 80791	470μF 20% 16V	2682	3198 017 41050	1μF 10V 0603		
—	—	2450	4822 121 40434	330nF 10% 100V	2975	4822 126 13883	220pF 5% 50V		
—	—	2451	4822 121 40434	330nF 10% 100V	2976	4822 126 13883	220pF 5% 50V		
—	—	2452	4822 126 14238	2.2nF 50V 0603	2986	2020 552 96424	100nF 10% 50V 0603		
—	—	2453	4822 126 14238	2.2nF 50V 0603	2987	2020 552 96424	100nF 10% 50V 0603		
—	—	2454	2020 012 00037	2200μF 20% 16V	2988	2020 552 96424	100nF 10% 50V 0603		
—	—	2455	4822 122 32542	47nF 10% 63V	2989	4822 126 13879	220nF +80-20% 16V		
—	—	2456	4822 124 41828	1μF 20% 250V	2990	3198 016 31020	1nF 25V 0603		
—	—	2457	2238 930 55618	470pF 200V	2991	4822 124 80604	47μF 20% 50V		
—	—	2459	2238 930 55618	470pF 200V	2992	4822 126 13879	220nF +80-20% 16V		
—	—	2460	2020 012 00037	2200μF 20% 16V	2993	3198 016 31020	1nF 25V 0603		
—	—	2461	4822 124 41751	47μF 20% 50V	2994	3198 017 34730	47nF 16V 0603		
—	—	2462	2020 552 96683	220nF 10% 50V	2995	3198 017 34730	47nF 16V 0603		
—	—	2463	2020 552 96683	220nF 10% 50V	2996	3198 017 34730	47nF 16V 0603		
—	—	2464	2222 780 19867	2.2μF 16V 0805	2997	3198 017 34730	47nF 16V 0603		
—	—	2465	2222 780 19867	2.2μF 16V 0805	2998	4822 124 80604	47μF 20% 50V		
—	—	2466	2020 557 00005	330pF 100V	3000	3198 021 31820	1.8kΩ 5% 0.062W 0603		
—	—	2468	5322 121 42578	100nF 5% 250V	3241	4822 051 30101	100Ω 5% 0.062W		
—	—	2469	5322 124 40641	10μF 20% 100V	3242	4822 051 30101	100Ω 5% 0.062W		
—	—	2470	2222 930 56627	2.2nF 10% 200V 0805	3305	4822 052 10108	1Ω 5% 0.33W		
—	—	2471	4822 126 14238	2.2nF 50V 0603	3306	4822 052 10568	5.6Ω 5% 0.33W		
—	—	2473	4822 126 14238	2.2nF 50V 0603	3307	4822 052 10568	5.6Ω 5% 0.33W		
—	—	2476	4822 122 33177	10nF 20% 50V	3307	4822 052 11338	3.3Ω 5% 0.5W		
—	—	2477	2238 580 15637	12nF 10% 50V 0805	3317	4822 050 11002	1kΩ 1% 0.4W		
—	—	2478	4822 126 14241	330pF 0603 50V	3318	4822 052 10109	10Ω 5% 0.33W		
—	—	2479	2020 552 96275	12nF 10% 50V 0603	3319	4822 051 30154	150kΩ 5% 0.062W		
—	—	2488	4822 121 51319	1μF 10% 63V	3320	4822 051 30223	22kΩ 5% 0.062W		
—	—	2489	4822 124 40248	10μF 20% 63V	3321	4822 051 30273	27kΩ 5% 0.062W		
—	—	2492	2238 930 55618	470pF 200V	3322	4822 051 30154	150kΩ 5% 0.062W		
—	—	2493	2238 930 55618	470pF 200V	3325	3198 021 31820	1.8kΩ 5% 0.062W 0603		
—	—	2494	2238 930 55618	470pF 200V	3331	4822 116 52175	100Ω 5% 0.5W		
—	—	2495	2020 552 96424	100nF 10% 50V 0603	3332	3198 013 04710	470Ω 20% 0.5W		
—	—	2497	4822 126 13883	220pF 5% 50V	3333	4822 116 52175	100Ω 5% 0.5W		
—	—	2498	4822 126 14585	100nF 10% 0805 50V	3336	3198 013 04710	470Ω 20% 0.5W		
—	—	2499	4822 126 14585	100nF 10% 0805 50V	3337	2322 242 13104	100kΩ 20W		
—	—	2500	2222 338 22474	470nF 20% 275V	3338	4822 051 30222	2.2kΩ 5% 0.062W		
—	—	2501	4822 126 12793	2.2nF 10% 2kV	3339	4822 051 30272	2.7kΩ 5% 0.062W		
—	—	2503	4822 126 12793	2.2nF 10% 2kV	3340	4822 051 30102	1kΩ 5% 0.062W		
—	—	2504	4822 126 12793	2.2nF 10% 2kV	3341	2322 242 13104	100kΩ 20W		
—	—	2505	2020 024 90773	330μF 400V	3342	4822 051 30272	2.7kΩ 5% 0.062W		
—	—	2506	4822 121 10798	33nF 5% 400V	3343	4822 051 30222	2.2kΩ 5% 0.062W		
—	—	2508	2222 338 22104	100nF 20% 275V	3344	4822 050 11002	1kΩ 1% 0.4W		
—	—	2509	4822 126 14335	1nF 10% 0805 100V	3345	4822 050 23309	33Ω 1% 0.6W		
—	—	2510	4822 124 81151	22μF 50V	3347	3198 013 01520	1.5kΩ 20% 0.5W		
—	—	2511	4822 124 81151	22μF 50V	3348	4822 050 11002	1kΩ 1% 0.4W		
—	—	2512	2238 586 59812	100nF 20% 50V 0603	3350	4822 116 52244	15kΩ 5% 0.5W		
—	—	2513	4822 126 13881	470pF 5% 50V	3351	2306 207 03151	150Ω 5% 0.5W		
—	—	2514	4822 126 13862	1.5nF 10% 2kV	3352	2322 242 13104	100kΩ 20W		
—	—	2515	5322 126 11578	1nF 10% 50V 0603	3353	4822 051 30222	2.2kΩ 5% 0.062W		
—	—	2516	2238 586 59812	100nF 20% 50V 0603	3354	4822 051 30272	2.7kΩ 5% 0.062W		
—	—	2517	5322 126 11578	1nF 10% 50V 0603	3355	4822 051 30102	1kΩ 5% 0.062W		
—	—	2519	4822 126 13881	470pF 5% 50V	3357	2122 552 00004	1mA 18V 0603		
—	—	2520	2238 586 59812	100nF 20% 50V 0603	3359	4822 051 30682	6.8Ω 5% 0.062W		
—	—	2521	3198 017 34730	47nF 16V 0603	3360	4822 051 30221			

3361	4822 050 24701	470Ω 1% 0.6W	3520	3198 012 11570	0.15Ω 5% 1W	5408	2422 531 02357	Bridge coil W7132-004Y
3362	2120 108 94133	R Fuse 10Ω	3521	4822 117 11817	1.2kΩ 1% 0.0625W	5410	2422 536 00059	12μH 10%
3363	4822 051 30561	560Ω 5% 0.062W	3522	4822 051 30563	56kΩ 5% 0.062W	5411	4822 157 71097	0.56μH 10%
3364	4822 051 20108	1Ω 5% 0.1W	3523	2122 663 00018	4.7Ω 20%	5450	2422 531 00079	UU 1372.7077D
3365	4822 051 30472	4.7Ω 5% 0.062W	3524	4822 116 52269	3.3kΩ 5% 0.5W	5450▲	2422 531 00081	UU 1372.0130A
3366	4822 051 30683	68kΩ 5% 0.062W	3525	2312 915 13004	300kΩ	5452	4822 157 51462	10μH 10%
3367	4822 116 52297	68kΩ 5% 0.5W	3526	2322 750 61501	150Ω 1206	5453	4822 157 11771	0.09μH 10%
3368	4822 051 30561	560Ω 5% 0.062W	3527	4822 117 12925	47kΩ 1% 0.063W 0603	5456	4822 526 10704	Bead 50 Ω at 100MHz
3370	4822 051 20108	1Ω 5% 0.1W	3528	4822 051 30105	1MΩ 5% 0.062W	5500	4822 157 10476	DMF-2820H
3371	2312 915 11002	1kΩ 1% 0.5W	3529	4822 053 20155	1.5MΩ 5% 0.25W	5501	4822 157 11523	Line filter 5mH/2A
3372	2312 915 11002	1kΩ 1% 0.5W	3530	4822 051 30563	56kΩ 5% 0.062W	5502	2422 549 45296	Mains harm. filter 38mH
3373	2322 257 41152	1.5kΩ 5W	3531	4822 050 11002	1kΩ 1% 0.4W	5511	4822 526 10704	Bead 50 Ω at 100MHz
3375	4822 051 30681	680Ω 5% 0.062W	3532	4822 051 20158	1.5Ω 5% 0.1W	5512	2422 531 02632	SS42316-0
3377	4822 051 30272	2.7kΩ 5% 0.062W	3533	4822 051 20128	1R20 5% 0.1W	5532	4822 526 10704	Bead 50 Ω at 100MHz
3378	4822 051 30221	220Ω 5% 0.062W	3534	2322 734 63309	33Ω 1% 0.1W 0805	5551	4822 526 10704	Bead 50 Ω at 100MHz
3380	4822 051 30222	2.2kΩ 5% 0.062W	3535	4822 052 11108	1Ω 5% 0.5W	5552	4822 157 71401	27μH
3381	4822 051 30222	2.2kΩ 5% 0.062W	3536	4822 052 10221	220Ω 5% 0.33W	5561	4822 526 10704	Bead 50 Ω at 100MHz
3385	4822 051 30681	680Ω 5% 0.062W	3540	4822 051 30683	68kΩ 5% 0.062W	5562	4822 526 10704	Bead 50 Ω at 100MHz
3389	2120 108 94132	1Ω 1206	3541	4822 117 12925	47kΩ 1% 0.063W 0603	5564	2422 535 94637	4.7μH 20% LHL08
3392	4822 051 30271	270Ω 5% 0.062W	3542	4822 051 30681	680Ω 5% 0.062W	5565	4822 157 11411	Bead 80Ω at 100MHz
3393	4822 051 30109	10Ω 5% 0.062W	3543	4822 051 30103	10kΩ 5% 0.062W	5566	4822 157 11411	Bead 80Ω at 100MHz
3394	4822 051 30472	4.7Ω 5% 0.062W	3544	2322 704 62202	2.2kΩ 1% 0603	5567	4822 157 11411	Bead 80Ω at 100MHz
3395	4822 116 52219	330Ω 5% 0.5W	3545	2322 704 62202	2.2kΩ 1% 0603			
3396	3198 021 31820	1.8kΩ 5% 0.062W 0603	3546	4822 051 30683	68kΩ 5% 0.062W			
3397	2122 552 00004	1mA 18V 0603	3549	4822 117 12925	47kΩ 1% 0.063W 0603			
3401	4822 050 24703	47kΩ 1% 0.6W	3550	4822 116 52269	3.3kΩ 5% 0.5W	6234	9322 077 99685	BZX384-B33-V
3402	4822 116 52219	330Ω 5% 0.5W	3551	4822 051 30223	22kΩ 5% 0.062W	6307	4822 130 11416	PDZ6.8B
3403	4822 050 11002	1kΩ 1% 0.4W	3553	2322 704 61803	18kΩ 1% 0603	6325	4822 130 10838	UDZ3.3B
3408	4822 117 13632	100kΩ 1% 0603 0.62W	3554	4822 051 30103	10kΩ 5% 0.062W	6331	9322 197 45703	BAV21WS
3410	4822 051 30221	220Ω 5% 0.062W	3563	4822 116 83872	220Ω 5% 0.5W	6332	9322 197 45703	BAV21WS
3411	4822 051 30102	1kΩ 5% 0.062W	3565	4822 051 30273	27kΩ 5% 0.062W	6333	9322 197 45703	BAV21WS
3413	2122 101 01386	1kΩ 5% CRB 0.25W	3567	4822 051 30154	150kΩ 5% 0.062W	6334	4822 130 10838	UDZ3.3B
3414	4822 050 24708	4.7Ω 1% 0.6W	3568	4822 117 12925	47kΩ 1% 0.063W 0603	6361	4822 130 11397	BAS316
3415	4822 050 24708	4.7Ω 1% 0.6W	3571	4822 116 52228	680Ω 5% 0.5W	6362	4822 130 11397	BAS316
3416	4822 051 20479	47Ω 5% 0.1W	3573	4822 051 30153	15kΩ 5% 0.062W	6403	9322 185 83668	SM ES1D
3417	4822 051 30684	680kΩ 5% 0.062W	3574	2322 702 60184	180kΩ 5% 0603	6404	9322 169 61687	DMV1500M
3418	4822 050 11002	1kΩ 1% 0.4W	3575	4822 050 28203	82kΩ 1% 0.6W	6405	4822 130 11397	BAS316
3418	4822 116 52269	3.3kΩ 5% 0.5W	3576	5322 117 13034	1.5kΩ 1% 0.063W 0603	6409	4822 130 11397	BAS316
3419	4822 050 24708	4.7Ω 1% 0.6W	3579	4822 116 52256	2.2kΩ 5% 0.5W	6410	4822 130 11397	BAS316
3421	4822 116 52182	15Ω 5% 0.5W	3580	4822 117 12891	220kΩ 1%	6411	4822 130 11397	BAS316
3425	4822 050 21004	100kΩ 1% 0.6W	3588	4822 051 30334	330kΩ 5% 0.062W	6452	4822 130 31607	RGP10D
3425	4822 050 28203	82kΩ 1% 0.6W	3589	4822 051 30103	10kΩ 5% 0.062W	6453	9334 939 60673	RGP10G
3426	4822 052 10398	3.9Ω 5% 0.33W	3593	4822 051 30103	10kΩ 5% 0.062W	6456	5322 130 31938	BYV27-200
3432	4822 050 24708	4.7Ω 1% 0.6W	3594	4822 051 30223	22kΩ 5% 0.062W	6457	5322 130 31938	BYV27-200
3433	4822 053 12279	27Ω 5% 3W	3595	4822 117 13632	100kΩ 1% 0603 0.62W	6458	9340 548 69115	PDZ27B
3450	4822 052 10828	8.2Ω 5% 0.33W	3596	4822 051 30392	3.9Ω 5% 0.063W 0603	6459	9340 548 69115	PDZ27B
3451	4822 050 24708	4.7Ω 1% 0.6W	3597	4822 117 12891	220kΩ 1%	6461	9322 128 65685	RS1G
3452	2138 112 01568	5.6Ω 5% 0805	3598	4822 053 20334	330kΩ 5% 0.25W	6464	9340 548 69115	PDZ27B
3455	4822 052 11108	1Ω 5% 0.5W	3688	4822 051 30103	10kΩ 5% 0.062W	6465	9340 260 20115	BAW56W
3456	2306 207 03277	Ω	3985	4822 051 30103	10kΩ 5% 0.062W	6466	9322 185 83668	SM ES1D
3458	4822 052 11478	4.7Ω 5% 0.5W	3988	4822 051 30123	12kΩ 5% 0.1W	6469	3139 120 52021	BYV29X-500
3459	4822 051 30102	1kΩ 5% 0.062W	3989	4822 051 30109	10Ω 5% 0.062W	6471	4822 130 31607	RGP10D
3461	4822 051 30152	1.5Ω 5% 0.062W	3991	4822 051 30103	10kΩ 5% 0.062W	6474	4822 130 34379	BZX79-B27
3463	4822 051 30152	1.5Ω 5% 0.062W	3992	4822 051 30123	12kΩ 5% 0.1W	6476	5322 130 32296	BZV85-C10
3466	4822 052 10568	5.6Ω 5% 0.33W	3993	4822 051 30109	10Ω 5% 0.062W	6478	4822 130 10837	UDZS8.2B
3467	4822 116 83872	220Ω 5% 0.5W	3998	4822 117 11817	1.2kΩ 1% 0.0625W	6500	3198 010 10640	Bridge cell GBU4K
3468	4822 052 10228	2.2Ω 5% 0.33W	4303	4822 051 30008	Jumper 0603	6509	4822 130 31607	RGP10D
3470	3198 039 27080	2.7Ω 1%	4304	4822 051 30008	Jumper 0603	6511	4822 130 31607	RGP10D
3471	3198 039 27080	2.7Ω 1%	4305	4822 051 30008	Jumper 0603	6512	4822 130 11397	BAS316
3471	4822 050 22208	2.2Ω 1% 0.6W	4306	4822 051 30008	Jumper 0603	6514	4822 130 11397	BAS316
3472	4822 050 22208	2.2Ω 1% 0.6W	4307	4822 051 30008	Jumper 0603	6532	9322 197 45703	BAV21WS
3473	2322 194 63109	10Ω 5% 2W	4308	4822 051 30008	Jumper 0603	6533	9322 197 45703	BAV21WS
3477	4822 116 52231	820Ω 5% 0.5W	4309	4822 051 30008	Jumper 0603	6536	9322 212 98673	SB260
3479	4822 051 30102	1kΩ 5% 0.062W	4310	4822 051 20008	Jumper 0805	6538	4822 130 11397	BAS316
3480	4822 051 30183	18kΩ 5% 0.062W	4321	4822 051 30008	Jumper 0603	6539	9322 212 82685	UDZS13B
3480	4822 051 30683	68kΩ 5% 0.062W	4322	4822 051 20008	Jumper 0805	6540	4822 130 11152	UDZ18B
3485	4822 052 10228	2.2Ω 5% 0.33W	4330	4822 051 30008	Jumper 0603	6541	9322 129 41685	BZM55-C12
3486	4822 052 10108	1Ω 5% 0.33W	4402	4822 051 30008	Jumper 0603	6548	9322 161 46687	STPS745FP
3487	2120 105 00041	820Ω 5% 2W	4403	4822 051 30008	Jumper 0603	6551	9337 443 80127	BYT28-500
3487	4822 053 11102	1kΩ 5% 2W	4420	4822 051 20008	Jumper 0805	6562	9322 161 78682	SB360L-7024
3488	4822 053 20224	220Ω 5% 0.25W	4421	4822 051 30008	Jumper 0603	6563	9322 161 78682	SB360L-7024
3490	4822 050 21501	150Ω 1% 0.6W	4501	4822 051 20008	Jumper 0805	6564	4822 130 11397	BAS316
3491	4822 051 30273	27kΩ 5% 0.062W	4550	4822 051 30008	Jumper 0603	6565	4822 130 10837	UDZS8.2B
3491	4822 051 30683	68kΩ 5% 0.062W	4685	4822 051 30008	Jumper 0603	6567	4822 130 11397	BAS316
3492	4822 116 52283	4.7Ω 5% 0.5W	4723	4822 051 30008	Jumper 0603	6575	4822 130 31878	1N4003G
3493	4822 052 11228	2R20 5% 0.5W	4905	4822 051 20008	Jumper 0805	6578	4822 130 11397	BAS316
3494	4822 116 21239	VDR 1mA/612V	4973	4822 051 20008	Jumper 0805	6581	9322 197 45703	BAV21WS
3495	4822 116 21239	VDR 1mA/612V	4974	4822 051 20008	Jumper 0805	6682	4822 130 10838	UDZ3.3B
3499	4822 116 52285	470kΩ 5% 0.5W	4985	4822 051 30008	Jumper 0603	6684	9322 163 91685	BZX384-C6V2
3502	4822 116 83872	220Ω 5% 0.5W						
3503	4822 252 11215	DSP301N-A21F						
3504	4822 053 21155	1.5MΩ 5% 0.5W						
3505	2122 550 00171	1mΩ 612V						

7365	4822 130 60887	BF840	2107	2020 552 96823	10 μ F 16V	2341	3198 017 41050	1 μ F 10V 0603
7366	9352 628 51112	TDA8941p/N1	2108	2020 552 96823	10 μ F 16V	2342	3198 016 31020	1nF 25V 0603
7403	4822 130 44568	BC557B	2109	2020 552 96823	10 μ F 16V	2343	5322 126 11583	10nF 10% 50V 0603
7404	9340 547 13215	BSH103	2110	2020 552 96823	10 μ F 16V	2344	3198 017 41050	1 μ F 10V 0603
7405	9340 591 84127	ON5277	2112	5322 126 11583	10nF 10% 50V 0603	2345	3198 016 31020	1nF 25V 0603
7406	9322 191 77687	STP6NK60ZFP	2113	2020 552 96823	10 μ F 16V	2346	5322 126 11583	10nF 10% 50V 0603
7406	9322 194 27687	STP3NK60ZFP	2114	2020 552 96823	10 μ F 16V	2347	4822 126 14507	18pF 5% 50V 0603
7407	4822 130 10255	MUN2213	2115	3198 016 31020	1nF 25V 0603	2348	4822 126 14507	18pF 5% 50V 0603
7455	9352 637 54112	TDA4863J/V1	2116	4822 126 14241	330pF 0603 50V	2349	3198 016 31020	1nF 25V 0603
7510	9352 720 43118	TEA1506T/N1	2117	4822 126 14491	2.2 μ F 10V 0805	2350	3198 016 31020	1nF 25V 0603
7511	9352 720 43118	TEA1506T/N1	2118	3198 016 31020	1nF 25V 0603	2400	2020 552 96823	10 μ F 16V
7512	9322 229 41687	FET STF8NK85Z	2119	4822 126 14241	330pF 0603 50V	2401	2238 586 59812	100nF 20% 50V 0603
7513	8238 274 02070	TCET1103G	2120	4822 126 14491	2.2 μ F 10V 0805	2403	2238 586 59812	100nF 20% 50V 0603
7516	8238 274 02070	TCET1103G	2121	3198 016 31020	1nF 25V 0603	2404	4822 124 80151	47 μ F 16V
7517	5322 130 60159	BC846B	2122	4822 126 14241	330pF 0603 50V	2405	2238 586 59812	100nF 20% 50V 0603
7525	9322 194 21687	STP5NK80ZFP	2123	4822 126 14491	2.2 μ F 10V 0805	2406	2020 552 94743	5.6nF 50V 0603
7532	4822 130 60373	BC856B	2124	3198 016 31020	1nF 25V 0603	2407	4822 122 33741	10pF 10% 50V
7541	4822 130 60373	BC856B	2125	4822 126 14241	330pF 0603 50V	2408	4822 126 14225	56pF 5% 50V 0603
7542	4822 209 14933	TL431IZ	2126	4822 126 14491	2.2 μ F 10V 0805	2410	3198 016 31020	1nF 25V 0603
7545	9322 179 08685	SI2305DS	2128	2020 552 96823	10 μ F 16V	2412	2238 586 59812	100nF 20% 50V 0603
7547	4822 130 11155	PDT1C14ET	2150	4822 126 14241	330pF 0603 50V	2413	2020 552 96823	10 μ F 16V
7548	4822 130 11155	PDT1C14ET	2151	4822 126 14508	180pF 5% 50V 0603	2414	2238 586 59812	100nF 20% 50V 0603
7549	4822 130 62343	IMX1	2152	4822 126 14241	330pF 0603 50V	2415	3198 016 31020	1nF 25V 0603
7561	3198 010 42310	BC847BW	2153	4822 126 14508	180pF 5% 50V 0603	2416	4822 126 14225	56pF 5% 50V 0603
7567	5322 130 60159	BC846B	2155	2020 552 00183	2.2 μ F 10% 6.3V 0603	2418	2238 586 59812	100nF 20% 50V 0603
7571	4822 209 14933	TL431IZ	2157	2020 552 00183	2.2 μ F 10% 6.3V 0603	2420	2238 586 59812	100nF 20% 50V 0603
7573	4822 130 11155	PDT1C14ET	2162	4822 126 14241	330pF 0603 50V	2421	4822 126 14225	56pF 5% 50V 0603
7990	4822 209 32641	TDA2616Q	2163	4822 126 14508	180pF 5% 50V 0603	2423	4822 121 70159	0.1 μ F 16V
7991	4822 130 63732	MMUN2212	2164	4822 126 14241	330pF 0603 50V	2424	2020 552 94427	100pF 5% 50V
			2165	4822 126 14508	180pF 5% 50V 0603	2425	2020 552 94427	100pF 5% 50V
			2167	2020 552 00183	2.2 μ F 10% 6.3V 0603	2426	4822 126 11785	47pF 5% 50V 0603
			2168	2238 586 59812	100nF 20% 50V 0603	2427	4822 126 11669	27pF 5% 50V 0603
			2169	2020 552 00183	2.2 μ F 10% 6.3V 0603	2428	4822 126 11669	27pF 5% 50V 0603
			2176	2238 586 59812	100nF 20% 50V 0603	2429	3198 017 34730	47nF 16V 0603
			2177	2238 586 59812	100nF 20% 50V 0603	2430	2020 552 96807	1 μ F 10% 10V 0603
			2201	2238 586 59812	100nF 20% 50V 0603	2432	3198 017 41050	1 μ F 10V 0603
			2202	3198 016 31590	15pF 10% 50V 0603	2433	2238 586 59812	100nF 20% 50V 0603
			2203	3198 016 31590	15pF 10% 50V 0603	2434	4822 126 13193	4.7nF 10% 63V
			2204	2020 552 94427	100pF 5% 50V	2435	3198 030 82280	2.2 μ F 20% 50V
			2205	2020 552 96823	10 μ F 16V	2436	4822 126 13881	470pF 5% 50V
			2206	3198 016 31020	1nF 25V 0603	2437	3198 016 38210	820pF 25V 0603
			2207	2020 552 94427	100pF 5% 50V	2444	3198 017 33330	33nF 20% 16V 0603
			2208	2020 552 94427	100pF 5% 50V	2500	2020 552 96823	10 μ F 16V
			2209	2020 552 94427	100pF 5% 50V	2502	2020 552 00183	2.2 μ F 10% 6.3V 0603
			2210	2020 552 94427	100pF 5% 50V	2503	2238 586 59812	100nF 20% 50V 0603
			2213	2020 552 96823	10 μ F 16V	2506	4822 123 14018	2.2 μ F 10% 10V
			2214	4822 124 23002	10 μ F 16V	2507	2020 552 96822	10 μ F 10% 25V 1210
			2215	2238 586 59812	100nF 20% 50V 0603	2508	2020 552 96822	10 μ F 10% 25V 1210
			2216	2238 586 59812	100nF 20% 50V 0603	2509	2238 916 15641	22nF 10% 25V 0603
			2218	2238 586 59812	100nF 20% 50V 0603	2510	4822 126 13883	220pF 5% 50V
			2219	2238 586 59812	100nF 20% 50V 0603	2511	2022 031 00373	470 μ F 20% 16V
			2222	5322 126 11583	10nF 10% 50V 0603	2512	2022 031 00373	470 μ F 20% 16V
			2223	5322 126 11583	10nF 10% 50V 0603	2513	2238 586 59812	100nF 20% 50V 0603
			2300	5322 124 41945	22 μ F 20% 35V	2515	2020 552 96823	10 μ F 16V
			2301	2238 586 59812	100nF 20% 50V 0603	2560	2238 586 59812	100nF 20% 50V 0603
			2302	2020 021 91557	100 μ F 20% 16V	2587	3198 017 44740	470nF 10V 0603
			2303	5322 126 11583	10nF 10% 50V 0603	2600	2020 552 96637	10 μ F 10% 6.3V 0805
			2304	2020 552 96823	10 μ F 16V	2601	2238 586 59812	100nF 20% 50V 0603
			2305	2238 586 59812	100nF 20% 50V 0603	2602	2238 586 59812	100nF 20% 50V 0603
			2306	2238 586 59812	100nF 20% 50V 0603	2603	2238 586 59812	100nF 20% 50V 0603
			2307	2238 586 59812	100nF 20% 50V 0603	2604	2238 586 59812	100nF 20% 50V 0603
			2308	2238 586 59812	100nF 20% 50V 0603	2605	2020 552 96823	10 μ F 16V
			2309	3198 016 31020	1nF 25V 0603	2606	2020 552 96637	10 μ F 10% 6.3V 0805
			2310	3198 016 31020	1nF 25V 0603	2607	2238 586 59812	100nF 20% 50V 0603
			2311	3198 016 31020	1nF 25V 0603	2608	2238 586 59812	100nF 20% 50V 0603
			2312	3198 016 31020	1nF 25V 0603	2609	2238 586 59812	100nF 20% 50V 0603
			2313	3198 016 31020	1nF 25V 0603	2610	2238 586 59812	100nF 20% 50V 0603
			2314	3198 016 31020	1nF 25V 0603	2611	2020 552 96823	10 μ F 16V
			2315	3198 016 31020	1nF 25V 0603	2614	2020 552 96823	10 μ F 16V
			2316	3198 016 31020	1nF 25V 0603	2615	2020 552 96637	10 μ F 10% 6.3V 0805
			2317	2020 552 96823	10 μ F 16V	2616	2238 586 59812	100nF 20% 50V 0603
			2318	2238 586 59812	100nF 20% 50V 0603	2617	2238 586 59812	100nF 20% 50V 0603
			2319	2238 586 59812	100nF 20% 50V 0603	2618	3198 017 34730	47nF 16V 0603
			2320	2238 586 59812	100nF 20% 50V 0603	2619	3198 017 34730	47nF 16V 0603
			2321	2238 586 59812	100nF 20% 50V 0603	2620	3198 017 34730	47nF 16V 0603
			2322	3198 016 31020	1nF 25V 0603	2622	2238 786 55648	82nF 10% 16V 0603
			2323	3198 016 31020	1nF 25V 0603	2623	2238 586 15635	8.2nF 10% 50V 0603
			2324	3198 016 31020	1nF 25V 0603	2624	2238 586 59812	100nF 20% 50V 0603
			2325	3198 016 31020	1nF 25V 0603	2625	2020 552 96823	10 μ F 16V
			2326	3198 016 31020	1nF 25V 0603	2626	2238 586 59812	100nF 20% 50V 0603
			2327	3198 016 31020	1nF 25V 0603	2627	2020 552 96823	10 μ F 16V
			2328	2020 552 96823	10 μ F 16V	2635	4822 124 11131	47 μ F 6.3V
			2329	2238 586 59812	100nF 20% 50V 0603	2636	4822 124 11131	47 μ F 6.3V
			2330	2238 586 59812	100nF 20% 50V 0603	2650	5322 126 11583	10nF 10% 50V 0603
			2331	3198 016 31020	1nF 25V 0603	2651	5322 126 11583	10nF 10% 50V 0603
			2332	3198 016 31020	1nF 25V 0603	2652	5322 126 11583	10nF 10% 50V 0603
			2333	3198 016 31020	1nF 25V 0603	2653	5322 126 11583	10nF 10% 50V 0603
			2334	3198 016 31020	1nF 25V 0			

2661	5322 126 11583	10nF 10% 50V 0603	2789	2238 586 15628	2.7nF 10% 50V 0603	3102	4822 117 12925	47kΩ 1% 0.063W 0603
2662	5322 126 11583	10nF 10% 50V 0603	2790	2020 552 96823	10μF 16V	3103	4822 051 30101	100Ω 5% 0.062W
2663	5322 126 11583	10nF 10% 50V 0603	2791	2238 586 59812	100nF 20% 50V 0603	3104	4822 051 30759	75Ω 5% 0.062W
2664	5322 126 11583	10nF 10% 50V 0603	2792	2238 586 59812	100nF 20% 50V 0603	3105	4822 051 30151	150Ω 5% 0.062W
2665	5322 126 11583	10nF 10% 50V 0603	2794	2020 552 96823	10μF 16V	3106	4822 117 12925	47kΩ 1% 0.063W 0603
2666	5322 126 11583	10nF 10% 50V 0603	2795	2238 586 59812	100nF 20% 50V 0603	3107	4822 051 30759	75Ω 5% 0.062W
2680	5322 126 11583	10nF 10% 50V 0603	2796	2238 586 59812	100nF 20% 50V 0603	3108	4822 051 30101	100Ω 5% 0.062W
2681	5322 126 11583	10nF 10% 50V 0603	2797	2238 586 59812	100nF 20% 50V 0603	3109	4822 051 30759	75Ω 5% 0.062W
2682	5322 126 11583	10nF 10% 50V 0603	2798	2020 552 96823	10μF 16V	3110	4822 051 30221	220Ω 5% 0.062W
2701	2238 586 59812	100nF 20% 50V 0603	2901	4822 126 14225	56pF 5% 50V 0603	3111	4822 051 30151	150Ω 5% 0.062W
2702	2238 586 59812	100nF 20% 50V 0603	2902	4822 126 14225	56pF 5% 50V 0603	3114	4822 051 30101	100Ω 5% 0.062W
2703	5322 126 11583	10nF 10% 50V 0603	2903	4822 126 14225	56pF 5% 50V 0603	3115	4822 051 30479	47Ω 5% 0.062W
2704	2238 586 59812	100nF 20% 50V 0603	2906	5322 126 11578	1nF 10% 50V 0603	3116	4822 051 30479	47Ω 5% 0.062W
2705	2238 586 59812	100nF 20% 50V 0603	2907	5322 126 11578	1nF 10% 50V 0603	3117	4822 051 30471	47Ω 5% 0.062W
2706	5322 126 11583	10nF 10% 50V 0603	2908	5322 126 11578	1nF 10% 50V 0603	3118	4822 051 30223	22kΩ 5% 0.062W
2707	5322 126 11583	10nF 10% 50V 0603	2909	5322 126 11578	1nF 10% 50V 0603	3120	4822 051 30222	2.2kΩ 5% 0.062W
2708	5322 126 11583	10nF 10% 50V 0603	2910	2020 552 96823	10μF 16V	3121	4822 051 30561	560Ω 5% 0.062W
2709	5322 126 11583	10nF 10% 50V 0603	2911	4822 126 14241	330pF 0603 50V	3122	4822 051 30471	47Ω 5% 0.062W
2710	5322 126 11583	10nF 10% 50V 0603	2912	5322 126 11578	1nF 10% 50V 0603	3123	4822 051 30223	22kΩ 5% 0.062W
2711	4822 126 13193	4.7nF 10% 63V	2913	5322 126 11578	1nF 10% 50V 0603	3124	4822 051 30151	150Ω 5% 0.062W
2712	4822 126 13193	4.7nF 10% 63V	2914	5322 126 11578	1nF 10% 50V 0603	3125	4822 117 12925	47kΩ 1% 0.063W 0603
2713	2238 586 59812	100nF 20% 50V 0603	2915	5322 126 11578	1nF 10% 50V 0603	3126	4822 051 30151	150Ω 5% 0.062W
2714	2238 586 59812	100nF 20% 50V 0603	2918	4822 126 13881	470pF 5% 50V	3127	4822 117 12925	47kΩ 1% 0.063W 0603
2715	5322 126 11583	10nF 10% 50V 0603	2920	2238 586 59812	100nF 20% 50V 0603	3128	4822 117 12891	220kΩ 1%
2716	2020 552 96823	10μF 16V	2921	3198 016 33380	3.3pF 50V 0603	3129	4822 051 30151	150Ω 5% 0.062W
2717	2020 552 96823	10μF 16V	2922	3198 016 33380	3.3pF 50V 0603	3130	4822 117 12891	220kΩ 1%
2718	2020 552 96823	10μF 16V	2923	3198 016 31020	1nF 25V 0603	3131	4822 117 13632	100kΩ 1% 0.063 0.62W
2719	2238 586 59812	100nF 20% 50V 0603	2924	3198 016 31020	1nF 25V 0603	3132	4822 117 13632	100kΩ 1% 0.063 0.62W
2720	4822 126 14225	56pF 5% 50V 0603	2925	3198 016 31020	1nF 25V 0603	3133	4822 117 13632	100kΩ 1% 0.063 0.62W
2721	2238 586 59812	100nF 20% 50V 0603	2926	3198 016 31020	1nF 25V 0603	3134	4822 117 13632	100kΩ 1% 0.063 0.62W
2722	2238 586 15628	2.7nF 10% 50V 0603	2927	3198 016 31020	1nF 25V 0603	3135	4822 117 13632	100kΩ 1% 0.063 0.62W
2723	2020 552 96749	20pF 5% 50V 0603	2928	3198 016 31020	1nF 25V 0603	3136	4822 117 13632	100kΩ 1% 0.063 0.62W
2724	2020 552 96749	20pF 5% 50V 0603	2929	2238 586 59812	100nF 20% 50V 0603	3137	4822 117 13632	100kΩ 1% 0.063 0.62W
2725	2238 586 59812	100nF 20% 50V 0603	2930	2020 552 96823	10μF 16V	3138	4822 117 13632	100kΩ 1% 0.063 0.62W
2726	2238 586 59812	100nF 20% 50V 0603	2931	2238 586 59812	100nF 20% 50V 0603	3139	4822 051 30472	4.7Ω 5% 0.062W
2727	2238 586 59812	100nF 20% 50V 0603	2932	2020 552 96823	10μF 16V	3140	4822 051 30472	4.7Ω 5% 0.062W
2728	2238 586 59812	100nF 20% 50V 0603	2933	2238 586 59812	100nF 20% 50V 0603	3141	4822 051 30472	4.7Ω 5% 0.062W
2729	2238 586 59812	100nF 20% 50V 0603	2934	5322 124 41945	22μF 20% 35V	3142	4822 051 30472	4.7Ω 5% 0.062W
2730	2020 552 96823	10μF 16V	2935	2020 552 96823	10μF 16V	3156	4822 051 30759	75Ω 5% 0.062W
2731	2238 586 59812	100nF 20% 50V 0603	2936	3198 016 31020	1nF 25V 0603	3157	4822 051 30759	75Ω 5% 0.062W
2732	2020 552 96747	30pF 5% 50V 0603	2937	2020 552 00132	2.2μF 10% 10V	3158	4822 051 30759	75Ω 5% 0.062W
2733	2238 586 59812	100nF 20% 50V 0603	2938	3198 016 31020	1nF 25V 0603	3159	4822 051 30682	6.8Ω 5% 0.062W
2734	2238 586 59812	100nF 20% 50V 0603	2939	2020 552 00132	2.2μF 10% 10V	3160	4822 117 12925	47kΩ 1% 0.063W 0603
2735	2238 586 59812	100nF 20% 50V 0603	2940	3198 017 42240	220nF 16V Y5V 0603	3161	4822 051 30273	27kΩ 5% 0.062W
2736	2238 586 59812	100nF 20% 50V 0603	2941	2020 552 96823	10μF 16V	3162	4822 051 30101	100Ω 5% 0.062W
2737	2238 586 59812	100nF 20% 50V 0603	2942	2020 552 96823	10μF 16V	3163	4822 051 30759	75Ω 5% 0.062W
2738	2238 586 59812	100nF 20% 50V 0603	2943	2020 552 96823	10μF 16V	3164	4822 051 30689	68Ω 5% 0.063W 0603
2740	2238 586 59812	100nF 20% 50V 0603	2944	2238 586 59812	100nF 20% 50V 0603	3165	4822 051 30102	1kΩ 5% 0.062W
2741	2238 586 59812	100nF 20% 50V 0603	2945	2020 552 96823	10μF 16V	3168	4822 051 30101	100Ω 5% 0.062W
2743	2238 586 59812	100nF 20% 50V 0603	2946	2020 552 96637	10μF 10% 6.3V 0805	3169	4822 051 30759	75Ω 5% 0.062W
2744	2238 586 59812	100nF 20% 50V 0603	2947	2020 552 96637	10μF 10% 6.3V 0805	3170	4822 051 30151	150Ω 5% 0.062W
2746	2238 586 59812	100nF 20% 50V 0603	2948	2020 552 96637	10μF 10% 6.3V 0805	3171	4822 117 12891	220kΩ 1%
2747	2238 586 59812	100nF 20% 50V 0603	2949	2020 552 96637	10μF 10% 6.3V 0805	3172	4822 051 30151	150Ω 5% 0.062W
2748	2238 586 59812	100nF 20% 50V 0603	2950	2238 586 59812	100nF 20% 50V 0603	3173	4822 117 12925	47kΩ 1% 0.063W 0603
2749	2020 552 96823	10μF 16V	2951	2238 586 59812	100nF 20% 50V 0603	3174	4822 051 30151	150Ω 5% 0.062W
2750	2238 586 59812	100nF 20% 50V 0603	2952	2020 552 96637	10μF 10% 6.3V 0805	3175	4822 117 12891	220kΩ 1%
2751	2022 552 05636	10μF 10% 16V 1210	2953	2020 552 96637	10μF 10% 6.3V 0805	3176	4822 051 30151	150Ω 5% 0.062W
2752	2020 552 96823	10μF 16V	2954	4822 126 14491	2.2μF 10V 0805	3177	4822 117 12925	47kΩ 1% 0.063W 0603
2753	2238 586 59812	100nF 20% 50V 0603	2955	4822 126 14491	2.2μF 10V 0805	3178	4822 051 30759	75Ω 5% 0.062W
2754	2238 586 59812	100nF 20% 50V 0603	2961	2238 586 59812	100nF 20% 50V 0603	3184	4822 051 30273	27kΩ 5% 0.062W
2755	2238 586 59812	100nF 20% 50V 0603	2962	4822 124 11131	47μF 6.3V	3185	4822 051 30682	6.8Ω 5% 0.062W
2756	2238 586 59812	100nF 20% 50V 0603	2963	4822 124 11131	47μF 6.3V	3186	4822 051 30479	47Ω 5% 0.062W
2757	2238 586 59812	100nF 20% 50V 0603	2964	2238 586 59812	100nF 20% 50V 0603	3187	4822 051 30479	47Ω 5% 0.062W
2758	2238 586 59812	100nF 20% 50V 0603	2965	3198 017 41050	1μF 10V 0603	3188	4822 051 30689	68Ω 5% 0.063W 0603
2759	2238 586 59812	100nF 20% 50V 0603	2966	5322 126 11583	10nF 10% 50V 0603	3189	4822 051 30102	1kΩ 5% 0.062W
2760	2238 586 59812	100nF 20% 50V 0603	2967	3198 017 41050	1μF 10V 0603	3191	4822 051 30759	75Ω 5% 0.062W
2761	2238 586 59812	100nF 20% 50V 0603	2968	5322 126 11583	10nF 10% 50V 0603	3192	4822 051 30101	100Ω 5% 0.062W
2762	2238 586 59812	100nF 20% 50V 0603	2969	4822 124 11131	47μF 6.3V	3193	4822 051 30151	150Ω 5% 0.062W
2763	2238 586 59812	100nF 20% 50V 0603	2970	2238 586 59812	100nF 20% 50V 0603	3194	4822 117 12891	220kΩ 1%
2764	2238 586 59812	100nF 20% 50V 0603	2971	4822 124 23002	10μF 16V	3195	4822 051 30151	150Ω 5% 0.062W
2765	2238 586 59812	100nF 20% 50V 0603	2972	4822 126 14491	2.2μF 10V 0805	3196	4822 117 12925	47kΩ 1% 0.063W 0603
2766	2238 586 59812	100nF 20% 50V 0603	2973	4822 126 14491	2.2μF 10V 0805	3197	4822 051 30151	150Ω 5% 0.062W

3221	4822 051 30103	10kΩ 5% 0.062W	3425	4822 051 30101	100Ω 5% 0.062W	3758	3198 021 32290	22Ω 5% 0603
3222	4822 051 30103	10kΩ 5% 0.062W	3426	4822 051 30471	47Ω 5% 0.062W	3759	3198 021 32290	22Ω 5% 0603
3223	4822 051 30101	100Ω 5% 0.062W	3427	4822 051 30471	47Ω 5% 0.062W	3760	3198 021 32290	22Ω 5% 0603
3224	4822 051 30101	100Ω 5% 0.062W	3428	4822 051 30101	100Ω 5% 0.062W	3761	3198 021 32290	22Ω 5% 0603
3225	4822 051 30472	4.7Ω 5% 0.062W	3429	4822 051 30103	10kΩ 5% 0.062W	3790	3198 021 32290	22Ω 5% 0603
3227	4822 051 30101	100Ω 5% 0.062W	3430	4822 051 30472	4.7Ω 5% 0.062W	3791	3198 021 32290	22Ω 5% 0603
3228	4822 051 30101	100Ω 5% 0.062W	3431	4822 051 30222	2.2kΩ 5% 0.062W	3792	3198 021 32290	22Ω 5% 0603
3229	4822 051 30103	10kΩ 5% 0.062W	3432	4822 051 30475	4.7MΩ 5% 0.062W 0603	3793	3198 021 32290	22Ω 5% 0603
3230	4822 051 30103	10kΩ 5% 0.062W	3433	4822 051 30152	1.5Ω 5% 0.062W	3902	4822 051 30101	100Ω 5% 0.062W
3231	4822 051 30103	10kΩ 5% 0.062W	3436	4822 051 30102	1kΩ 5% 0.062W	3903	4822 051 30101	100Ω 5% 0.062W
3232	4822 051 30479	47Ω 5% 0.062W	3437	4822 117 12864	82kΩ 5% 0.6W	3913	4822 051 30101	100Ω 5% 0.062W
3233	4822 051 30101	100Ω 5% 0.062W	3438	4822 051 30334	330kΩ 5% 0.062W	3914	4822 051 30101	100Ω 5% 0.062W
3234	4822 051 30682	6.8Ω 5% 0.062W	3439	4822 051 30561	560Ω 5% 0.062W	3915	4822 051 30101	100Ω 5% 0.062W
3236	4822 051 30101	100Ω 5% 0.062W	3440	4822 051 30393	39kΩ 5% 0.062W	3916	4822 051 30101	100Ω 5% 0.062W
3237	4822 051 30101	100Ω 5% 0.062W	3441	2322 704 62003	20kΩ 1% 0603	3917	4822 051 30223	22kΩ 5% 0.062W
3238	4822 051 30101	100Ω 5% 0.062W	3442	4822 051 30474	470kΩ 5% 0.062W	3925	4822 051 30331	330Ω 5% 0.062W
3239	3198 021 31080	1Ω 5% 0603	3443	4822 117 12889	270kΩ 1% 0.063W 0603	3926	4822 051 30331	330Ω 5% 0.062W
3240	4822 051 30472	4.7Ω 5% 0.062W	3444	4822 051 30222	2.2kΩ 5% 0.062W	3927	4822 051 30331	330Ω 5% 0.062W
3241	4822 051 30101	100Ω 5% 0.062W	3445	4822 051 30334	330kΩ 5% 0.062W	3928	4822 051 30331	330Ω 5% 0.062W
3242	4822 051 30472	4.7Ω 5% 0.062W	3446	4822 051 30123	12kΩ 5% 0.1W	3929	4822 051 30331	330Ω 5% 0.062W
3243	4822 051 30101	100Ω 5% 0.062W	3447	4822 051 30102	1kΩ 5% 0.062W	3930	4822 051 30331	330Ω 5% 0.062W
3245	4822 051 30103	10kΩ 5% 0.062W	3449	4822 051 30472	4.7Ω 5% 0.062W	3952	4822 051 30101	100Ω 5% 0.062W
3246	4822 051 30101	100Ω 5% 0.062W	3450	4822 051 30472	4.7Ω 5% 0.062W	3953	4822 051 30101	100Ω 5% 0.062W
3247	4822 117 12925	47kΩ 1% 0.063W 0603	3457	4822 051 30222	2.2kΩ 5% 0.062W	3961	3198 021 31080	1Ω 5% 0603
3248	4822 051 30101	100Ω 5% 0.062W	3458	4822 051 30101	100Ω 5% 0.062W	3962	3198 021 31080	1Ω 5% 0603
3249	4822 051 30472	4.7Ω 5% 0.062W	3459	4822 051 30101	100Ω 5% 0.062W	3963	4822 051 30101	100Ω 5% 0.062W
3250	4822 051 30101	100Ω 5% 0.062W	3500	4822 117 12925	47kΩ 1% 0.063W 0603	3964	4822 117 12891	220kΩ 1%
3251	4822 051 30472	4.7Ω 5% 0.062W	3501	4822 051 30223	22kΩ 5% 0.062W	3965	4822 051 30101	100Ω 5% 0.062W
3252	4822 051 30472	4.7Ω 5% 0.062W	3503	5322 117 13031	5.6kΩ 1% 0.063W 0603	3966	4822 117 12891	220kΩ 1%
3253	3198 021 32290	22Ω 5% 0603	3504	4822 051 30472	4.7Ω 5% 0.062W	3967	4822 117 13632	100kΩ 1% 0603 0.62W
3254	4822 051 30339	33Ω 5% 0.062W	3505	2322 704 63302	3.3kΩ 1% 0603	3968	4822 117 13632	100kΩ 1% 0603 0.62W
3255	4822 051 30339	33Ω 5% 0.062W	3506	4822 051 30103	10kΩ 5% 0.062W	3969	4822 117 13632	100kΩ 1% 0603 0.62W
3256	4822 051 30101	100Ω 5% 0.062W	3561	3198 021 38220	8.2kΩ 5% 0.062W 0603	3970	4822 117 13632	100kΩ 1% 0603 0.62W
3258	3198 031 13390	4 x 33Ω 5% 1206	3581	4822 051 30101	100Ω 5% 0.062W	3971	4822 051 30472	4.7Ω 5% 0.062W
3259	3198 031 13390	4 x 33Ω 5% 1206	3582	4822 051 30101	100Ω 5% 0.062W	4005	4822 051 30008	Jumper 0603
3260	3198 031 13390	4 x 33Ω 5% 1206	3583	4822 051 30472	4.7Ω 5% 0.062W	4006	4822 051 30008	Jumper 0603
3261	3198 031 13390	4 x 33Ω 5% 1206	3584	4822 051 30759	75Ω 5% 0.062W	4062	4822 051 30008	Jumper 0603
3262	3198 031 13390	4 x 33Ω 5% 1206	3606	4822 051 30222	2.2kΩ 5% 0.062W	4113	4822 051 30008	Jumper 0603
3263	3198 031 13390	4 x 33Ω 5% 1206	3607	4822 051 30222	2.2kΩ 5% 0.062W	4119	4822 051 30008	Jumper 0603
3264	3198 031 13390	4 x 33Ω 5% 1206	3608	4822 051 30272	2.7kΩ 5% 0.062W	4154	4822 051 30008	Jumper 0603
3265	4822 051 30103	10kΩ 5% 0.062W	3609	4822 051 30339	33Ω 5% 0.062W	4156	4822 051 30008	Jumper 0603
3266	4822 051 30103	10kΩ 5% 0.062W	3612	4822 051 30101	100Ω 5% 0.062W	4166	4822 051 30008	Jumper 0603
3267	4822 051 30103	10kΩ 5% 0.062W	3613	4822 051 30222	2.2kΩ 5% 0.062W	4168	4822 051 30008	Jumper 0603
3268	4822 051 30103	10kΩ 5% 0.062W	3614	4822 051 30101	100Ω 5% 0.062W	4183	4822 051 30008	Jumper 0603
3269	4822 051 30103	10kΩ 5% 0.062W	3617	3198 031 13390	4 x 33Ω 5% 1206	4190	4822 051 30008	Jumper 0603
3271	4822 051 30101	100Ω 5% 0.062W	3618	3198 031 13390	4 x 33Ω 5% 1206	4203	4822 051 30008	Jumper 0603
3277	4822 051 30103	10kΩ 5% 0.062W	3619	3198 031 13390	4 x 33Ω 5% 1206	4204	4822 051 30008	Jumper 0603
3287	4822 117 13632	100kΩ 1% 0603 0.62W	3620	3198 031 13390	4 x 33Ω 5% 1206	4205	4822 051 30008	Jumper 0603
3288	4822 051 30103	10kΩ 5% 0.062W	3621	3198 031 13390	4 x 33Ω 5% 1206	4301	4822 051 30008	Jumper 0603
3290	4822 051 30102	1kΩ 5% 0.062W	3622	3198 031 13390	4 x 33Ω 5% 1206	4302	4822 051 30008	Jumper 0603
3292	4822 051 30101	100Ω 5% 0.062W	3623	4822 051 30101	100Ω 5% 0.062W	4402	4822 051 30008	Jumper 0603
3293	4822 051 30339	33Ω 5% 0.062W	3624	4822 051 30101	100Ω 5% 0.062W	4407	4822 051 30008	Jumper 0603
3301	4822 051 30472	4.7Ω 5% 0.062W	3625	4822 051 30101	100Ω 5% 0.062W	4411	4822 051 30008	Jumper 0603
3302	4822 051 30222	2.2kΩ 5% 0.062W	3626	4822 051 30339	33Ω 5% 0.062W	4412	4822 051 30008	Jumper 0603
3303	4822 051 30103	10kΩ 5% 0.062W	3627	4822 051 30339	33Ω 5% 0.062W	4416	4822 051 30008	Jumper 0603
3304	4822 051 30101	100Ω 5% 0.062W	3701	4822 051 30102	1kΩ 5% 0.062W	4419	4822 051 30008	Jumper 0603
3305	4822 051 30103	10kΩ 5% 0.062W	3702	4822 051 30102	1kΩ 5% 0.062W	4421	4822 051 30008	Jumper 0603
3306	4822 051 30103	10kΩ 5% 0.062W	3703	2322 704 65109	51Ω 1% 0603	4501	4822 051 30008	Jumper 0603
3307	4822 051 30105	1MΩ 5% 0.062W	3704	2322 704 65109	51Ω 1% 0603	4502	4822 051 30008	Jumper 0603
3308	4822 051 30339	33Ω 5% 0.062W	3705	3198 021 31080	1Ω 5% 0.0603	4505	4822 051 30008	Jumper 0603
3309	4822 051 30472	4.7Ω 5% 0.062W	3706	4822 117 12971	150Ω 5% 0.0603 0.62W	4506	4822 051 30008	Jumper 0603
3310	4822 051 30472	4.7Ω 5% 0.062W	3707	3198 031 13390	4 x 33Ω 5% 1206	4586	4822 051 30008	Jumper 0603
3311	4822 051 30472	4.7Ω 5% 0.062W	3708	3198 031 13390	4 x 33Ω 5% 1206	4589	4822 051 30008	Jumper 0603
3312	4822 051 30339	33Ω 5% 0.062W	3709	3198 031 13390	4 x 33Ω 5% 1206	4590	4822 051 30008	Jumper 0603
3313	4822 051 30339	33Ω 5% 0.062W	3710	3198 031 13390	4 x 33Ω 5% 1206	4604	4822 051 30008	Jumper 0603
3314	4822 051 30339	33Ω 5% 0.062W	3711	3198 031 13390	4 x 33Ω 5% 1206	4605	4822 051 30008	Jumper 0603
3315	4822 051 30339	33Ω 5% 0.062W	3712	3198 031 13390	4 x 33Ω 5% 1206	4606	4822 051 30008	Jumper 0603
3316	3198 031 13390	4 x 33Ω 5% 1206	3713	3198 031 13390	4 x 33Ω 5% 1206	4732	4822 051 30008	Jumper 0603
3323	4822 051 30102	1kΩ 5% 0.062W	3714	3198 031 13390	4 x 33Ω 5% 1206	4733	4822 051 30008	Jumper 0603
3324	4822 051 30223	22kΩ 5% 0.062W	3715	4822 051 30339	33Ω 5% 0.062W	4734	4822 051 30008	Jumper 0603
3325	4822 051 30472	4.7Ω 5% 0.062W	3716	4822 051 30339	33Ω 5% 0.062W	4735	4822 051 30008	Jumper 0603
3326	4822 051 30472	4.7Ω 5% 0.062W	3717	4822 051 30339	33Ω 5% 0.062W	4736	4822 051 30008	Jumper 0603
3327	4822 051 30472	4.7Ω 5% 0.062W	3721	4822 051 30759	75Ω 5% 0.062W	4737	4822 051 30008	Jumper 0603
3328	4822 051 30472	4.7Ω 5% 0.062W	3723	4822 051 30759	75Ω 5% 0.062W	4738	4822 051 30008	Jumper 0603
3329	4822 051 30472	4.7Ω 5% 0.062W	3725	4822 117 11817	1.2kΩ 1% 0.0625W	4739	4822 051 30008	Jumper 0603
3331	4822 051 30472	4.7Ω 5% 0.062W	3726	4822 051 30152	1.5Ω 5% 0.062W	4740	4822 051 30008	Jumper 0603
3333	4822 051 30101							

5307	4822 157 11716	Bead 30Ω at 100MHz	7304	9965 000 04199	BSN20	1354	2422 500 00004	Socket CRT 10p 32"	
5308	4822 157 11716	Bead 30Ω at 100MHz	7305	9965 000 04199	BSN20	1354	2422 500 80087	Socket CRT 9p 29"	
5501	2422 535 94995	10µH 20%	7306	For SW see item 0802			1361	2422 025 16382	Connector 3p m
5502	2422 536 00339	33µH 20%	7307	9322 219 66671	SI9011CLU	1382	4822 267 10735	Connector 3p	
5503	2422 535 94995	10µH 20%	7401	9352 681 65518	TDA9330N3	- -			
5504	2422 549 43062	Bead 600Ω at 100MHz	7402	4822 130 60373	BC856B	2313	4822 124 12373	47µF 20% 250V	
5560	2422 549 43062	Bead 600Ω at 100MHz	7403	4822 130 60373	BC856B	2319	4822 122 30043	10nF 80% 63V	
5561	2422 549 43062	Bead 600Ω at 100MHz	7404	9965 000 04199	BSN20	2324	4822 121 70581	1.5nF 5% 2kV	
5562	2422 549 43062	Bead 600Ω at 100MHz	7405	9965 000 04199	BSN20	2332	4822 126 13193	4.7nF 10% 63V	
5563	2422 549 43062	Bead 600Ω at 100MHz	7406	4822 130 10255	MUN2213	2333	3198 016 36810	680pF 25V 0603	
5583	2422 549 43062	Bead 600Ω at 100MHz	7500	9322 179 61668	LF80CPT	2336	3198 017 33330	33nF 20% 16V 0603	
5584	2422 549 43062	Bead 600Ω at 100MHz	7502	9322 219 75685	FET SI2333DS-E3	2338	2022 318 00109	100nF 250V	
5585	2422 549 43062	Bead 600Ω at 100MHz	7504	4822 130 11155	PDT114ET	2339	2022 318 00109	100nF 250V	
5587	2422 549 43062	Bead 600Ω at 100MHz	7505	9322 202 34668	L5973D	2340	2022 318 00109	100nF 250V	
5588	2422 549 43062	Bead 600Ω at 100MHz	7506	4822 130 11155	PDT114ET	2343	3198 016 36810	680pF 25V 0603	
5591	2422 549 43062	Bead 600Ω at 100MHz	7560	3198 010 42310	BC847BW	2344	4822 126 13193	4.7nF 10% 63V	
5592	2422 549 43062	Bead 600Ω at 100MHz	7601	9322 225 69671	AD9985AKSTZ-110	2346	3198 017 33330	33nF 20% 16V 0603	
5593	2422 549 43062	Bead 600Ω at 100MHz	7635	4822 209 17398	LD1117DT33	2347	4822 124 80791	470µF 20% 16V	
5594	2422 549 43062	Bead 600Ω at 100MHz	7701	9322 235 50671	K4D263238I-UC50	2348	3198 024 44730	47nF 50V 0603	
5600	2422 549 45333	Bead 120Ω at 100MHz	7702	4822 209 17398	LD1117DT33	2352	4822 126 13193	4.7nF 10% 63V	
5601	2422 549 45333	Bead 120Ω at 100MHz	7703	9322 217 23668	LD1117DT25C	2353	3198 016 36810	680pF 25V 0603	
5602	2422 549 45333	Bead 120Ω at 100MHz	7704	9322 189 19668	LD1086D2T18	2356	3198 017 33330	33nF 20% 16V 0603	
5701	2422 549 44197	Bead 220Ω at 100MHz	7721	9322 221 70671	SVPEX42	2361	2238 586 59812	100nF 20% 50V 0603	
5702	2422 549 44197	Bead 220Ω at 100MHz	7907	9322 196 03702	MSP3411G-Q1-B8V3	2363	4822 124 40764	22µF 100V	
5703	2422 549 44197	Bead 220Ω at 100MHz	7908	9340 425 20115	BC847BS	2364	2020 557 90732	4.7nF 10% 250V	
5704	2422 549 44197	Bead 220Ω at 100MHz	7909	9340 425 20115	BC847BS	2365	4822 126 13193	4.7nF 10% 63V	
5705	2422 549 44197	Bead 220Ω at 100MHz	7910	9340 425 20115	BC847BS	2367	2238 586 59812	100nF 20% 50V 0603	
5721	2422 549 44197	Bead 220Ω at 100MHz	7961	9352 703 94118	UDA1334BT/N2	2368	4822 124 40764	22µF 100V	
5722	2422 549 44197	Bead 220Ω at 100MHz	7962	5322 209 14481	HEF4053BT	2369	4822 126 14241	330pF 50V 0603	
5723	2422 549 44197	Bead 220Ω at 100MHz	7963	4822 130 11155	PDT114ET	2370	2238 586 59812	100nF 20% 50V 0603	
5724	2422 549 44197	Bead 220Ω at 100MHz	- -			2371	4822 126 11663	12pF 5% 50V 0603	
5725	2422 549 44197	Bead 220Ω at 100MHz	- -			2372	4822 126 11663	12pF 5% 50V 0603	
5726	2422 549 44197	Bead 220Ω at 100MHz	- -			2373	4822 126 11663	12pF 5% 50V 0603	
5727	2422 549 44197	Bead 220Ω at 100MHz	- -			2375	3198 017 34730	47nF 16V 0603	
5728	2422 549 44197	Bead 220Ω at 100MHz	- -			2381	4822 124 40433	47µF 20% 25V	
5729	2422 549 44197	Bead 220Ω at 100MHz	- -			2382	4822 126 13193	4.7nF 10% 63V	
5730	2422 549 44197	Bead 220Ω at 100MHz	- -			2383	2238 930 11541	220pF 5% 200V	
5731	2422 549 44197	Bead 220Ω at 100MHz	- -			2384	2238 586 59812	100nF 20% 50V 0603	
5941	2422 549 44197	Bead 220Ω at 100MHz	- -			2385	2238 586 59812	100nF 20% 50V 0603	
5942	2422 549 44197	Bead 220Ω at 100MHz	- -			2386	3198 017 34730	47nF 16V 0603	
5943	3198 018 51080	1µH 10% 0603	- -			2387	4822 126 14507	18pF 5% 50V 0603	
5944	3198 018 51080	1µH 10% 0603	- -			2388	4822 126 13193	4.7nF 10% 63V	
5961	4822 157 11778	5.6µH 0805 10%	- -			2389	2238 586 59812	100nF 20% 50V 0603	
5972	4822 157 11778	5.6µH 0805 10%	- -			2390	4822 124 11947	10µF 20% 16V	

Side I/O and Control Board [D]**Various**

1010	4822 267 10748	Connector 3p	1278	4822 267 10567	Connector 4p	-WW-		
1011	4822 276 13775	Switch 1p 0.1A 12V	8010	3139 131 06551	Cable 3p/560/3p Bk	3305	4822 052 10108	1Ω 5% 0.33W
1012	4822 276 13775	Switch 1p 0.1A 12V	8252	3139 131 07601	Cable 7p/560/7p Bk	3306	4822 052 10568	5.6Ω 5% 0.33W
1013	4822 276 13775	Switch 1p 0.1A 12V	8254	3139 131 06691	Cable 5p/560/5p Bk	3306	4822 052 11688	6Ω 5% 0.5W
1014	4822 276 13775	Switch 1p 0.1A 12V	2171	4822 126 14241	330pF 0603 50V	3307	4822 052 10568	5.6Ω 5% 0.33W
1016	3139 267 20481	Control Assy [E]	2172	4822 126 14241	330pF 0603 50V	3307	4822 052 11338	3.3Ω 5% 0.5W
1232	2422 026 05701	Socket Phone 1p f	2173	4822 126 14241	330pF 0603 50V	3317	4822 050 11002	1kΩ 1% 0.4W
1250	2422 026 05742	Socket Cinch 3p	2174	4822 126 14241	330pF 0603 50V	3318	4822 052 10109	10Ω 5% 0.33W
1251	2422 026 05494	Connector 7p f	2175	4822 124 22652	2.2µF 20% 50V	3319	4822 051 30154	150kΩ 5% 0.062W
1252	2422 025 12491	Connector 7p m	2176	4822 126 13881	470pF 5% 50V	3320	4822 051 30223	22kΩ 5% 0.062W
1254	2422 025 06353	Connector 5p m	2178	4822 126 13881	470pF 5% 50V	3321	4822 051 30273	27kΩ 5% 0.062W
1278	4822 267 10567	Connector 4p	2180	4822 124 22652	2.2µF 20% 50V	3322	4822 051 30154	150kΩ 5% 0.062W

-WW-

3011	4822 051 30151	150Ω 5% 0.062W	3012	4822 051 30391	390Ω 5% 0.062W	3336	3198 013 04710	470Ω 20% 0.5W
3013	3198 021 31820	1.8kΩ 5% 0.062W 0603	3015	4822 117 12968	820Ω 5% 0.62W	3337	2322 242 13104	100kΩ 20W
3150	4822 117 12925	47kΩ 1% 0.063W 0603	3151	4822 051 30151	150Ω 5% 0.062W	3338	4822 051 30222	2.2kΩ 5% 0.062W
3152	4822 117 12925	47kΩ 1% 0.063W 0603	3153	4822 051 30151	150Ω 5% 0.062W	3339	4822 051 30272	2.7kΩ 5% 0.062W
3155	4822 117 12968	820Ω 5% 0.62W	3156	4822 117 12968	820Ω 5% 0.62W	3340	4822 051 30102	1kΩ 5% 0.062W
3156	4822 117 12968	820Ω 5% 0.62W	3157	4822 117 12968	820Ω 5% 0.62W	3341	2322 242 13104	100kΩ 20W
3158	4822 051 30759	75Ω 5% 0.062W	3159	4822 051 30008	Jumper 0603	3342	4822 051 30272	2.7kΩ 5% 0.062W
4185	4822 051 30008	Jumper 0603	4186	4822 051 30008	Jumper 0603	3343	4822 051 30222	2.2kΩ 5% 0.062W

CRT Panel [F]**Various**

1317	4822 267 10637	Connector 5p	1325	4822 051 30222	220Ω 5% 0.062W	3351	2306 207 03151	150Ω 5% 0.5W
1335	3104 301 08281	Connector 1p	1336	4822 051 30272	2.7kΩ 5% 0.062W	3352	4822 051 30222	2.2kΩ 5% 0.062W
1351	4822 265 41113	Connector 7p	1335	4822 051 30102	1kΩ 5% 0.062W	3353	4822 051 30272	2.7kΩ 5% 0.062W
1351	4822 265 41113	Connector 7p	1336	4822 051 30102	1kΩ 5% 0.062W	3354	4822 051 30272	2.7kΩ 5% 0.062W
1351	4822 265 41113	Connector 7p	1335	4822 051 30102	1kΩ 5% 0.062W	3355	4822 051 30102	1kΩ 5% 0.062W
1351	4822 265 41113	Connector 7p	1336	4822 051 30102	1kΩ 5% 0.062W	3356	4822 051 30102	1kΩ 5% 0.062W
1351	4822 265 41113	Connector 7p	1335	4822 051 30102	1kΩ 5% 0.062W			

3366	4822 051 30683	68kΩ 5% 0.062W
3367	4822 116 52297	68kΩ 5% 0.5W
3368	4822 051 30561	560Ω 5% 0.062W
3370	4822 051 20108	1Ω 5% 0.1W
3371	2312 915 11002	1kΩ 1% 0.5W
3372	2312 915 11002	1kΩ 1% 0.5W
3373	2322 257 41152	1.5kΩ 5W
3375	4822 051 30681	680Ω 5% 0.062W
3377	4822 051 30272	2.7kΩ 5% 0.062W
3378	4822 051 30221	220Ω 5% 0.062W
3380	4822 051 30222	2.2kΩ 5% 0.062W
3381	4822 051 30222	2.2kΩ 5% 0.062W
3385	4822 051 30681	680Ω 5% 0.062W
3389	2120 108 94132	1Ω 1206
3392	4822 051 30271	270Ω 5% 0.062W
3393	4822 051 30109	10Ω 5% 0.062W
3394	4822 051 30472	4.7Ω 5% 0.062W
3395	4822 116 52219	330Ω 5% 0.5W
3396	3198 021 31820	1.8kΩ 5% 0.062W 0603
3397	2122 552 00004	1mA 18V 0603
3998	4822 117 11817	1.2kΩ 1% 0.0625W
4303	4822 051 30008	Jumper 0603
4304	4822 051 30008	Jumper 0603
4305	4822 051 30008	Jumper 0603
4306	4822 051 30008	Jumper 0603
4307	4822 051 30008	Jumper 0603
4308	4822 051 30008	Jumper 0603
4309	4822 051 30008	Jumper 0603
4310	4822 051 20008	Jumper 0805
4321	4822 051 30008	Jumper 0603
4322	4822 051 20008	Jumper 0805
4330	4822 051 30008	Jumper 0603

3691	4822 116 52283	4.7kΩ 5% 0.5W
3693	4822 116 83872	220Ω 5% 0.5W
3694	4822 116 52283	4.7kΩ 5% 0.5W
3696	4822 051 30154	150kΩ 5% 0.062W
4601	4822 051 30008	Jumper 0603

6691	9322 230 38682	LTL-102CBK5HCR
6692	9322 206 78667	TSOP34836UH1B
6693	9322 197 36682	LTR-301

IAP Board

Various

J1	Connector 3p m
J2	Connector 25p m
U1	Socket, DIP14

C1	10μF 16V
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R1	2kΩ 5% 0805
R2	2kΩ 5% 0805

D1	1N4148
D2	1N4148
D3	1N4148
D4	1N4148
D5	1N4148
D6	1N4148
D7	1N4148
D8	1N4148

U1	SN74LS05, DIP14
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|      |                |                     |
|------|----------------|---------------------|
| 5303 | 4822 157 11867 | 5.6μH 5%            |
| 5304 | 4822 526 10704 | Bead 50 Ω at 100MHz |
| 5308 | 4822 157 11867 | 5.6μH 5%            |
| 5339 | 4822 526 10704 | Bead 50 Ω at 100MHz |
| 5361 | 4822 157 11411 | Bead 80Ω at 100MHz  |

→+

|      |                |         |
|------|----------------|---------|
| 6307 | 4822 130 11416 | PDZ6.8B |
| 6325 | 4822 130 10838 | UDZ3.3B |
| 6331 | 9322 197 45703 | BAV21WS |
| 6332 | 9322 197 45703 | BAV21WS |
| 6333 | 9322 197 45703 | BAV21WS |
| 6334 | 4822 130 10838 | UDZ3.3B |
| 6361 | 4822 130 11397 | BAS316  |
| 6362 | 4822 130 11397 | BAS316  |



|      |                |             |
|------|----------------|-------------|
| 7330 | 4822 209 33365 | TDA6111Q/N4 |
| 7331 | 4822 130 60373 | BC856B      |
| 7332 | 4822 130 41246 | BC327-25    |
| 7333 | 4822 130 40981 | BC337-25    |
| 7340 | 4822 209 33365 | TDA6111Q/N4 |
| 7350 | 4822 209 33365 | TDA6111Q/N4 |
| 7361 | 5322 130 60159 | BC846B      |
| 7362 | 4822 130 60373 | BC856B      |
| 7363 | 9322 195 05687 | KTB631KY    |
| 7364 | 9322 195 14687 | KTD600KY    |
| 7365 | 4822 130 60887 | BF840       |
| 7366 | 9352 628 51112 | TDA8941p/N1 |

### Front Interface Panel [J]

#### Various

|      |                |                         |
|------|----------------|-------------------------|
| 1060 | 3139 267 21641 | Front Interf. Panel [J] |
| 1211 | 2422 025 16268 | Connector 2p m          |
| 1231 | 2422 128 03111 | Switch                  |
| 1505 | 2422 025 16268 | Connector 2p m          |
| 1693 | 2422 025 10738 | Connector 6p m          |
| 8505 | 3104 311 03011 | Cable 2p/340/2p Bk      |
| 8693 | 3139 131 07121 | Cable 6p/680/6p Bk      |

→|←

|      |                |              |
|------|----------------|--------------|
| 2691 | 4822 124 12379 | 220μF 25V    |
| 2692 | 3198 017 41050 | 1μF 10V 0603 |
| 2698 | 5322 121 42386 | 100nF 5% 63V |

→|←

|      |                |               |
|------|----------------|---------------|
| 3500 | 4822 053 21335 | 3.3MΩ 5% 0.5W |
| 3501 | 4822 053 21335 | 3.3MΩ 5% 0.5W |

## **11. Revision List**

**Manual xxxx xxx xxxx.0**

- First release.

